



*Personal Computer*

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# **IBM Enhanced Graphics Adapter**

IBM ENHANCED GRAPHICS ADAPTER



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# Description

The IBM Enhanced Graphics Adapter (EGA) is a graphics controller that supports both color and monochrome direct drive displays in a variety of modes. In addition to the direct drive port, a light pen interface is provided. Advanced features on the adapter include bit-mapped graphics in four planes and a RAM (Random Access Memory) loadable character generator. Design features in the hardware substantially reduce the software overhead for many graphics functions.

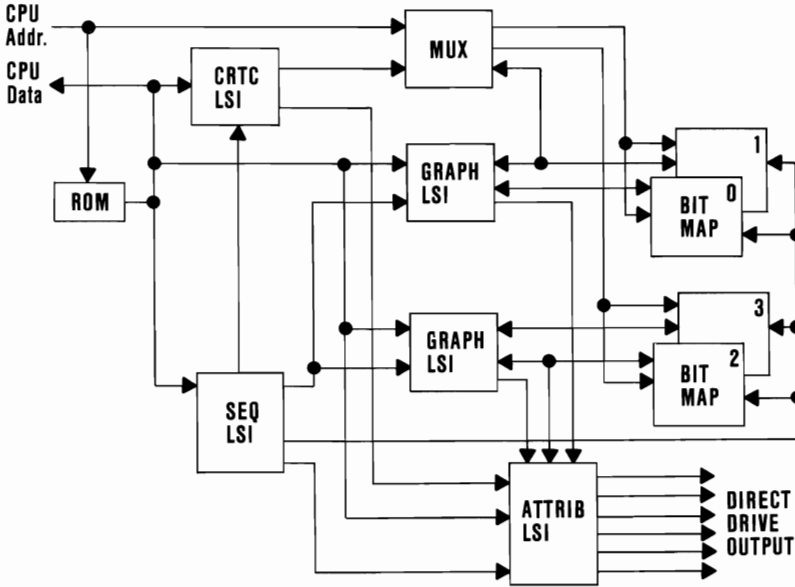
The Enhanced Graphics Adapter provides Basic Input Output System (BIOS) support for both alphanumeric (A/N) modes and all-points-addressable (APA) graphics modes, including all modes supported by the Monochrome Display Adapter and the Color/Graphics Monitor Adapter. Other modes provide APA 640x350 pel graphics support for the IBM Monochrome Display, full 16 color support in both 320x200 pel and 640x200 pel resolutions for the IBM Color Display, and both A/N and APA support with resolution of 640x350 for the IBM Enhanced Color Display. In alphanumeric modes, characters are formed from one of two ROM (Read Only Memory) character generators on the adapter. One character generator defines 7x9 characters in a 9x14 character box. For Enhanced Color Display support, the 9x14 character set is modified to provide an 8x14 character set. The second character generator defines 7x7 characters in an 8x8 character box. These generators contain dot patterns for 256 different characters. The character sets are identical to those provided by the IBM Monochrome Display Adapter and the IBM Color/Graphics Monitor Adapter.

The adapter contains 64K bytes of storage configured as four 16K byte bit planes. Memory expansion options are available to expand the adapter memory to 128K bytes or 256K bytes.

The adapter is packaged on a single 13-1/8 inch (333.50 mm) card. The direct drive port is a right-angle mounted connector at the rear of the adapter and extends through the rear panel of the system unit. Also on the card are five large scale integration (LSI) modules custom designed for this controller.

Located on the adapter is a feature connector that provides access to internal functions through a 32-pin berg connector. A separate 64-pin connector provides an interface for graphics memory expansion.

The following is a block diagram of the Enhanced Graphics Adapter:



**Enhanced Graphics Adapter Block Diagram**

# Major Components

## CRT Controller

The CRT (Cathode Ray Tube) Controller (CRTC) generates horizontal and vertical synchronous timings, addressing for the regenerative buffer, cursor and underline timings, and refresh addressing for the dynamic RAMs.

## Sequencer

The Sequencer generates basic memory timings for the dynamic RAMs and the character clock for controlling regenerative memory fetches. It allows the processor to access memory during active display intervals by inserting dedicated processor memory cycles periodically between the display memory cycles. Map mask registers are available to protect entire memory maps from being changed.

## Graphics Controller

The Graphics Controller directs the data from the memory to the attribute controller and the processor. In graphics modes, memory data is sent in serialized form to the attribute chip. In alpha modes the memory data is sent in parallel form, bypassing the graphics controller. The graphics controller formats the data for compatible modes and provides color comparators for use in color painting modes. Other hardware facilities allow the processor to write 32 bits in a single memory cycle, (8 bits per plane) for quick color presetting of the display areas, and additional logic allows the processor to write data to the display on non-byte boundaries.

## Attribute Controller

The Attribute Controller provides a color palette of 16 colors, each of which may be specified separately. Six color outputs are

available for driving a display. Blinking and underlining are controlled by this chip. This chip takes data from the display memory and formats it for display on the CRT screen.

## **Display Buffer**

The display buffer on the adapter consists of 64K bytes of dynamic read/write memory configured as four 16K byte video bit planes. Two options are available for expanding the graphics memory. The Graphics Memory Expansion Card plugs into the memory expansion connector on the adapter, and adds one bank of 16K to each of the four bit planes, increasing the graphics memory to 128K bytes. The expansion card also provides DIP sockets for further memory expansion. Populating the DIP sockets with the Graphics Memory Module Kit adds two additional 16K banks to each bit plane, bringing the graphics memory to its maximum of 256K bytes.

The address of the display buffer can be changed to remain compatible with other video cards and application software. Four locations are provided. The buffer can be configured at segment address hex A0000 for a length of 128K bytes, at hex A0000 for a length of 64K bytes, at hex B0000 for a length of 32K bytes, or at hex B8000 for a length of 32K bytes.

## **BIOS**

A read-only memory (ROM) Basic Input Output System (BIOS) module on the adapter is linked to the system BIOS. This ROM BIOS contains character generators and control code and is mapped into the processor address at hex C0000 for a length of 16K bytes.

## **Support Logic**

The logic on the card surrounding the LSI modules supports the modules and creates latch buses for the CRT controller, the

processor, and character generator. Two clock sources (14 MHz and 16 MHz) provide the dot rate. The clock is multiplexed under processor I/O control. Four I/O registers also resident on the card are not part of the LSI devices.

## Modes of Operation

### IBM Color Display

The following table describes the modes supported by BIOS on the IBM Color Display:

MODE #	TYPE	COLORS	ALPHA FORMAT	BUFFER START	BOX SIZE	MAX. PAGES	RESOLUTION
0	A/N	16	40x25	B8000	8x8	8	320x200
1	A/N	16	40x25	B8000	8x8	8	320x200
2	A/N	16	80x25	B8000	8x8	8	640x200
3	A/N	16	80x25	B8000	8x8	8	640x200
4	APA	4	40x25	B8000	8x8	1	320x200
5	APA	4	40x25	B8000	8x8	1	320x200
6	APA	2	80x25	B8000	8x8	1	640x200
D	APA	16	40x25	A0000	8x8	2/4/8	320x200
E	APA	16	80x25	A0000	8x8	1/2/4	640x200

Modes 0 through 6 emulate the support provided by the IBM Color/Graphics monitor Adapter.

Modes 0,2 and 5 are identical to modes 1,3 and 4 respectively at the adapter's direct drive interface.

The Maximum Pages fields for modes D and E indicate the number of pages supported when 64K, 128K or 256K bytes of graphics memory is installed, respectively.

## IBM Monochrome Display

The following table describes the modes supported by BIOS on the IBM Monochrome Display.

MODE #	TYPE	COLORS	ALPHA FORMAT	BUFFER START	BOX SIZE	MAX. PAGES	RESOLUTION
7	A/N	4	80x25	B0000	9x14	8	720x350
F	APA	4	80x25	A0000	8x14	1/2	640x350

Mode 7 emulates the support provided by the IBM Monochrome Display Adapter.

## IBM Enhanced Color Display

The Enhanced Graphics Adapter supports attachment of the IBM Enhanced Color Display. The IBM Enhanced Color Display is capable of running at the standard television frequency of 15.75 KHz as well as running 21.85 KHz. The table below summarizes the characteristics of the IBM Enhanced Color Display:

Parameter	TV Frequency	High Resolution
Horiz Scan Rate	15.75 KHz.	21.85 KHz.
Vertical Scan Rate	60 Hz.	60 Hz.
Video Bandwidth	14.318 MHz.	16.257 MHz.
Displayable Colors	16 Maximum	16 or 64
Character Size	7 by 7 Pels	7 by 9 Pels
Character Box Size	8 by 8 Pels	8 by 14 Pels
Maximum Resolution	640x200 Pels	640 by 350 Pels
Alphanumeric Modes	0,1,2,3	0,1,2,3
Graphics Modes	4,5,6,D,E	10

In the television frequency mode, the IBM Enhanced Color Display displays information identical in color and resolution to the IBM Color Display.

In the high resolution mode, the adapter provides enhanced alphanumeric character support. This enhanced alphanumeric support consists of transforming the 8 by 8 character box into an 8 by 14 character box, and providing 16 colors out of a palette of

64 possible display colors. Display colors are changed by altering the programming of the color palette registers in the Attribute Controller. In alphanumeric modes, any 16 of 64 colors are displayable. the screen resolution is 320x350 for modes 0 and 1, and 640x350 for modes 2 and 3.

The resolution displayed on the IBM Enhanced Color Display is selected by the switch settings on the Enhanced Graphics Adapter.

The Enhanced Color Display is compatible with all modes listed for the IBM Color Display. the following table describes additional modes supported by BIOS for the IBM Enhanced Color Display:

MODE #	TYPE	COLORS	ALPHA FORMAT	BUFFER START	BOX SIZE	MAX. PAGES	RESOLUTION
0*	A/N	16/64	40x25	B8000	8x14	8	320x350
1*	A/N	16/64	40x25	B8000	8x14	8	320x350
2*	A/N	16/64	80x25	B8000	8x14	8	640x350
3*	A/N	16/64	80x25	B8000	8x14	8	640x350
10*	APA	4/16 16/64	80x25	A0000	8x14	1/2	640x350

\* Note that modes 0, 1, 2, and 3, are also listed for IBM Color Display support. BIOS provides enhanced support for these modes when an Enhanced Color Display is attached.

The values in the "COLORS" field indicate 16 colors of a 64 color palette or 4 colors of a sixteen color palette.

In mode 10, The dual values for the "COLORS" field and the "MAX. PAGES" field indicate the support provided when 64K or when greater than 64K of graphics memory is installed, respectively.

# **Basic Operations**

## **Alphanumeric Modes**

The data format for alphanumeric modes on the Enhanced Graphics Adapter is the same as the data format on the IBM Color/Graphics Monitor Adapter and the IBM Monochrome Display Adapter. As an added function, bit three of the attribute byte may be redefined by the Character Map Select register to act as a switch between character sets. This gives the programmer access to 512 characters at one time. This function is valid only when memory has been expanded to 128K bytes or more.

When an alphanumeric mode is selected, the BIOS transfers character patterns from the ROM to bit plane 2. The processor stores the character data in bit plane 0, and the attribute data in bit plane 1. The programmer can view bit planes 0 and 1 as a single buffer in alphanumeric modes. The CRTC generates sequential addresses, and fetches one character code byte and one attribute byte at a time. The character code and row scan count address bit plane 2, which contains the character generators. The appropriate dot patterns are then sent to the palette in the attribute chip, where color is assigned according to the attribute data.

## **Graphics Modes**

### **320x200 Two and Four Color Graphics (Modes 4 and 5)**

Addressing, mapping and data format are the same as the 320x200 pel mode of the Color/Graphics Monitor Adapter. The display buffer is configured at hex B8000. Bit image data is stored in bit planes 0 and 1.

### **640x200 Two Color Graphics (Mode 6)**

Addressing, mapping and data format are the same as the 640x200 pel black and white mode of the Color/Graphics



Monitor Adapter. The display buffer is configured at hex B8000. Bit image data is stored in bit plane 0.

### 640x350 Monochrome Graphics (Mode F )

This mode supports graphics on the IBM Monochrome Display with the following attributes: black, video, blinking video, and intensified video. Resolution of 640x350 requires 56K bytes to support four attributes. By chaining maps 0 and 1, then maps 2 and 3 together, two 32K bit planes can be formed. This chaining is done only when necessary (less than 128K of graphics memory). The first map is the video bit plane, and the second map is the intensity bit plane. Both planes reside at hex address A0000.

Two bits, one from each bit plane, define one picture element (pel) on the screen. The bit definitions for the pels are given in the following table. The video bit plane is denoted by C0 and the Intensity Bit Plane is denoted by C2.

C2	C0	Pixel Color	Valid Attributes
0	0	Black	0
0	1	Video	3
1	0	Blinking Video	C
1	1	Intensified Video	F

The byte organization in memory is sequential. The first eight pels on the screen are defined by the contents of memory in location A000:0H, the second eight pels by location A000:1H, and so on. The first pel within any one byte is defined by bit 7 in the byte. The last pel within the byte is defined by bit 0 in the byte.

Monochrome graphics works in odd/even mode, which means that even CPU addresses go into even bit planes and odd CPU addresses go into odd bit planes. Since both bit planes reside at address A0000, the user must select which plane or planes he desires to update. This is accomplished by the map mask register of the sequencer. (See the table above for valid attributes).

## 16/64 Color Graphics Modes (Mode 10)

These modes support graphics in 16 colors on either a medium or high resolution monitor. The memory in these modes consists of using all four bit planes. Each bit plane represents a color as shown below. The bit planes are denoted as C0,C1,C2 and C3 respectively.

C0 = Blue Pels  
C1 = Green Pels  
C2 = Red Pels  
C3 = Intensified Pels

Four bits (one from each plane) define one pel on the screen. The color combinations are illustrated in the following table:

I	R	G	B	Color
0	0	0	0	Black
0	0	0	1	Blue
0	0	1	0	Green
0	0	1	1	Cyan
0	1	0	0	Red
0	1	0	1	Magenta
0	1	1	0	Brown
0	1	1	1	White
1	0	0	0	Dark Gray
1	0	0	1	Light Blue
1	0	1	0	Light Green
1	0	1	1	Light Cyan
1	1	0	0	Light Red
1	1	0	1	Light Magenta
1	1	1	0	Yellow
1	1	1	1	Intensified White

The display buffer resides at address A0000. The map mask register of the sequencer is used to select any or all of the bit planes to be updated when a memory write to the display buffer is executed by the CPU.

### Color Mapping

The Enhanced Graphics Adapter supports 640x350 Graphics for both the IBM Monochrome and the IBM Enhanced Color

Displays. Four color capability is supported on the EGA without the Graphics Memory Expansion Card (base 64 KB), and sixteen colors are supported when the Graphics Memory Expansion Card is installed on the adapter (128 KB or above). This section describes the differences in the colors displayed depending upon the graphics memory available. Note that colors 0H, 1H, 4H, and 7H map directly regardless of the graphics memory available.

<b>Character Attribute</b>	<b>Monochrome</b>	<b>Mode 10H 64KB</b>	<b>Mode 10H &gt;64KB</b>
00H*	Black	Black	Black
01H*	Video	Blue	Blue
02H	Black	Black	Green
03H	Video	Blue	Cyan
04H*	Blinking	Red	Red
05H	Intensified	White	Magenta
06H	Blinking	Red	Brown
07H*	Intensified	White	White
08H	Black	Black	Dark Gray
09H	Video	Blue	Light Blue
0AH	Black	Black	Light Green
0BH	Video	Blue	Light Cyan
0CH	Blinking	Red	Light Red
0DH	Intensified	White	Light Magenta
0EH	Blinking	Red	Yellow
0FH	Intensified	White	Intensified White

\* Graphics character attributes which map directly regardless of the graphics memory available.

# Registers

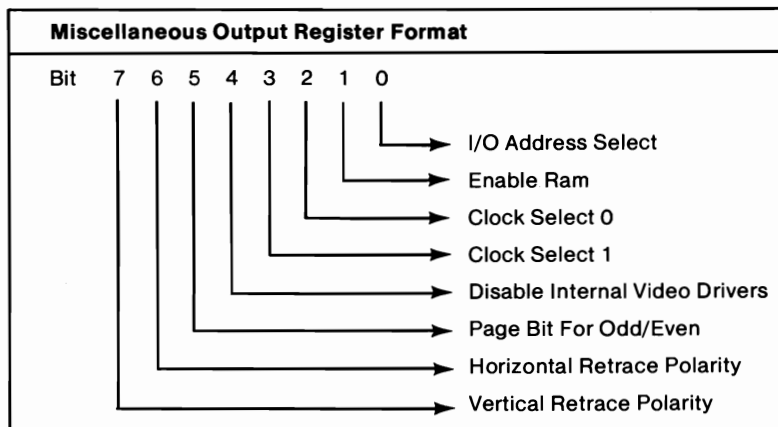
## External Registers

This section contains descriptions of the registers of the Enhanced Graphics Adapter that are not contained in an LSI device.

Name	Port	Index
Miscellaneous Output Register	3C2	-
Feature Control Register	3?A	-
Input Status Register 0	3C2	-
Input Status Register 1	3?2	-
? = B in Monochrome Modes		? = D in Color Modes

### Miscellaneous Output Register

This is a write-only register. The processor output port address is hex 3C2. A hardware reset causes all bits to reset to zero.



#### Bit 0

**3BX/3DX CRT C I/O Address**—This bit maps the CRT C I/O addresses for IBM Monochrome or Color/Graphics Monitor Adapter emulation. A logical 0 sets CRT C addresses to 3BX and Input Status Register 1's address to 3BA for Monochrome emulation. A logical 1 sets CRT C

addresses to 3DX and Input Status Register 1's address to 3DA for Color/Graphics Monitor Adapter emulation.

**Bit 1**

**Enable RAM**—A logical 0 disables RAM from the processor; a logical 1 enables RAM to respond at addresses designated by the Control Data Select value programmed into the Graphics Controllers.

**Bit 2–Bit 3**

**Clock Select**—These two bits select the clock source according to the following table:

**Bits**

**3 2**

- |            |   |
|------------|---|
| <b>0 0</b> | Selects 14 MHz clock from the processor I/O channel       |
| <b>0 1</b> | Selects 16 MHz clock on-board oscillator                  |
| <b>1 0</b> | Selects external clock source from the feature connector. |
| <b>1 1</b> | Not used  |

**Bit 4**

**Disable Internal Video Drivers**—A logical 0 activates internal video drivers; a logical 1 disables internal video drivers. When the internal video drivers are disabled, the source of the direct drive color output becomes the feature connector direct drive outputs.

**Bit 5**

**Page Bit For Odd/Even**—Selects between two 64K pages of memory when in the Odd/Even modes (0,1,2,3,7). A logical 0 selects the low page of memory; a logical 1 selects the high page of memory.

**Bit 6**

**Horizontal Retrace Polarity**—A logical 0 selects positive horizontal retrace; a logical 1 selects negative horizontal retrace.

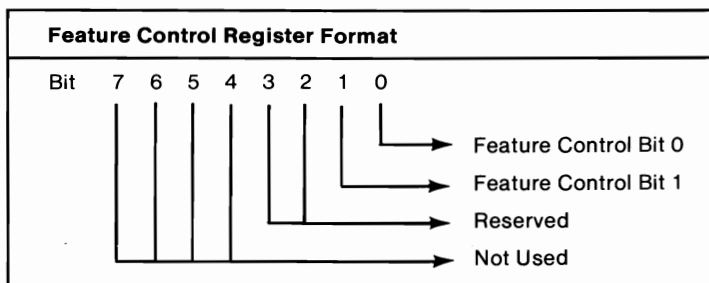
**Bit 7**

**Vertical Retrace Polarity**—A logical 0 selects positive vertical retrace; a logical 1 selects

negative vertical retrace. The IBM Monochrome display requires a negative vertical retrace polarity.

## Feature Control Register

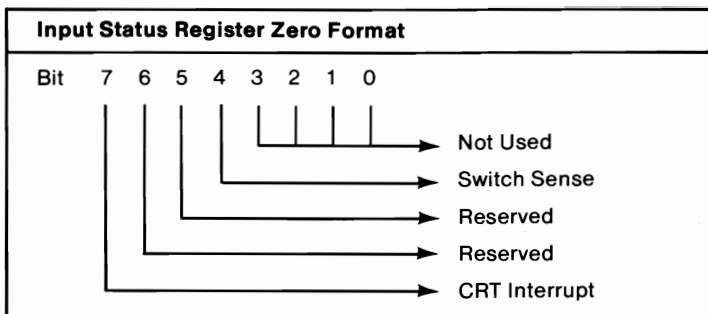
This is a write-only register. The processor output register is hex 3BA or 3DA.



**Bits 0 and 1** Feature Control Bits—These bits are used to convey information to the feature connector. The output of these bits goes to the FEAT 0 (pin 19) and FEAT 1 (pin 17) of the feature connector.

## Input Status Register Zero

This is a read-only register. The processor input port address is hex 3C2.



**Bit 4** Switch Sense—When set to 1, this bit allows the processor to read the four configuration switches on the board. The setting of the CLKSEL field determines which switch is being read. The switch configuration can be determined by reading byte 40:88H in RAM.

Bit 3: Switch 4 ; Logical 0 = switch closed

Bit 2: Switch 3 ; Logical 0 = switch closed

Bit 1: Switch 2 ; Logical 0 = switch closed

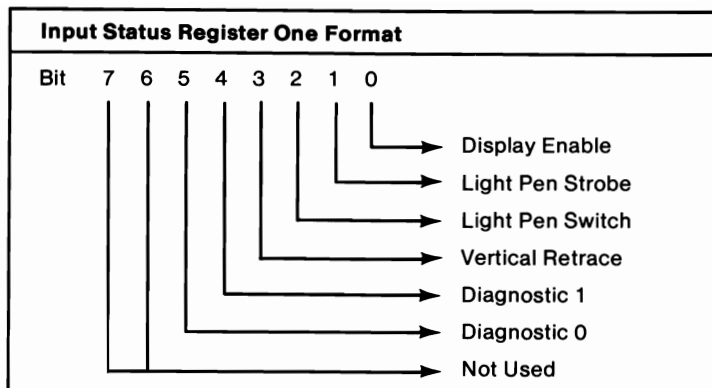
Bit 0: Switch 1 ; Logical 0 = switch closed

**Bits 5 and 6** Feature Code—These bits are input from the Feat (0) and Feat (1) pins on the feature connector.

**Bit 7** CRT Interrupt—A logical 1 indicates video is being displayed on the CRT screen; a logical 0 indicates that vertical retrace is occurring.

## Input Status Register One

This is a read-only register. The processor port address is hex 3BA or hex 3DA.



- Bit 0** Display Enable—Logical 0 indicates the CRT raster is in a horizontal or vertical retrace interval. This bit is the real time status of the display enable signal. Some programs use this status bit to restrict screen updates to inactive display intervals. The Enhanced Graphics Adapter does not require the CPU to update the screen buffer during inactive display intervals to avoid glitches in the display image.
- Bit 1** Light Pen Strobe—A logical 0 indicates that the light pen trigger has not been set; a logical 1 indicates that the light pen trigger has been set.
- Bit 2** Light Pen Switch—A logical 0 indicates that the light pen switch is closed; a logical 1 indicates that the light pen switch is open.
- Bit 3** Vertical Retrace—A logical 0 indicates that video information is being displayed on the CRT screen; a logical 1 indicates the CRT is in a vertical retrace interval. This bit can be programmed to interrupt the processor on interrupt level 2 at the start of the vertical retrace. This is done through bits 4 and 5 of the Vertical Retrace End Register of the CRTC.
- Bits 4 and 5** Diagnostic Usage—These bits are selectively connected to two of the six color outputs of the



Attribute Controller. The Color Plane Enable register controls the multiplexer for the video wiring. The following table illustrates the combinations available and the color output wiring.

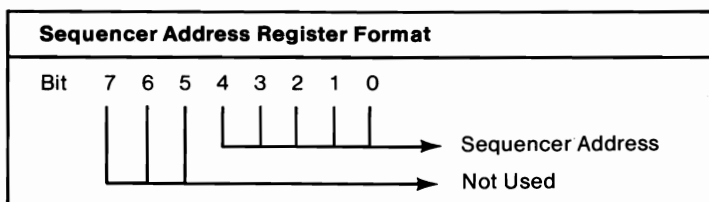
Color Plane Register		Input Status Register One	
Bit 5	Bit 4	Bit 5	Bit 4
0	0	Red	Blue
0	1	Secondary Blue	Green
1	0	Secondary Red	Secondary Green
1	1	Not Used	Not Used

## Sequencer Registers

Name	Port	Index
Address	3C4	-
Reset	3C5	00
Clocking Mode	3C5	01
Map Mask	3C5	02
Character Map Select	3C5	03
Memory Mode	3C5	04

### Sequencer Address Register

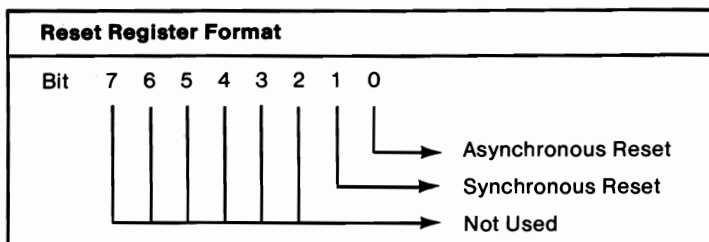
The Address Register is a pointer register located at address hex 3C4. This register is loaded with a binary value that points to the sequencer data register where data is to be written. This value is referred to as "Index" in the table above.



**Bit 0–Bit 3** Sequencer Address Bits—A binary value pointing to the register where data is to be written.

### Reset Register

This is a write-only register pointed to when the value in the address register is hex 00. The output port address for this register is hex 3C5.

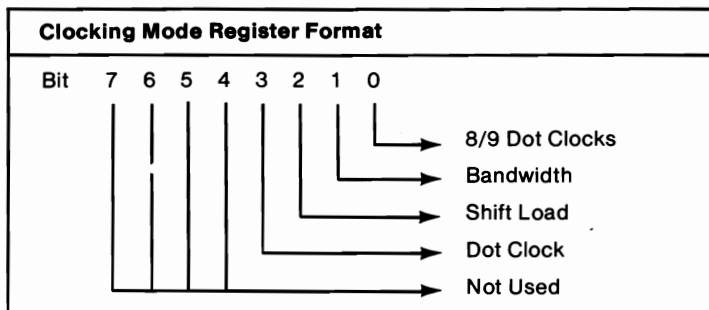


**Bit 0** Asynchronous Reset—A logical 0 commands the sequencer to asynchronous clear and halt. All outputs are placed in the high impedance state when this bit is a 0. A logical 1 commands the sequencer to run unless bit 1 is set to zero. Resetting the sequencer with this bit can cause data loss in the dynamic RAMs.

**Bit 1** Synchronous Reset—A logical 0 commands the sequencer to synchronous clear and halt. Bits 1 and 0 must both be ones to allow the sequencer to operate. Reset the sequencer with this bit before changing the Clocking Mode Register, if memory contents are to be preserved.

## Clocking Mode Register

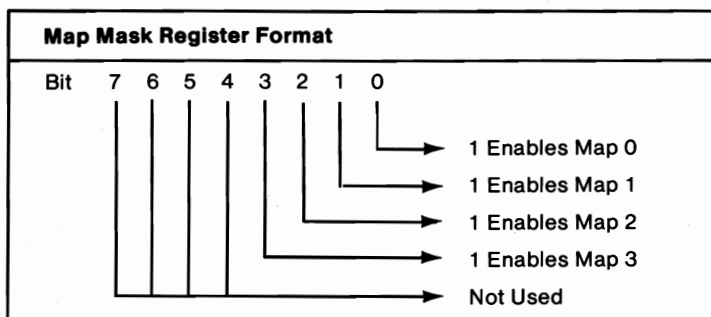
This is a write-only register pointed to when the value in the address register is hex 01. The output port address for this register is hex 3C5.



- Bit 0**      8/9 Dot Clocks—A logical 0 directs the sequencer to generate character clocks 9 dots wide; a logical 1 directs the sequencer to generate character clocks 8 dots wide. Monochrome alphanumeric mode (07H) is the only mode that uses character clocks 9 dots wide. All other modes must use 8 dots per character clock.
- Bit 1**      Bandwidth—A logical 0 makes CRT memory cycles occur on 4 out of 5 available memory cycles; a logical 1 makes CRT memory cycles occur on 2 out of 5 available memory cycles. Medium resolution modes require less data to be fetched from the display buffer during the horizontal scan time. This allows the CPU greater access time to the display buffer. All high resolution modes must provide the CRTC with 4 out of 5 memory cycles in order to refresh the display image.
- Bit 2**      Shift Load—When set to 0, the video serializers are reloaded every character clock; when set to 1, the video serializers are loaded every other character clock. This mode is useful when 16 bits are fetched per cycle and chained together in the shift registers.
- Bit 3**      Dot Clock—A logical 0 selects normal dot clocks derived from the sequencer master clock input. When this bit is set to 1, the master clock will be divided by 2 to generate the dot clock. All the other timings will be stretched since they are derived from the dot clock. Dot clock divided by two is used for 320x200 modes (0, 1, 4, 5) to provide a pixel rate of 7 MHz, (9 MHz for mode D).

## **Map Mask Register**

This is a write-only register pointed to when the value in the address register is hex 02. The output port address for this register is hex 3C5.

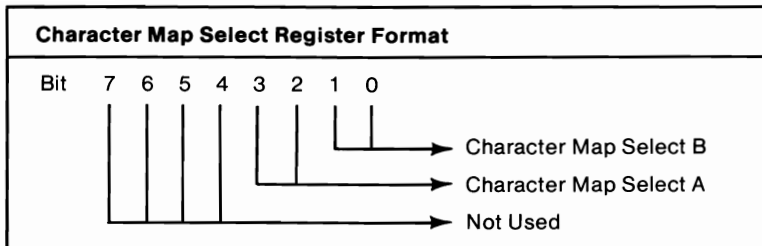


### Bit 0–Bit 3

**Map Mask**—A logical 1 in bits 3 through 0 enables the processor to write to the corresponding maps 3 through 0. If this register is programmed with a value of 0FH, the CPU can perform a 32-bit write operation with only one memory cycle. This substantially reduces the overhead on the CPU during display update cycles in graphics modes. Data scrolling operations are also enhanced by setting this register to a value of 0FH and writing the display buffer address with the data stored in the CPU data latches. This is a read-modify-write operation. When odd/even modes are selected, maps 0 and 1 and maps 2 and 3 should have the same map mask value.

### Character Map Select Register

This is a write-only register pointed to when the value in the address register is hex 03. The output port address for this register is 3C5.



**Bit 0–Bit 1**      Character Map Select B—Selects the map used to generate alpha characters when attribute bit 3 is a 0, according to the following table:

Bits		Map Selected	Table Location
1	0		
Value			
0	0	0	1st 8K of Plane 2 Bank 0
0	1	1	2nd 8K of Plane 2 Bank 1
1	0	2	3rd 8K of Plane 2 Bank 2
1	1	3	4th 8K of Plane 2 Bank 3

**Bit 2–Bit 3**      Character Map Select A—Selects the map used to generate alpha characters when attribute bit 3 is a 1, according to the following table:

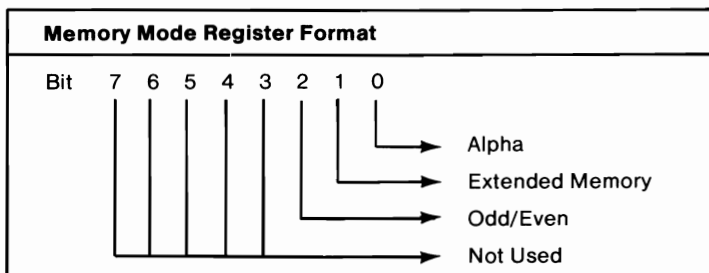
Bits		Map Selected	Table Location
3	2		
Value			
0	0	0	1st 8K of Plane 2 Bank 0
0	1	1	2nd 8K of Plane 2 Bank 1
1	0	2	3rd 8K of Plane 2 Bank 2
1	1	3	4th 8K of Plane 2 Bank 3

In alphanumeric modes, bit 3 of the attribute byte normally has the function of turning the foreground intensity on or off. This bit however may be redefined as a switch between character sets. This function is enabled when there is a difference between the value in Character Map Select A and the value in Character Map Select B. Whenever these two values are the same, the character select function is disabled. The memory mode register bit 1 must be a 1 (indicates the memory extension card is installed in the unit) to enable this function; otherwise, bank 0 is always selected.

128K of graphics memory is required to support two character sets. 256K supports four character sets. Asynchronous reset clears this register to 0. This should be done only when the sequencer is reset.

## Memory Mode Register

This is a write-only register pointed to when the value in the address register is hex 04. The processor output port address for this register is 3C5.



**Bit 0** Alpha—A logical 0 indicates that a non-alpha mode is active. A logical 1 indicates that alpha mode is active and enables the character generator map select function.

**Bit 1** Extended Memory—A logical 0 indicates that the memory expansion card is not installed. A logical 1 indicates that the memory expansion card is installed and enables access to the extended memory through address bits 14 and 15.

**Bit 2** Odd/Even—A logical 0 directs even processor addresses to access maps 0 and 2, while odd processor addresses access maps 1 and 3. A logical 1 causes processor addresses to sequentially access data within a bit map. The maps are accessed according to the value in the map mask register.

## CRT Controller Registers

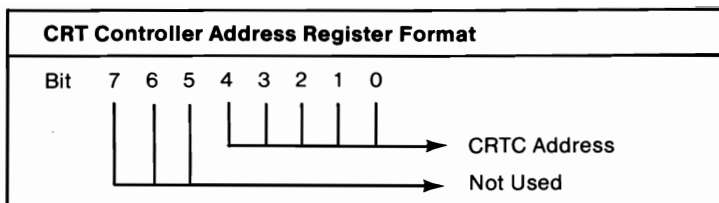
Name	Port	Index
Address Register	374	-
Horizontal Total	375	00
Horizontal Display End	375	01
Start Horizontal Blank	375	02
End Horizontal Blank	375	03
Start Horizontal Retrace	375	04
End Horizontal Retrace	375	05
Vertical Total	375	06
Overflow	375	07
Preset Row Scan	375	08
Max Scan Line	375	09
Cursor Start	375	0A
Cursor End	375	0B
Start Address High	375	0C
Start Address Low	375	0D
Cursor Location High	375	0E
Cursor Location Low	375	0F
Vertical Retrace Start	375	10
Light Pen High	375	10
Vertical Retrace End	375	11
Light Pen Low	375	11
Vertical Display End	375	12
Offset	375	13
Underline Location	375	14
Start Vertical Blank	375	15
End Vertical Blank	375	16
Mode Control	375	17
Line Compare	375	18

? = B in Monochrome Modes and D in Color Modes

### CRT Controller Address Register

The Address register is a pointer register located at hex 3B4 or hex 3D4. If an IBM Monochrome Display is attached to the adapter, address 3B4 is used. If a color display is attached to the adapter, address 3D4 is used. This register is loaded with a binary value that points to the CRT Controller data register where data is to be written. This value is referred to as "Index" in the table above.

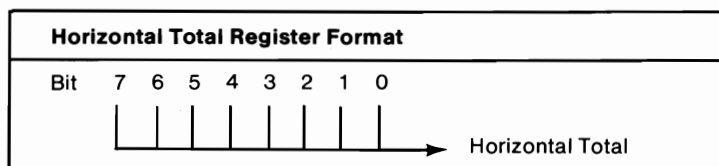




**Bit 0–Bit 4** CRT Controller Address Bits—A binary value pointing to the CRT Controller register where data is to be written.

## Horizontal Total Register

This is a write-only register pointed to when the value in the CRT Controller address register is hex 00. The processor output port address for this register is hex 3B5 or hex 3D5.

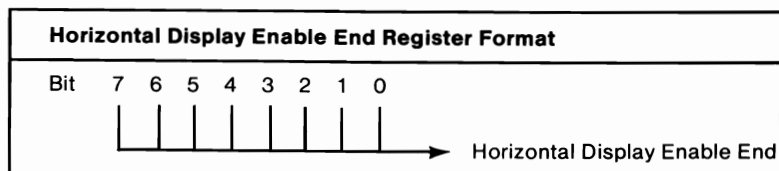


This register defines the total number of characters in the horizontal scan interval including the retrace time. The value directly controls the period of the horizontal retrace output signal. An internal horizontal character counter counts character clock inputs to the CRT Controller, and all horizontal and vertical timings are based upon the horizontal register. Comparators are used to compare register values with horizontal character values to provide horizontal timings.

**Bit 0–Bit 7** Horizontal Total—The total number of characters less 2.

## Horizontal Display Enable End Register

This is a write-only register pointed to when the value in the CRT Controller address register is hex 01. The processor output port address for this register is hex 3B5 or hex 3D5.

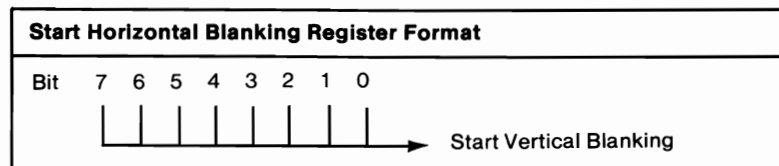


This register defines the length of the horizontal display enable signal. It determines the number of displayed character positions per horizontal line.

**Bit 0-Bit 7**      Horizontal display enable end —A value one less than the total number of displayed characters.

## Start Horizontal Blanking Register

This is a write-only register pointed to when the value in the CRT Controller address register is hex 02. The processor output port address for this register is hex 3B5 or hex 3D5.



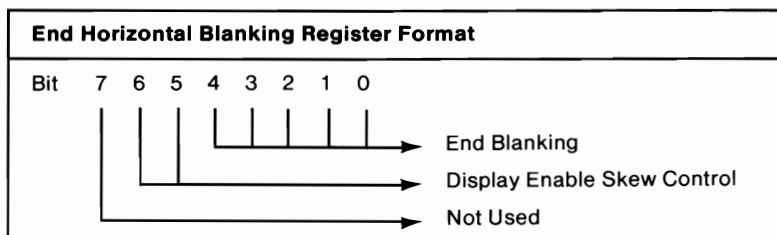
This register determines when the horizontal blanking output signal becomes active. The row scan address and underline scan line decode outputs are multiplexed on the memory address outputs and cursor outputs respectively during the blanking interval. These outputs are latched external to the CRT Controller with the falling edge of the BLANK output signal. The row scan address and underline signals remain on the output signals for one character count beyond the end of the blanking signal.

**Bit 0-Bit 7**

**Start Horizontal Blanking**—The horizontal blanking signal becomes active when the horizontal character counter reaches this value.

**End Horizontal Blanking Register**

This is a write-only register pointed to when the value in the CRT Controller address register is hex 03. The processor output port address for this register is hex 3B5 or hex 3D5.



This register determines when the horizontal blanking output signal becomes inactive. The row scan address and underline scan line decode outputs are multiplexed on the memory address outputs and the cursor outputs respectively during the blanking interval. These outputs are latched external to the CRT Controller with the falling edge of the BLANK output signal. The row scan address and underline signals remain on the output signals for one character count beyond the end of the blanking signal.

**Bit 0-Bit 4**

**End Horizontal Blanking**—A value equal to the five least significant bits of the horizontal character counter value at which time the horizontal blanking signal becomes inactive (logical 0). To obtain a blanking signal of width W, the following algorithm is used: Value of Start Blanking Register + Width of Blanking signal in character clock units = 5-bit result to be programmed into the End Horizontal Blanking Register.

**Bit 5–Bit 6**

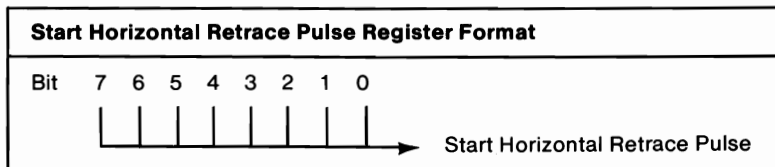
**Display Enable Skew Control**—These two bits determine the amount of display enable skew. Display enable skew control is required to provide sufficient time for the CRT Controller to access the display buffer to obtain a character and attribute code, access the character generator font, and then go through the Horizontal Pel Panning Register in the Attribute Controller. Each access requires the display enable signal to be skewed one character clock unit so that the video output is in synchronization with the horizontal and vertical retrace signals. The bit values and amount of skew are shown in the following table:

**Bits****6 5**

<b>0 0</b>	Zero character clock skew
<b>0 1</b>	One character clock skew
<b>1 0</b>	Two character clock skew
<b>1 1</b>	Three character clock skew

**Start Horizontal Retrace Pulse Register**

This is a write-only register pointed to when the value in the CRT Controller address register is hex 04. The processor output port address for this register is hex 3B5 or hex 3D5.



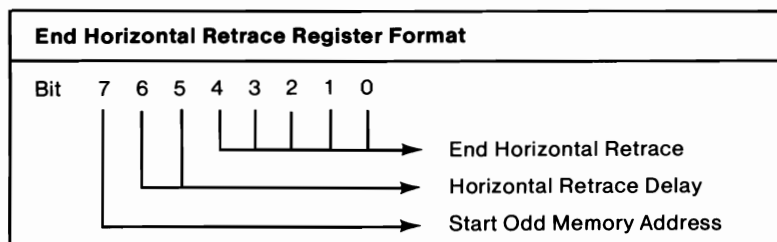
This register is used to center the screen horizontally, and to specify the character position at which the Horizontal Retrace Pulse becomes active.

**Bit 0-Bit 7**

**Start Horizontal Retrace Pulse**—The value programmed is a binary count of the character position number at which the signal becomes active.

## End Horizontal Retrace Register

This is a write-only register pointed to when the value in the CRT Controller address register is hex 05. The processor output port address for this register is hex 3B5 or hex 3D5.



This register specifies the character position at which the Horizontal Retrace Pulse becomes inactive (logical 0).

**Bit 0-Bit 4**

**End Horizontal Retrace**—A value equal to the five least significant bits of the horizontal character counter value at which time the horizontal retrace signal becomes inactive (logical 0). To obtain a retrace signal of width W, the following algorithm is used: Value of Start Retrace Register + width of horizontal retrace signal in character clock units = 5-bit result to be programmed into the End Horizontal Retrace Register.

**Bit 5-Bit 6**

**Horizontal Retrace Delay**—These bits control the skew of the horizontal retrace signal. Binary 00 equals no Horizontal Retrace Delay. For some modes, it is necessary to provide a horizontal retrace signal that takes up the entire blanking interval. Some internal timings are generated by the falling edge of the horizontal retrace signal. To guarantee the signals are

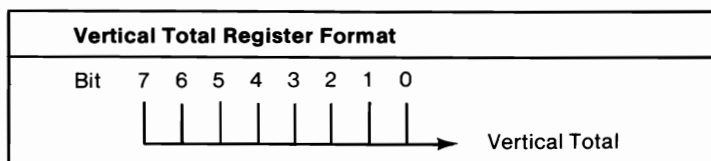
latched properly, the retrace signal is started before the end of the display enable signal, and then skewed several character clock times to provide the proper screen centering.

#### Bit 7

**Start Odd/Even Memory Address**—This bit controls whether the first CRT memory address output after a horizontal retrace begins with an even or an odd address. A logical 0 selects even addresses; a logical 1 selects odd addresses. This bit is used for horizontal pel panning applications. Generally, this bit should be set to a logical 0.

### Vertical Total Register

This is a write-only register pointed to when the value in the CRT Controller address register is hex 06. The processor output port address for this register is hex 3B5 or 3D5.

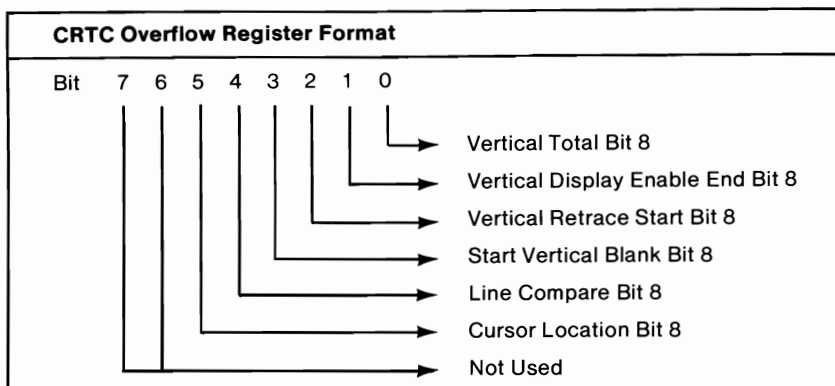


#### Bit 0–Bit 7

**Vertical Total**—This is the low-order eight bits of a nine-bit register. The binary value represents the number of horizontal raster scans on the CRT screen, including vertical retrace. The value in this register determines the period of the vertical retrace signal. Bit 8 of this register is contained in the CRT Controller Overflow Register hex 07 bit 0.

### CRT Controller Overflow Register

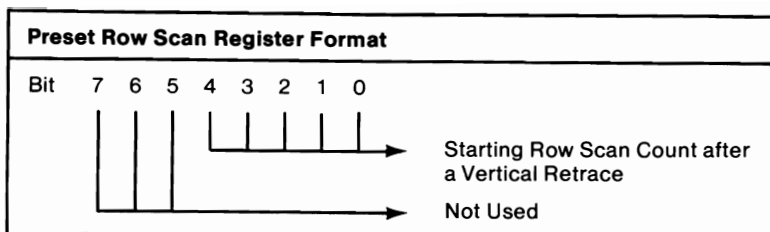
This is a write-only register pointed to when the value in the CRT Controller Address Register is hex 07. The processor output port address for this register is hex 3B5 or hex 3D5.



- Bit 0**            Vertical Total—Bit 8 of the Vertical Total register (index hex 06).
- Bit 1**            Vertical Display Enable End—Bit 8 of the Vertical Display Enable End register (index hex 12).
- Bit 2**            Vertical Retrace Start—Bit 8 of the Vertical Retrace Start register (index hex 10).
- Bit 3**            Start Vertical Blank—Bit 8 of the Start Vertical Blank register (index hex 15).
- Bit 4**            Line Compare—Bit 8 of the Line Compare register (index hex 18).
- Bit 5**            Cursor Location—Bit 8 of the Cursor Location register (index hex 0A).

### Preset Row Scan Register

This is a write-only register pointed to when the value in the CRT Controller address register is hex 08. The processor output port address for this register is hex 3B5 or hex 3D5.

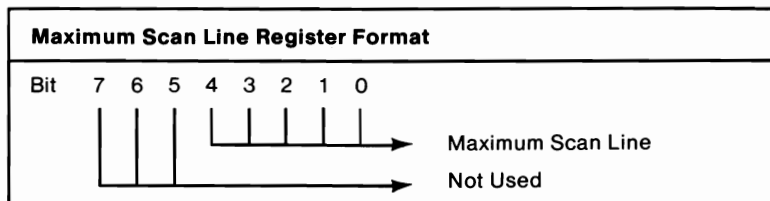


This register is used for pel scrolling.

**Bit 0–Bit 4**      **Preset Row Scan (Pel Scrolling)**—This register specifies the starting row scan count after a vertical retrace. The row scan counter increments each horizontal retrace time until a maximum row scan occurs. At maximum row scan compare time the row scan is cleared (not preset).

## Maximum Scan Line Register

This is a write-only register pointed to when the value in the CRT Controller address register is hex 09. The processor output port address for this register is hex 3B5 or hex 3D5.



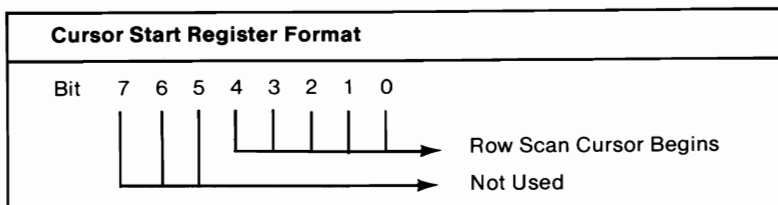
**Bit 0–Bit 4**      **Maximum Scan Line**—This register specifies the number of scan lines per character row. The number to be programmed is the maximum row scan number minus one.

## Cursor Start Register

This is a write-only register pointed to when the value in the CRT Controller address register is hex 0A. The processor output port



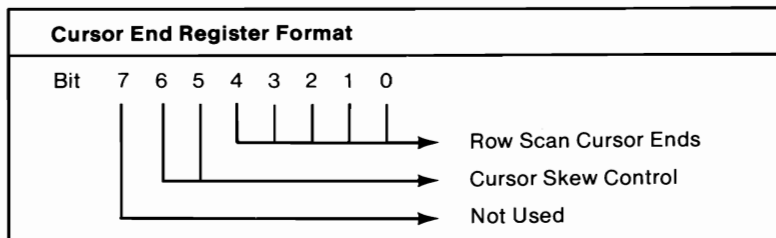
address for this register is hex 3B5 or hex 3D5.



**Bit 0-Bit 4**      **Cursor Start**—This register specifies the row scan of a character line where the cursor is to begin. The number programmed should be one less than the starting cursor row scan.

### Cursor End Register

This is a write-only register pointed to when the value in the CRT Controller address register is hex 0B. The processor output port address for this register is hex 3B5 or hex 3D5.



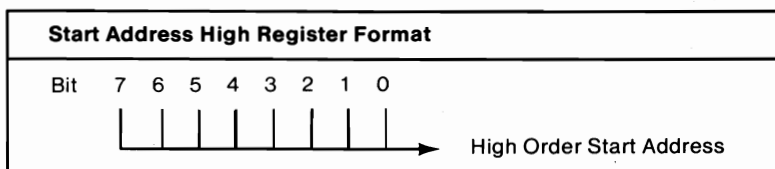
**Bit 0-Bit 4**      **Cursor End**—These bits specify the row scan where the cursor is to end.

**Bit 5-Bit 6**      **Cursor Skew**—These bits control the skew of the cursor signal.

Bits		
6	5	
<hr/>		
0	0	Zero character clock skew
0	1	One character clock skew
1	0	Two character clock skew
1	1	Three character clock skew

## Start Address High Register

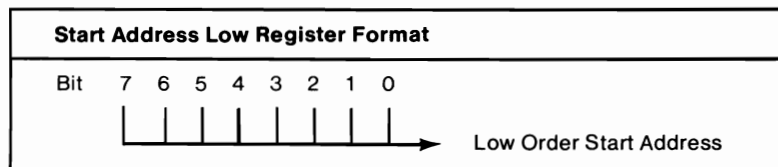
This is a read/write register pointed to when the value in the CRT Controller address register is hex 0C. The processor input/output port address for this register is hex 3B5 or hex 3D5.



**Bit 0-Bit 7**      **Start Address High**—These are the high-order eight bits of the start address. The 16-bit value, from the high-order and low-order start address registers, is the first address after the vertical retrace on each screen refresh.

## Start Address Low Register

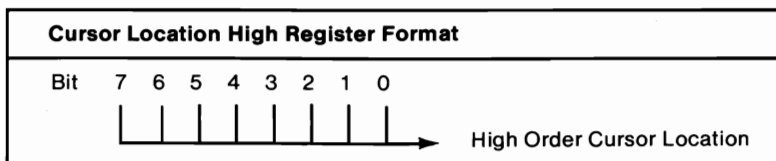
This is a read/write register pointed to when the value in the CRT Controller address register is hex 0D. The processor input/output port address for this register is hex 3B5 or hex 3D5.



**Bit 0-Bit 7**      **Start Address Low**—These are the low-order 8 bits of the start address.

### Cursor Location High Register

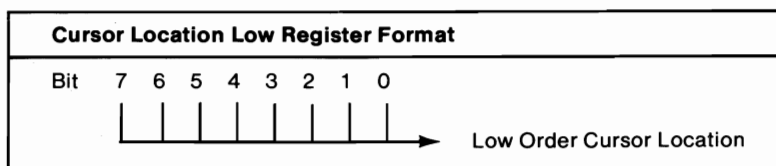
This is a read/write register pointed to when the value in the CRT Controller address register is hex 0E. The processor input/output port address for this register is hex 3B5 or hex 3D5.



**Bit 0-Bit 7**      **Cursor Location High**—These are the high-order 8 bits of the cursor location.

### Cursor Location Low Register

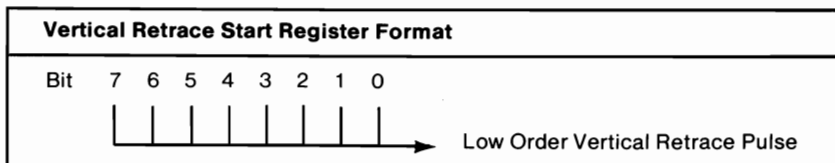
This is a read/write register pointed to when the value in the CRT Controller address register is hex 0F. The processor input/output port address for this register is hex 3B5 or Hex 3D5.



**Bit 0-Bit 7**      **Cursor Location Low**— These are the low-order 8 bits of the cursor location.

## Vertical Retrace Start Register

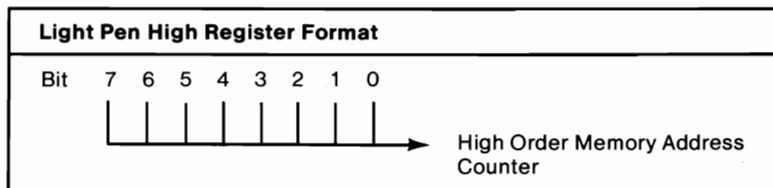
This is a write-only register pointed to when the value in the CRT Controller address register is hex 10. The processor output port address for this register is hex 3B5 or hex 3D5.



**Bit 0–Bit 7** Vertical Retrace Start—This is the low-order 8 bits of the vertical retrace pulse start position programmed in horizontal scan lines. Bit 8 is in the overflow register location hex 07.

## Light Pen High Register

This is a read-only register pointed to when the value in the CRT Controller address register is hex 10. The processor input port address for this register is hex 3B5 or hex 3D5.

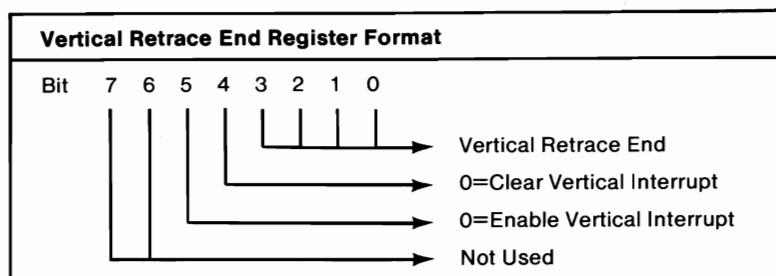


**Bit 0–Bit 7** Light Pen High—This is the high order 8 bits of the memory address counter at the time the light pen was triggered.

## Vertical Retrace End Register

This is a write-only register pointed to when the value in the CRT Controller address register is hex 11. The processor output port

address for this register is hex 3B5 or hex 3D5.



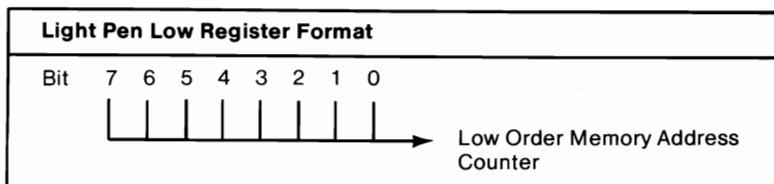
**Bit 0–Bit 3**      **Vertical Retrace End**—These bits determine the horizontal scan count value when the vertical retrace output signal becomes inactive. The register is programmed in units of horizontal scan lines. To obtain a vertical retrace signal of width W, the following algorithm is used: Value of Start Vertical Retrace Register + width of vertical retrace signal in horizontal scan units = 4-bit result to be programmed into the End Horizontal Retrace Register.

**Bit 4**              **Clear Vertical Interrupt**—A logical 0 will clear a vertical interrupt.

**Bit 5**              **Enable Vertical Interrupt**—A logical 0 will enable vertical interrupt.

## Light Pen Low Register

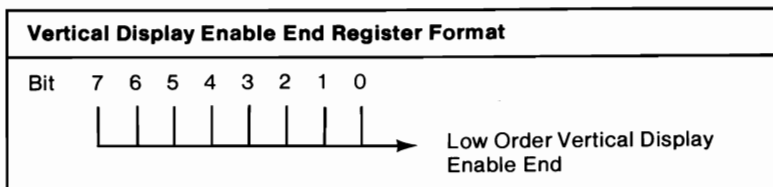
This is a read-only register pointed to when the value in the CRT Controller address register is hex 11. The processor input port address for this register is hex 3B5 or 3D5.



**Bit 0–Bit 7**      Light Pen Low—This is the low-order 8 bits of the memory address counter at the time the light pen was triggered.

## Vertical Display Enable End Register

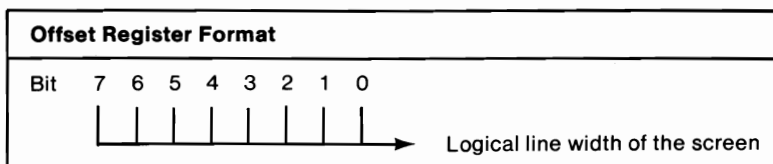
This is a write-only register pointed to when the value in the CRT Controller address register is hex 12. The processor output port address for this register is hex 3B5 or hex 3D5.



**Bit 0–Bit 7**      Vertical Display Enable End—These are the low-order 8 bits of the vertical display enable end position. This address specifies which scan line ends the active video area of the screen. Bit 8 is in the overflow register location hex 07.

## Offset Register

This is a write-only register pointed to when the value in the CRT Controller address register is hex 13. The processor output port address for this register is hex 3B5 or hex 3D5.

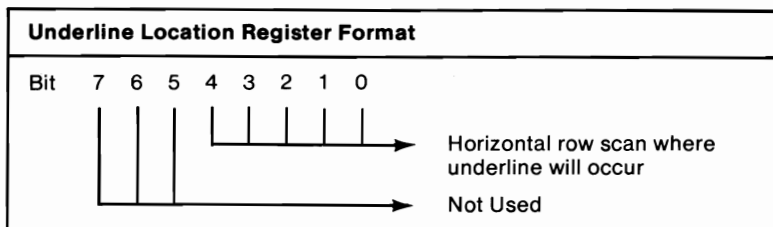


### Bit 0–Bit 7

**Offset**—This register specifies the logical line width of the screen. The starting memory address for the next character row is larger than the current character row by this amount. The Offset Register is programmed with a word address. Depending upon the method of clocking the CRT Controller, this word address is either a word or double word address.

## Underline Location Register

This is a write-only register pointed to when the value in the CRT Controller address register is hex 14. The processor output port address for this register is hex 3B5 or hex 3D5.



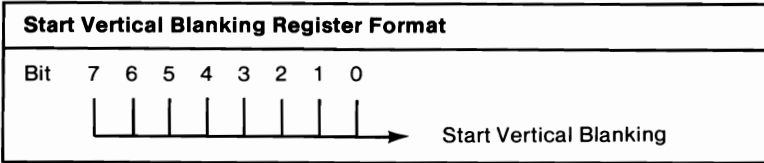
### Bit 0–Bit 4

**Underline Location**—This register specifies the horizontal row scan on which underline will occur. The value programmed is one less than the scan line number desired.

## Start Vertical Blanking Register

This is a write-only register pointed to when the value in the CRT Controller address register is hex 15. The processor output port

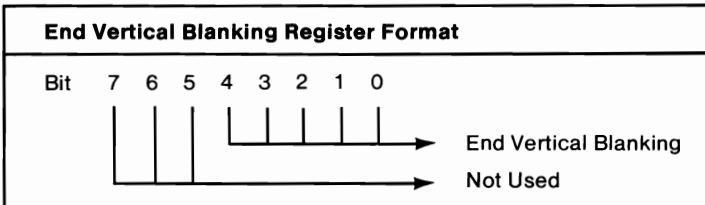
address for this register is hex 3B5 or hex 3D5.



**Bit 0–Bit 7**      **Start Vertical Blank**—These are the low 8 bits of the horizontal scan line count, at which the vertical blanking signal becomes active. Bit 8 bit is in the overflow register hex 07.

### End Vertical Blanking Register

This is a write-only register pointed to when the value in the CRT Controller address register is hex 16. The processor output port address for this register is hex 3B5 or hex 3D5.

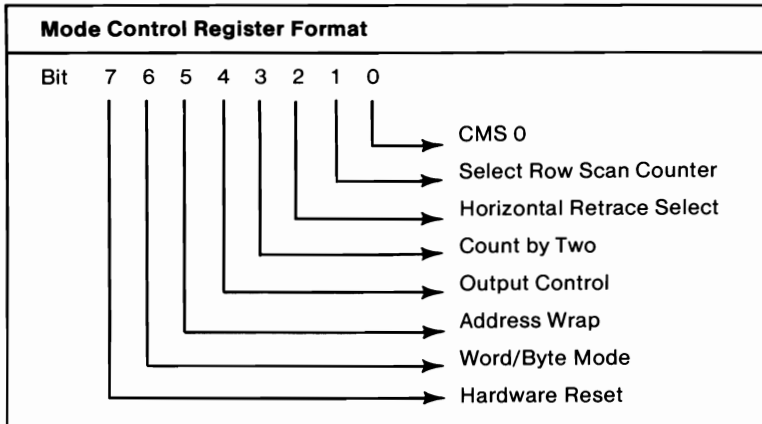


**Bit 0–Bit 4**      **End Vertical Blank**—This register specifies the horizontal scan count value when the vertical blank output signal becomes inactive. The register is programmed in units of horizontal scan lines. To obtain a vertical blank signal of width  $W$ , the following algorithm is used: Value of Start Vertical Blank Register + width of vertical blank signal in horizontal scan units = 5-bit result to be programmed into the End Vertical Blank Register.



## Mode Control Register

This is a write-only register pointed to when the value in the CRT Controller address register is hex 17. The processor output port address for this register is hex 3B5 or hex 3D5.



### Bit 0

**Compatibility Mode Support**— When this bit is a logical 0, the row scan address bit 0 is substituted for memory address bit 13 during active display time. A logical 1 enables memory address bit 13 to appear on the memory address output bit 13 signal of the CRT Controller. The CRT Controller used on the IBM Color/Graphics Monitor Adapter is the 6845. The 6845 has 128 horizontal scan line address capability. To obtain 640 by 200 graphics resolution, the CRTC was programmed for 100 horizontal scan lines with 2 row scan addresses per character row. Row scan address bit 0 became the most significant address bit to the display buffer. Successive scan lines of the display image were displaced in memory by 8K bytes. This bit allows compatibility with the 6845 and Color Graphics APA modes of operation.

- Bit 1**                      Select Row Scan Counter—A logical 0 selects row scan counter bit 1 on MA 14 output pin. A logical 1 selects MA 14 counter bit on MA 14 output pin.
- Bit 2**                      Horizontal Retrace Select—This bit selects Horizontal Retrace or Horizontal Retrace divided by 2 as the clock that controls the vertical timing counter. This bit can be used to effectively double the vertical resolution capability of the CRT Controller. The vertical counter has a maximum resolution of 512 scan lines due to the 9-bit wide Vertical Total Register. If the vertical counter is clocked with the horizontal retrace divided by 2 clock, then the vertical resolution is doubled to 1024 horizontal scan lines. A logical 0 selects HRTC and a logical 1 selects HRTC divided by 2.
- Bit 3**                      Count By Two— When this bit is set to 0, the memory address counter is clocked with the character clock input. A logical 1 clocks the memory address counter with the character clock input divided by 2. This bit is used to create either a byte or word refresh address for the display buffer.
- Bit 4**                      Output Control—A logical 0 enables the module output drivers. A logical 1 forces all outputs into high impedance state.
- Bit 5**                      Address Wrap—This bit selects Memory Address counter bit MA 13 or bit MA 15, and it appears on the MA 0 output pin in the word address mode. If you are not in the word address mode, MA 0 counter output appears on the MA 0 output pin. A logical 1 selects MA 15. In odd/even mode, bit MA 13 should be selected when the 64K memory is installed on the board. Bit MA 15 should be selected when greater than 64K memory is installed. This function is used to implement Color Graphics Monitor Adapter compatibility.

**Bit 6**

**Word Mode or Byte Mode**—When this bit is a logical 0, the Word Mode shifts all memory address counter bits down one bit, and the most significant bit of the counter appears on the least significant bit of the memory address outputs. See table below for address output details. A logical 1 selects the Byte Address mode.

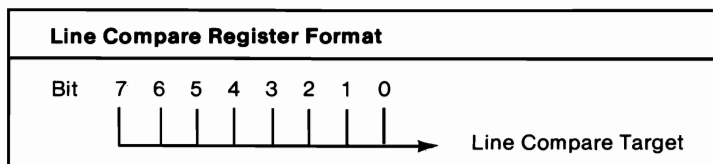
CRTC Out Pin	Internal Memory Address Counter Wiring to the Output Multiplexer	
	Byte Address Mode	Word Address Mode
MA 0/RFA 0	MA 0	MA 15 or MA 13
MA 1/RFA 1	MA 1	MA 0
MA 2/RFA 2	MA 2	MA 1
MA 3/RFA 3	MA 3	MA 2
*	*	*
*	*	*
*	*	*
MA 14/RS 3	MA 14	MA 13
MA 15/RS 4	MA 15	MA 14

**Bit 7**

**Hardware Reset**—A logical 0 forces horizontal and vertical retrace to clear. A logical 1 forces horizontal and vertical retrace to be enabled.

## Line Compare Register

This is a write-only register pointed to when the value in the CRT Controller address register is hex 18. The processor output port address for this register is hex 3B5 or hex 3D5.

**Bit 0–Bit 7**

**Line Compare**—This register is the low-order 8 bits of the compare target. When the vertical

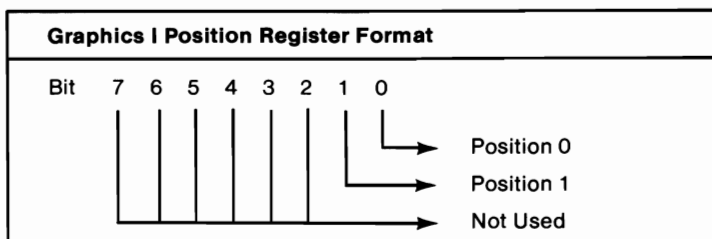
counter reaches this value, the internal start of the line counter is cleared. This allows an area of the screen to be immune to scrolling. Bit 8 of this register is in the overflow register hex 07.

# Graphics Controller Registers

Name	Port	Index
Graphics 1 Position	3CC	-
Graphics 2 Position	3CA	-
Graphics 1 & 2 Address	3CE	-
Set/Reset	3CF	00
Enable Set/Reset	3CF	01
Color Compare	3CF	02
Data Rotate	3CF	03
Read Map Select	3CF	04
Mode Register	3CF	05
Miscellaneous	3CF	06
Color Don't Care	3CF	07
Bit Mask	3CF	08

## Graphics 1 Position Register

This is a write-only register. The processor output port address for this register is hex 3CC.

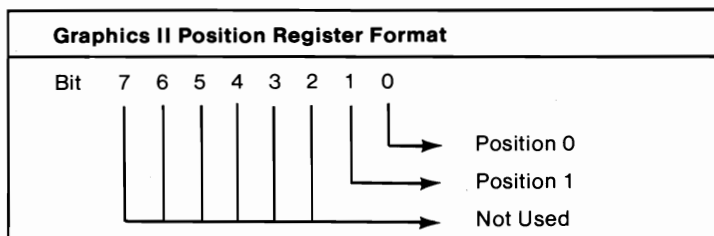


### Bit 0-Bit 1

**Position**—These 2 bits are binary encoded hierarchy bits for the graphics chips. The position register controls which 2 bits of the processor data bus each chip responds to. Graphics 1 must be programmed with a position register value of 0 for this card.

## Graphics 2 Position Register

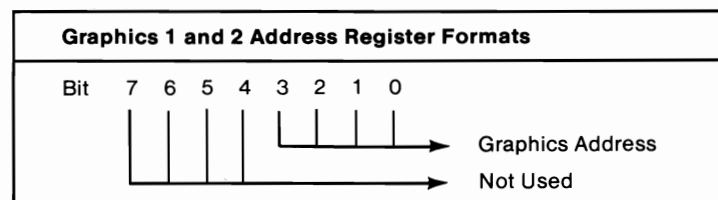
This is a write-only register. The processor output port address for this register is hex 3CA.



**Bit 0–Bit 1** Position—These 2 bits are binary encoded hierarchy bits for the graphics chips. The position register controls which 2 bits of the processor data bus to which each chip responds. Graphics 2 must be programmed with a position register value of 1 for this card.

## Graphics 1 and 2 Address Register

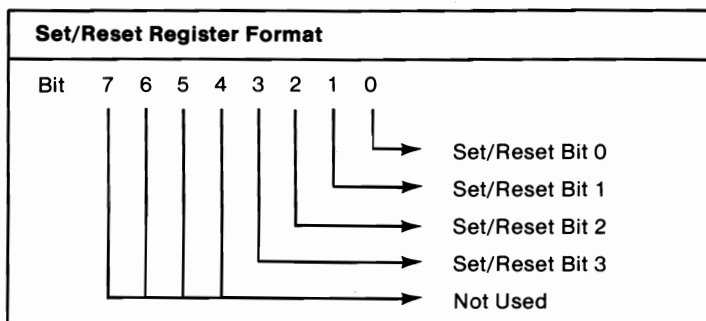
This is a write-only register and the processor output port address for this register is hex 3CE.



**Bit 0–Bit 3** Graphics 1 and 2 Address Bits—This output loads the address register in both graphics chips simultaneously. This register points to the data register of the graphics chips.

## Set/Reset Register

This is a write-only register pointed to by the value in the Graphics 1 and 2 address register. This value must be hex 00 before writing can take place. The processor output port address for this register is hex 3CF.

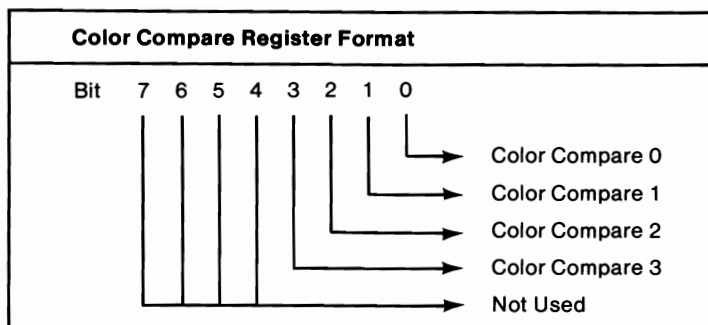


### Bit 0–Bit 3

**Set/Reset**—These bits represent the value written to the respective memory planes when the processor does a memory write with write mode 0 selected and set/reset mode is enabled. Set/Reset can be enabled on a plane by plane basis with separate OUT commands to the Set/Reset register.

## Enable Set/Reset Register

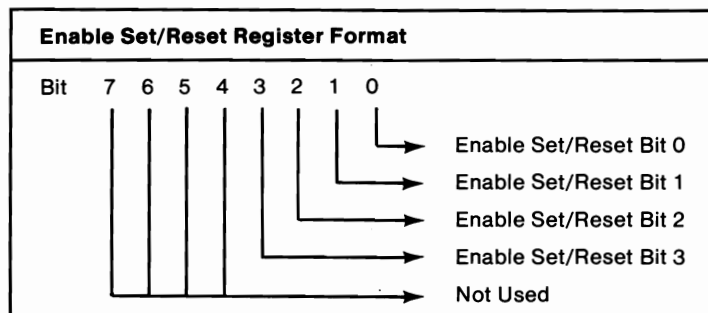
This is a write-only register and is pointed to by the value in the Graphics 1 and 2 address register. This value must be hex 01 before writing can take place. The processor output port for this register is hex 3CF.



**Bit 0-Bit 3** Enable Set/Reset—These bits enable the set/reset function. The respective memory plane is written with the value of the Set/Reset register provided the write mode is 0. When write mode is 0 and Set/Reset is not enabled on a plane, that plane is written with the value of the processor data.

## Color Compare Register

This is a write-only register pointed to by the value in the Graphics 1 and 2 address register. This value must be hex 02 before writing can take place. The processor output port address for this register is hex 3CF.



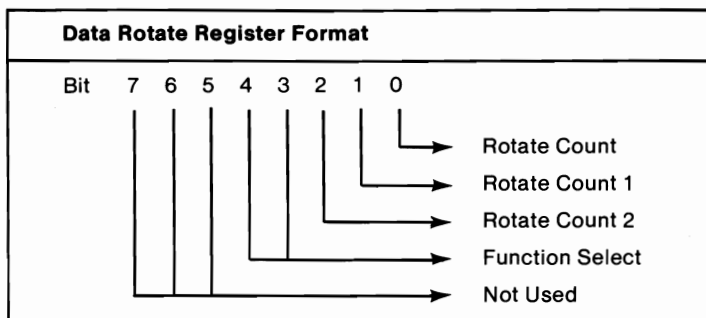
**Bit 0-Bit 3** Color Compare—These bits represent a 4 bit color value to be compared. If the processor sets



read mode 1 on the graphics chips, and does a memory read, the data returned from the memory cycle will be a 1 in each bit position where the 4 bit planes equal the color compare register.

## Data Rotate Register

This is a write-only register pointed to by the value in the Graphics 1 and 2 address register. This value must be hex 03 before writing can take place. The processor output port address for this register is hex 3CF.



### Bit 0–Bit 2

**Rotate Count**—These bits represent a binary encoded value of the number of positions to rotate the processor data bus during processor memory writes. This operation is done when the write mode is 0. To write unrotated data the processor must select a count of 0.

### Bit 3–Bit 4

**Function Select**—Data written to memory can operate logically with data already in the processor latches. The bit functions are defined in the following table.

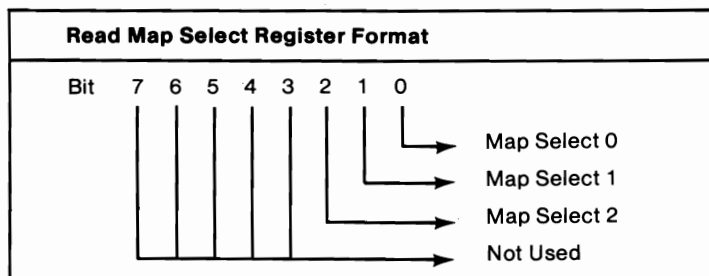
**Bits****4 3**

<b>0 0</b>	Data unmodified.
<b>0 1</b>	Data AND'ed with latched data.
<b>1 0</b>	Data OR'ed with latched data.
<b>1 1</b>	Data XOR'ed with latched data.

Data may be any of the choices selected by the Write Mode Register except processor latches. If rotated data is selected, the rotate applies before the logical function.

## Read Map Select Register

This is a write-only register pointed to by the value in the Graphics 1 and 2 address register. This value must be hex 04 before writing can take place. The processor output port address for this register is hex 3CF.

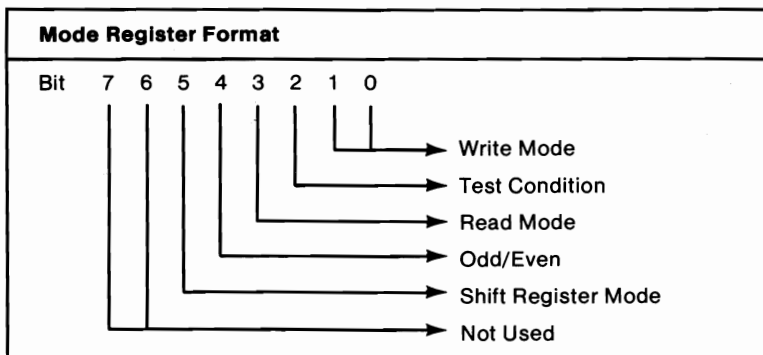


**Bit 0–Bit 2** Map Select—These bits represent a binary encoded value of the memory plane number from which the processor reads data. This register has no effect on the color compare read mode described elsewhere in this section.

## Mode Register

This is a write-only register pointed to by the value in the Graphics 1 and 2 address register. This value must be hex 05

before writing can take place. The processor output port address for this register is 3CF.



### Bit 0-Bit 1 Write Mode

**Bits**  
**1 0**

- |     |  |
|-----|--|
| 0 0 | Each memory plane is written with the processor data rotated by the number of counts in the rotate register, unless Set/Reset is enabled for the plane. Planes for which Set/Reset is enabled are written with 8 bits of the value contained in the Set/Reset register for that plane. |
| 0 1 | Each memory plane is written with the contents of the processor latches. These latches are loaded by a processor read operation.   |
| 1 0 | Memory plane $n$ (0 through 3) is filled with 8 bits of the value of data bit $n$ .  |
| 1 1 | Not Valid  |

The logic function specified by the function select register also applies.

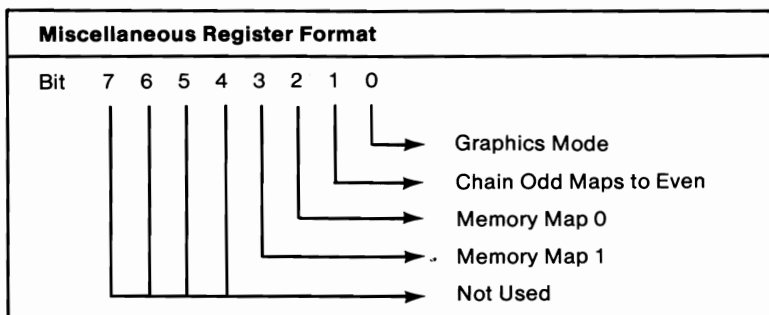
### Bit 2

**Test Condition**—A logical 1 directs graphics controller outputs to be placed in high impedance state for testing.

- Bit 3**      **Read Mode**—When this bit is a logical 0, the processor reads data from the memory plane selected by the read map select register. When this bit is a logical 1, the processor reads the results of the comparison of the 4 memory planes and the color compare register.
- Bit 4**      **Odd/Even**—A logical 1 selects the odd/even addressing mode, which is useful for emulation of the Color Graphics Monitor Adapter compatible modes. Normally the value here follows the value of the Memory Mode Register bit 3 of the Sequencer.
- Bit 5**      **Shift Register**—A logical 1 directs the shift registers on each graphics chip to format the serial data stream with even numbered bits on the even numbered maps and odd numbered bits on the odd maps.

## Miscellaneous Register

This is a write-only register pointed to by the value in the Graphics 1 and 2 address register. This value must be hex 06 before writing can take place. The processor output port for this register is hex 3CF.



- Bit 0** Graphics Mode—This bit controls alpha-mode addressing. A logical 1 selects graphics mode. When set to graphics mode, the character generator address latches are disabled.
- Bit 1** Chain Odd Maps To Even Maps—When set to 1, this bit directs the processor address bit 0 to be replaced by a higher order bit and odd/even maps to be selected with odd/even values of the processor A0 bit, respectively.
- Bit 2-Bit 3** Memory Map—These bits control the mapping of the regenerative buffer into the processor address space.

**Bits**

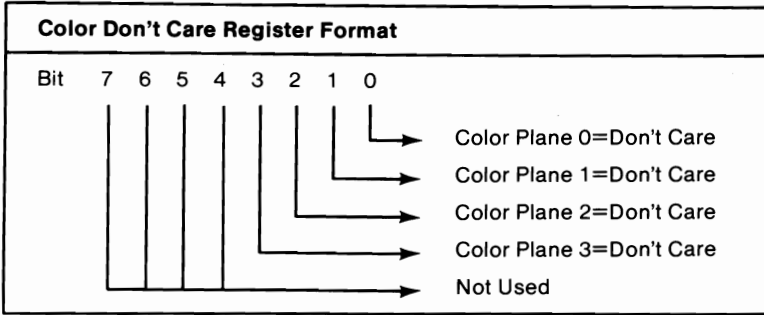
**3 2**

<b>0 0</b>	Hex A000 for 128K bytes.
<b>0 1</b>	Hex A000 for 64K bytes.
<b>1 0</b>	Hex B000 for 32K bytes
<b>1 1</b>	Hex B800 for 32K bytes.

If the display adapter is mapped at address hex A000 for 128K bytes, no other adapter can be installed in the system.

## Color Don't Care Register

This is a write-only register and is pointed to by the value in the Graphics 1 and 2 address register. This value must be hex 07 before writing can take place. The processor output port for this register is hex 3CF.



**Bit 0** Color Don't Care—Color plane 0=don't care when reading color compare when this bit is set to 1.

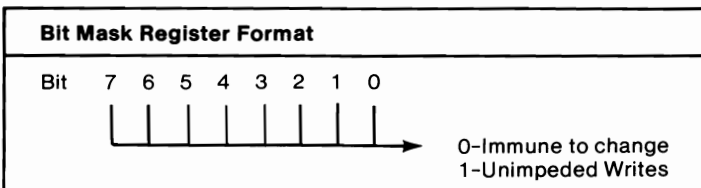
**Bit 1** Color Don't Care—Color plane 1=don't care when reading color compare when this bit is set to 1.

**Bit 2** Color Don't Care—Color plane 2=don't care when reading color compare when this bit is set to 1.

**Bit 3** Color Don't Care—Color plane 3=don't care when reading color compare when this bit is set to 1.

## Bit Mask Register

This is a write-only register and is pointed to by the value in the Graphics 1 and 2 address register. This value must be hex 08 before writing can take place. The processor output port for this register is hex 3CF.



**Bit 0-Bit 7**

**Bit Mask**—Any bit programmed to  $n$  causes the corresponding bit  $n$  in each bit plane to be immune to change provided that the location being written was the last location read by the processor. Bits programmed to a 1 allow unimpeded writes to the corresponding bits in the bit planes.

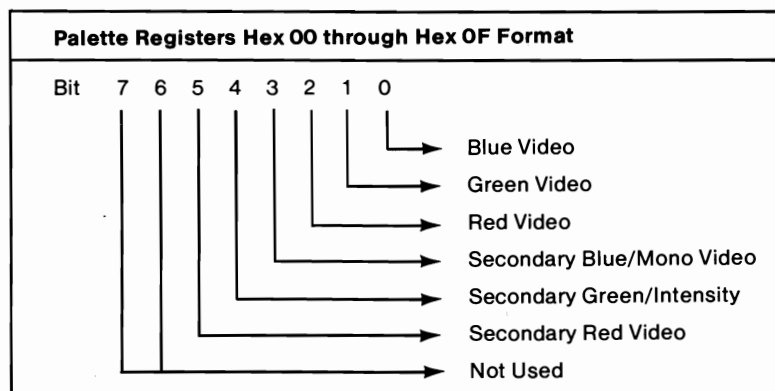
The bit mask applies to any data written by the processor (rotate, AND'ed, OR'ed, XOR'ed, DX, and S/R). To preserve bits using the bit mask, data must be latched internally by reading the location. When data is written to preserve the bits, the most current data in latches is written in those positions. The bit mask applies to all bit planes simultaneously.

## Attribute Controller Registers

Name	Port	Index
Address Register	3C0	-
Palette Registers	3C0	00-0F
Mode Control Register	3C0	10
Overscan Color Register	3C0	11
Color Plane Enable Register	3C0	12
Horizontal Pel Panning Register	3C0	13

### Attribute Address Register

This is a write-only register. The processor output port is hex 3C0.



#### Bit 0-Bit 4

**Attribute Address Bits**—The Address Register is a pointer register located at hex 3C0. This register is loaded with a binary value that points to the attribute data register where data is to be written. The Attribute Controller does not have an address bit input to control selection of the address and data registers. An internal address flip-flop controls selection of either the address or data registers. To initialize the flip-flop, an IOR instruction is issued to the Attribute Controller at address 3BA or 3DA. This clears the flip-flop, and selects the Address Register. After the Address Register has been loaded, the



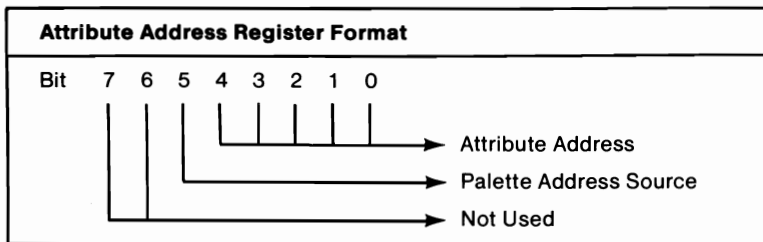
next OUT instruction loads the data register. The flip-flop toggles each time an OUT is issued to the Attribute Controller.

### Bit 5

**Palette Address Source**—When loading the color palette registers, bit 5 must be cleared to 0. To enable the memory data to access the color palette, bit 5 must be set to 1.

## Palette Register Hex 00 through Hex 0F

This is a write-only register. The processor output port is hex 3C0.

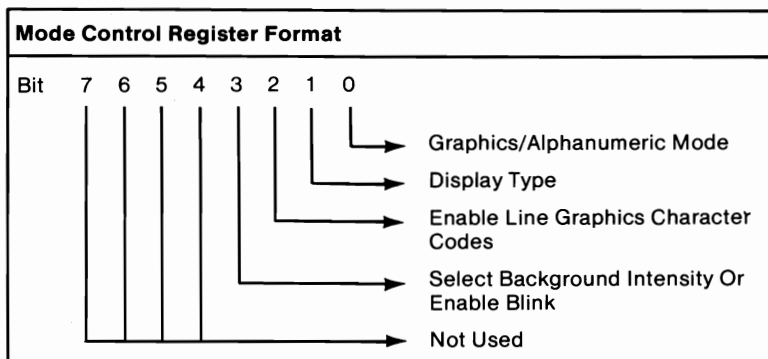


### Bit 0-Bit 5

**Palette**—These 6-bit registers allow a dynamic mapping between the text attribute or graphic color input value and the display color in the CRT screen. A logical 1 selects the appropriate color. A logical 0 de-selects. The color palette register should be modified only during the vertical retrace interval to avoid glitches in the displayed image. Note that some color monitors do not have an intensity input and only a maximum of eight colors are available. Monitors with four color inputs display sixteen colors, and monitors with six color inputs display 64 colors.

## Mode Control Register

This is a write-only register pointed to by the value in the Attribute address register. This value must be hex 10 before writing can take place. The processor output port address for this register is hex 3C0.



- Bit 0** Graphics/Alphanumeric Mode—A logical 0 selects selects alphanumeric mode. A logical 1 selects graphics mode.
- Bit 1** Monochrome Display/Color Display—A logical 0 selects color display attributes. A logical 1 selects IBM Monochrome Display attributes.
- Bit 2** Enable Line Graphics Character Codes—When this bit is set to 0, the ninth dot will be the same as the background. A logical 1 enables the special line graphics character codes for the IBM Monochrome Display adapter. This bit when enabled forces the ninth dot of a line graphic character to be identical to the eighth dot of the character. The line graphics character codes for the Monochrome Display Adapter are Hex C0 through Hex DF.

For character fonts that do not utilize the line graphics character codes in the range of Hex C0

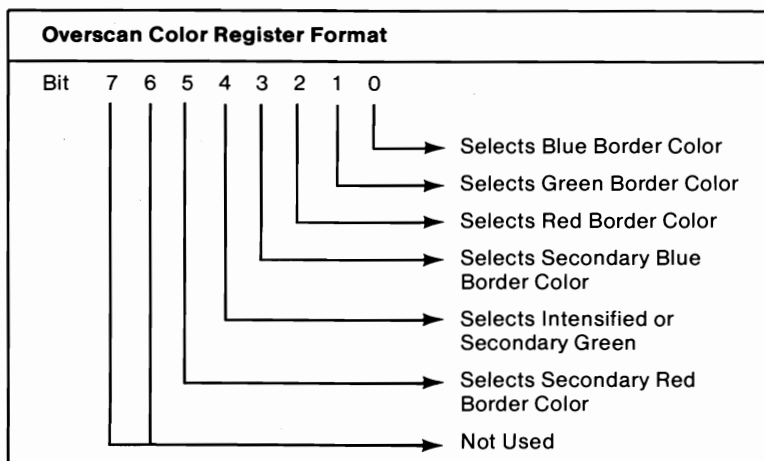
through Hex DF, bit 2 of this register should be a logical 0. Otherwise unwanted video information will be displayed on the CRT screen.

### Bit 3

**Enable Blink/Select Background Intensity**—A logical 0 selects the background intensity of the attribute input. This mode was available on the Monochrome and Color Graphics adapters. A logical 1 enables the blink attribute in alphanumeric modes. This bit must also be set to 1 for blinking graphics modes.

## Overscan Color Register

This is a write-only register pointed to by the value in the Attribute address register. This value must be hex 11 before writing can take place. The processor output port address for this register is hex 3C0.

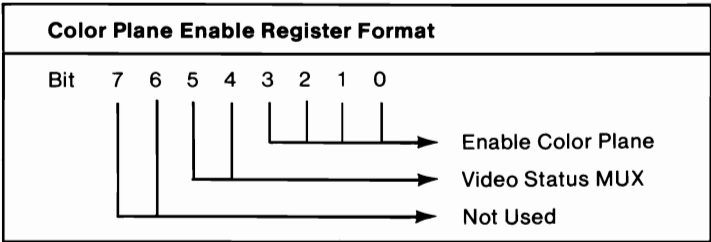


### Bit 0-Bit 5

**Overscan Color**—This 6-bit register determines the overscan (border) color displayed on the CRT screen. For monochrome display this register should be set to a value of 0. A logical 1 selects the appropriate color.

# Color Plane Enable Register

This is a write-only register pointed to by the value in the Attribute address register. This value must be hex 12 before writing can take place. The processor output port address for this register is 3C0.



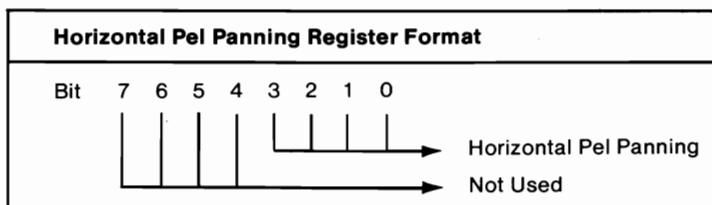
**Bit 0-Bit 3** Enable Color Plane—Writing a logical 1 in any of bits 0 through 3 enables the respective display memory color plane.

**Bit 4-Bit 5** Video Status MUX—Selects two of the six color outputs to be available on the status port. The following table illustrates the combinations available and the color output wiring.

COLOR PLANE ENABLE REGISTER		INPUT STATUS REGISTER ONE	
Bit 5	Bit 4	Bit 5	Bit 4
0	0	Red	Blue
0	1	Secondary Blue	Green
1	0	Secondary Red	Secondary Green
1	1	Not Used	Not Used

# Horizontal Pel Panning Register

This is a write-only register pointed to by the value in the Attribute address register. This value must be hex 12 before writing can take place. The processor output port address for this register is hex 3C0.



### Bit 0–Bit 3

**Horizontal Pel Panning**—This 4 bit register selects the number of picture elements (pels) to shift the video data horizontally to the left. Pel panning is available in both A/N and APA modes. In Monochrome A/N mode, the image can be shifted a maximum of 9 pels. In all other A/N and APA modes, the image can be shifted a maximum of 8 pels. The sequence for shifting the image is given below:

9 pels/character : 8, 0, 1, 2, 3, 4, 5, 6, 7  
(Monochrome A/N mode only)

8 pels/character : 0, 1, 2, 3, 4, 5, 6, 7 (All other Modes)

# Programming Considerations

## Programming the Registers

Each of the LSI devices has an address register and a number of data registers. The address register serves as a pointer to the other registers on the LSI device. It is a write-only register that is loaded by the processor by executing an 'OUT' instruction to its I/O address with the index of the selected data register.

The data registers on each LSI device are accessed through a common I/O address. They are distinguished by the pointer (index) in the address register. To write to a data register, the address register is loaded with the index of the appropriate data register, then the selected data register is loaded by executing an 'OUT' instruction to the common I/O address.

The external registers that are not part of an LSI device and the Graphics I and II registers are not accessed through an address register; they are written to directly.

The following tables define the values that are loaded into the registers by BIOS to support the different modes of operation supported by this adapter.

Register			Mode of Operation																	
Name	Port	Index	0	1	2	3	4	5	6	7	D	E	F	10	F*	10*	0*	1*	2*	3*
Miscellaneous	3C2	—	23	23	23	23	23	23	23	A6	23	23	A2	A7	A2	A7	A7	A7	A7	A7
Feature Cntrl	3?A	—	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
Input Stat 0	3C2	—	—	—	—	—	—	—	—	—	—	—	—	—	—	—	—	—	—	—
Input Stat 1	3?2	—	—	—	—	—	—	—	—	—	—	—	—	—	—	—	—	—	—	—
? = B in monochrome modes      ? = D in color modes																				
*Values for these modes when the IBM Enhanced Color Display is attached																				
*Values for these modes when greater than 64K Graphics Memory is installed																				

## External Registers

Register			Mode of Operation																	
Name	Port	Index	0	1	2	3	4	5	6	7	D	E	F	10	F*	10*	0*	1*	2*	3*
Seq Address	3C4	—	—	—	—	—	—	—	—	—	—	—	—	—	—	—	—	—	—	—
Reset	3C5	00	03	03	03	03	03	03	03	03	03	03	03	03	03	03	03	03	03	03
Clock Mode	3C5	01	0B	0B	01	01	0B	0B	01	00	0B	01	05	05	01	01	0B	0B	01	01
Map Mask	3C5	02	03	03	03	03	03	03	01	03	0F	0F	0F	0F	0F	0F	03	03	03	03
Char Gen Sel	3C5	03	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
Memory Mode	3C5	04	03	03	03	03	02	02	06	03	06	06	00	00	06	06	03	03	03	03
*Values for these modes when the IBM Enhanced Color Display is attached																				
*Values for these modes when greater than 64K Graphics Memory is installed																				

## Sequencer Registers

Register			Mode of Operation																	
Name	Port	Index	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F	10	11
Address Reg	374	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-
Horiz Total	375	00	37	37	70	70	37	37	70	60	37	70	60	5B	60	5B	2D	2D	5B	5B
Hz Disp End	375	01	27	27	4F	4F	27	27	4F	4F	27	4F	4F	4F	4F	4F	27	27	4F	4F
Strt Hz Blk	375	02	2D	2D	5C	5C	2D	2D	59	56	2D	56	56	53	56	53	2B	2B	53	53
End Hz Blk	375	03	37	37	2F	2F	37	37	2D	3A	37	2D	1A	17	3A	37	2D	2D	37	37
Strt Hz Retr	375	04	31	31	5F	5F	30	30	5E	51	30	5E	50	50	50	52	28	28	51	51
End Hz Retr	375	05	15	15	07	07	14	14	06	60	14	06	E0	BA	60	00	6D	6D	5B	5B
Vert Total	375	06	04	04	04	04	04	04	04	70	04	04	70	6C	70	6C	6C	6C	6C	6C
Overflow	375	07	11	11	11	11	11	11	11	1F	11	11	1F	1F	1F	1F	1F	1F	1F	1F
Preset Row SC	375	08	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
Max Scan Line	375	09	07	07	07	07	01	01	01	0D	00	00	00	00	00	00	0D	0D	0D	0D
Cursor Start	375	0A	06	06	06	06	00	00	00	0B	00	00	00	00	00	00	0B	0B	0B	0B
Cursor End	375	0B	07	07	07	07	00	00	00	0C	00	00	00	00	00	00	0C	0C	0C	0C
Strt Addr Hi	375	0C	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-
Strt Addr Lo	375	0D	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-
? = B in monochrome modes      ? = D in color modes																				
*Values for these modes when the IBM Enhanced Color Display is attached																				
‡Values for these modes when greater than 64K Graphics Memory is installed																				

## CRT Controller Registers (1 of 2)



Register			Mode of Operation																			
Name	Port	Index	0	1	2	3	4	5	6	7	D	E	F	10	F*	10*	0*	1*	2*	3*		
Cursor LC Hi	375	0E	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-		
Cursor LC Low	375	0F	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-		
Vrt Retr Strt	375	10	E1	E1	E1	E1	E1	E1	E0	5E	E1	E0	5E	5E	5E	5E	5E	5E	5E	5E		
Light Pen Hi	375	10	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-		
Vert Retr End	375	11	24	24	24	24	24	24	23	2E	24	23	2E	2B	2E	2B	2B	2B	2B	2B		
Light Pen Low	375	11	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-		
Vrt Disp End	375	12	C7	C7	C7	C7	C7	C7	C7	5D	C7	C7	5D	5D	5D	5D	5D	5D	5D	5D		
Offset	375	13	14	14	28	28	14	14	28	28	14	28	14	14	28	28	14	14	28	28		
Underline Loc	375	14	08	08	08	08	00	00	00	0D	00	00	0D	0F	0D	0F	0F	0F	0F	0F		
Strt Vert Blk	375	15	E0	E0	E0	E0	E0	E0	DF	5E	E0	DF	5E	5F	5E	5F	5E	5E	5E	5E		
End Vert Blk	375	16	F0	F0	F0	F0	F0	F0	EF	6E	F0	EF	6E	0A	6E	0A	0A	0A	0A	0A		
Mode Control	375	17	A3	A3	A3	A3	A2	A2	C2	A3	E3	E3	8B	8B	E3	E3	A3	A3	A3	A3		
Line Compare	375	18	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF		
? = B in monochrome modes      ? = D in color modes																						
*Values for these modes when the IBM Enhanced Color Display is attached																						
:Values for these modes when greater than 64K Graphics Memory is installed																						

## CRT Controller Registers (2 of 2)

Register			Mode of Operation																	
Name	Port	Index	0	1	2	3	4	5	6	7	D	E	F	10	F2	102	0*	1*	2*	3*
Grphx I Pos	3CC	-	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
Grphx II Pos	3CA	-	01	01	01	01	01	01	01	01	01	01	01	01	01	01	01	01	01	01
Grphx I II AD	3CE	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-
Set Reset	3CF	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
Enable S/R	3CF	01	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
Color Compare	3CF	02	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
Data Rotate	3CF	03	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
Read Map Sel	3CF	04	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
Mode Register	3CF	05	10	10	10	10	30	30	00	10	00	00	10	10	00	00	10	10	10	10
Miscellaneous	3CF	06	0E	0E	0E	0E	0F	0F	0D	0A	05	05	07	07	05	05	0E	0E	0E	0E
Color No Care	3CF	07	00	00	00	00	00	00	00	00	0F	0F	0F	0F	0F	0F	00	00	00	00
Bit Mask	3CF	08	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF
*Values for these modes when the IBM Enhanced Color Display is attached																				
:Values for these modes when greater than 64K Graphics Memory is installed																				

## Graphics SI Registers

Register			Mode of Operation																		
Name	Port	Index	0	1	2	3	4	5	6	7	D	E	F	10	F*	10*	0*	1*	2*	3*	
Address	3?A	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	
Palette	3C0	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	
Palette	3C0	01	01	01	01	01	13	13	17	08	01	01	08	01	08	01	01	01	01	01	
Palette	3C0	02	02	02	02	02	15	15	17	08	02	02	00	00	00	02	02	02	02	02	
Palette	3C0	03	03	03	03	03	17	17	17	08	03	03	00	00	00	03	03	03	03	03	
Palette	3C0	04	04	04	04	04	02	02	17	08	04	04	18	04	18	04	04	04	04	04	
Palette	3C0	05	05	05	05	05	04	04	17	08	05	05	18	07	18	05	05	05	05	05	
Palette	3C0	06	06	06	06	06	06	06	17	08	06	06	00	00	00	06	14	14	14	14	
Palette	3C0	07	07	07	07	07	07	07	17	08	07	07	00	00	00	07	07	07	07	07	
Palette	3C0	08	10	10	10	10	10	10	17	10	10	10	00	00	00	38	38	38	38	38	
Palette	3C0	09	11	11	11	11	11	11	17	18	11	11	08	01	08	39	39	39	39	39	
Palette	3C0	0A	12	12	12	12	12	12	17	18	12	12	00	00	00	3A	3A	3A	3A	3A	
Palette	3C0	0B	13	13	13	13	13	13	17	18	13	13	00	00	00	3B	3B	3B	3B	3B	
? = B in monochrome modes      ? = D in color modes																					
*Values for these modes when the IBM Enhanced Color Display is attached																					
*Values for these modes when greater than 64K Graphics Memory is installed																					

## Attribute Registers (1 of 2)

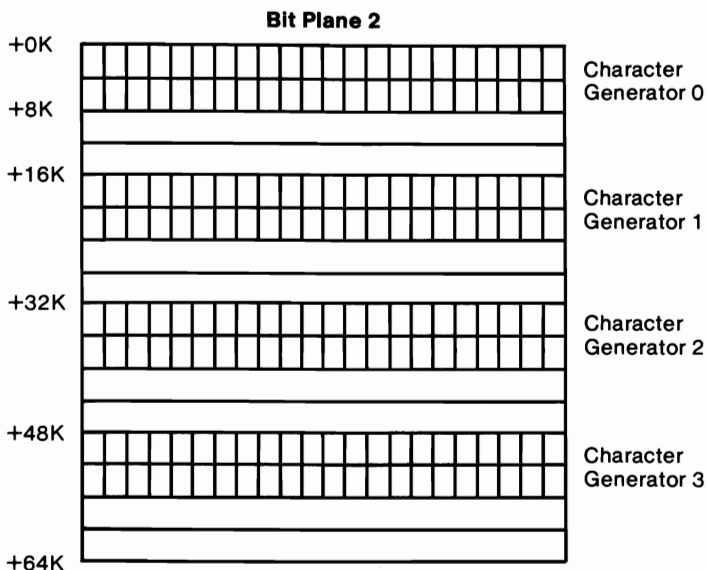
Register			Mode of Operation																							
Name	Port	Index	0	1	2	3	4	5	6	7	D	E	F	10	F*	10*	0*	1*	2*	3*						
Palette	3C0	0C	14	14	14	14	14	14	17	18	14	14	00	04	00	3C	3C	3C	3C	3C						
Palette	3C0	0D	15	15	15	15	15	15	17	18	15	15	18	07	18	3D	3D	3D	3D	3D						
Palette	3C0	0E	16	16	16	16	16	16	17	18	16	16	00	00	00	3E	3E	3E	3E	3E						
Palette	3C0	0F	17	17	17	17	17	17	18	17	17	00	00	00	3F	3F	3F	3F	3F	3F						
Mode Control	3C0	10	08	08	08	08	01	01	01	0E	01	01	0B	0B	0B	01	08	08	08	08						
Overscan	3C0	11	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00						
Color Plane	3C0	12	0F	0F	0F	0F	03	03	01	0F	0F	0F	05	05	05	0F	0F	0F	0F	0F						
Hrz Panning	3C0	13	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00						
*Values for these modes when the IBM Enhanced Color Display is attached																										
*Values for these modes when greater than 64K Graphics Memory is installed																										

## Attribute Registers (2 of 2)

## RAM Loadable Character Generator

The character generator on the adapter is RAM loadable and can support characters up to 32 scan lines high. Two character generators are stored within the BIOS and one is automatically loaded into the RAM by the BIOS when an alphanumeric mode is selected. The Character Map Select Register can be programmed to define the function of bit 3 of the attribute byte to be a character generator switch. This allows the user to select between any two character sets residing in bit plane 2. This effectively gives the user access to 512 characters instead of 256. character tables may be loaded off line. The adapter must have 128K bytes of storage to support this function. Up to four tables can be loaded can be loaded with 256K of graphics memory installed.

The structure of the character tables is described in the following figure. The character generator is in bit plane 2 and must be protected using the map mask function.



The following figure illustrates the structure of each character pattern. If the CRT controller is programmed to generate  $n$  row

scans, then  $n$  bytes must be filled in for each character in the character generator. The example assumes eight row scans per character.

Address	Byte Image								Data
CC * 32 + 0									18H
1									3EH
2									66H
3									66H
4									7EH
5									66H
6									66H
7									66H

CC = Value of the character code. For example, 41H in the case of an ASCII “A”.

## Creating a 512 Character Set

This section describes how to create a 512 character set on the IBM Color Display. Note that only 256 characters can be printed on the printer. This is a special application which the Enhanced Graphics Adapter will support. The 9 by 14 characters will be displayed when attribute bit 3 is a logical 0, and the IBM Color/Graphics Monitor Adapter 8 by 8 characters will be displayed when the attribute bit 3 is a logical 1. This example is for demonstrative purposes only. The assembly language routine for creating 512 characters is given below. Debug 2.0 was used for this example. The starting assembly address is 100 and the character string is stored in location 200. This function requires 128K or more of graphics memory.

```

a100
mov ax,1102      ;load 8x8 character font in character
mov bl,02        ;generator number 2
int 10

mov ax,1103      ;select 512 character operation
mov bl,08        ;if attribute bit 3=1 use 8x8 font
int 10           ;if attribute bit 3=0 use 9x14 font

mov ax,1000      ;set color plane enable to 7H to disable
mov bx,0712      ;attribute bit 3 in the color palette
int 10           ;lookup table

mov ax,1301
mov bx,000F      ;write char. string with attribute bit 3=1
mov cx,003A      ;cx = character string length
mov dx,1600      ;write character on line 22 of display
mov bp,0200      ;pointer to character string location
push cs
pop es
int 10

mov ax,1301
mov bx,000F      ;write char. string with attribute bit 3=0
mov cx,003A      ;cx = character string length
mov dx,1700      ;write character on line 23 of display
mov bp,0200      ;pointer to character string location
push cs
pop es
int 10
int 3

a200 db          "This character string is used to show 512
                  characters"

```

## Creating an 80 by 43 Alphanumeric Mode

The following examples show how to create 80 column by 43 row, both alphanumeric and graphics, images on the IBM Monochrome Display. The BIOS Interface supports an 80 column by *n* row display by using the character generator load routine call. The print screen routine must be revector to

handle the additional character rows on the screen. The assembly language required for both an alphanumeric and a graphics screen is shown below.

```
mov al,7           ;Monochrome alphanumeric mode
int 10             ;video interrupt call
mov ax,1112        ;character generator BIOS routine
mov bl,0           ;load 8 by 8 double dot character font
int 10             ;video interrupt call
mov ax,1200        ;alternate screen routine
move bl,20         ;select alternate print screen routine
int 10             ;video interrupt call
int 3
```

```
mov ax,f           ;Monochrome graphic mode
int 10             ;video interrupt call
mov ax,1123        ;character generator BIOS routine
mov bl,0           ;load 8 by 8 double dot character font
mov dl,2B          ;43 character rows
int 10             ;video interrupt call
mov ax,1200        ;alternate screen routine
mov bl,20          ;alternate print screen routine
int 10             ;video interrupt call
int 3
```

## Vertical Interrupt Feature

The Enhanced Graphics Adapter can be programmed to create an interrupt each time the vertical display refresh time has ended.

An interrupt handler routine must be written by the application to take advantage of this feature. The CRT Vertical interrupt is on IRQ2. The CPU can poll the Enhanced Graphics Adapter Input Status Register 0 (bit 7) to determine whether the CRTC caused the interrupt to occur.

The Vertical Retrace End Register (11H) in the CRT controller contains two bits which are used to control the interrupt circuitry. The remaining bits must be output as per the value in the mode table.



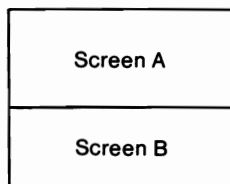
- Bit 5**                      Enable Vertical Interrupt—A logical 0 will enable vertical interrupt.
- Bit 4**                      Clear Vertical Interrupt—A logical 0 will clear a vertical interrupt.

The sequence of events which occur in an interrupt handler are outlined below.

1. Clear IRQ latch and enable driver
2. Enable IRQ latch
3. Wait for vertical interrupt
4. Poll Interrupt Status Register 0 to determine if CRTC has caused the interrupt
5. If CRTC interrupt, then clear IRQ latch; if not, then branch to next interrupt handler.
6. Enable IRQ latch
7. Update Enhanced Graphics Adapter during vertical blanking interval
8. Wait for next vertical interrupt

## Creating a Split Screen

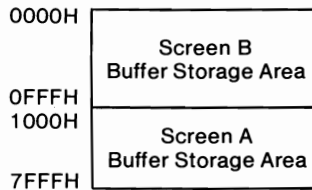
The Enhanced Graphics Adapter hardware supports an alphanumeric mode dual screen display. The top portion of the screen is designated as screen A, and the bottom portion of the screen is designated as screen B as per the following figure.



### Dual Screen Definition

The following figure shows the screen mapping for a system containing a 32K byte alphanumeric storage buffer. Note that the Enhanced Graphics Adapter has a 32K byte storage buffer in alphanumeric mode. Information displayed on screen A is

defined by the start address high and low registers (0CH and 0DH) of the CRTC. Information displayed on screen B always begins at address 0000H.



### **Screen Mapping Within the Display Buffer Address Space**

The Line Compare Register (18H) of the CRT Controller is utilized to perform the split screen function. The CRTC has an internal horizontal scan counter, and logic which compares the horizontal scan counter value to the Line Compare Register value and clears the memory address generator when a compare occurs. The linear address generator then sequentially addresses the display buffer starting at location zero, and each subsequent row address is determined by the 16 bit addition of the start of line latch and the offset register.

Screen B can be smoothly scrolled onto the CRT screen by updating the Line compare in synchronization with the vertical retrace signal. The information on screen B is immune from scrolling operations which utilize the Start Address High and Low registers to scroll through the Screen A address map.

## **Compatibility Issues**

The CRT Controller on the IBM Enhanced Graphics Adapter is a custom design, and is different than the 6845 controller used on the IBM Monochrome Monitor Adapter and the IBM Color/Graphics Monitor Adapter. It should be noted that several CRTC register addresses differ between the adapters. The following figure illustrates the registers which do not map directly across the two controllers.

Register	6485 Function	EGA CRTC Function
02H	Start Horiz. Retrace	Start Horiz. Blanking
03H	End Horiz. Retrace	End Horiz. Blanking
04H	Vertical Total	Start Horiz. Retrace
05H	Vertical Total Adjust	End Horiz. Retrace
06H	Vertical Displayed	Vertical Total
07H	Vertical Sync Position	Overflow
08H	Interlace Mode and Skew	Preset Row Scan

Existing applications which utilize the BIOS interface will generally be compatible with the Enhanced Graphics Adapter.

Horizontal screen centering was required on the IBM Color/Graphics Monitor Adapter in order to center the screen when generating composite video. This was done through the Horizontal Sync Position Register. Since the Enhanced Graphics Adapter does not support a composite video monitor, programs which do screen centering may cause loss of the screen image if centering is attempted.

The Enhanced Graphics Adapter offers a wider variety of displayable monochrome character attributes than the IBM Monochrome Display Adapter. Some attribute values may display differently between the two Adapters. The values listed in the table below, in any combinations with the blink and intensity attributes, will display identically.

Background R G B	Foreground R G B	Function
0 0 0	0 0 0	Non-Display
0 0 0	0 0 1	Underline
0 0 0	1 1 1	White Character/Black Background
1 1 1	0 0 0	Reverse Video

Software which explicitly addresses 3D8 (Mode Select Register) or 3D9 (Color Select Register) on the Color Graphics Monitor Adapter may produce different results on the Enhanced Graphics Adapter. For example, blinking which is disabled by writing to 3D8 on the Color Graphics Adapter will not be disabled on the Enhanced Graphics Adapter.

# Interface

## Feature Connector

The following is a description of the Enhanced Graphics Adapter feature connector. Note that signals coming from the Enhanced Graphics Adapter are labeled “inputs” and the signals coming to the Enhanced Graphics Adapter through the feature connector are labeled “outputs”.

Signal	Description
J2	This pin is connected to auxiliary jack 2 on the rear panel of the adapter.
R'OUT	Secondary red output
ATRS/L	Attribute shift load. This signal controls the serialization of the video information. The shift register parallel loads at the dot clock leading edge when this signal is low.
G OUT	Primary green output
R'	Secondary red input
R	Primary red input
FC1	This signal is input from bit 1 (Feature Control Bit 1) of the Feature Control Register.
FC0	This signal is input from bit 0 (Feature Control Bit 0) of the Feature control Register.
FEAT 0	This signal is output to bit 5 (Feature Code 0) of Input Status Register 0.
B'/V	Secondary blue input/Monochrome video
VIN	Vertical retrace input

**Internal** This signal is output to bit 4 (Disable Internal Video Drivers) of the Miscellaneous Output Register.

**V OUT** Vertical retrace output

**J1** This pin is connected to auxiliary jack 1 on the rear panel of the adapter.

**G'OUT** Secondary green output

**B'OUT** Secondary blue output

**B OUT** Blue output

**G** Green input

**B** Blue input

**R OUT** Red output

**BLANK** This is a composite horizontal and vertical blanking signal from the CRTC.

**FEAT 1** This signal is output to bit 6 (Feature Code 1) of Input Status Register 0.

**G'/I** Secondary green/Intensity input

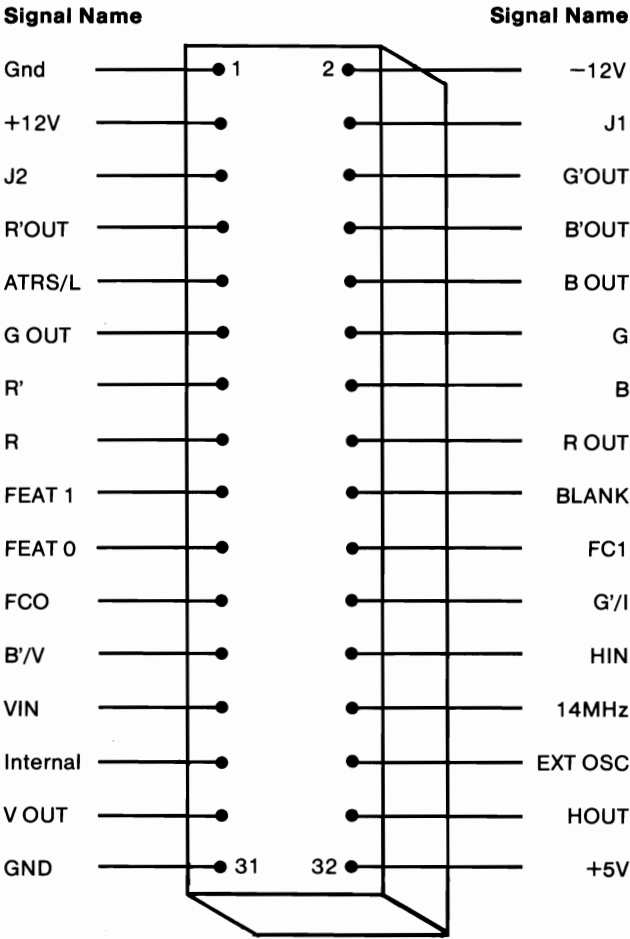
**HIN** Horizontal retrace input from the CRTC

**14MHZ** 14 MHz signal from the system board

**EXT OSC** External dot clock output

**HOUT** Horizontal retrace output

The following figure shows the layout and pin numbering of the feature connector.

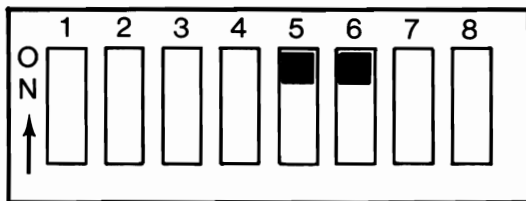


**Feature Connector Diagram**

# Specifications

## System Board Switches

The following figure shows the proper system board DIP switch settings for the IBM Enhanced Graphics Adapter when used with the Personal Computer and the Personal Computer XT. The switch block locations are illustrated in the Technical Reference Manual "System Board Component Diagram". The Personal Computer has two DIP switch blocks; the switch settings shown pertain to DIP Switch Block 1. The Personal Computer XT has one DIP switch block.

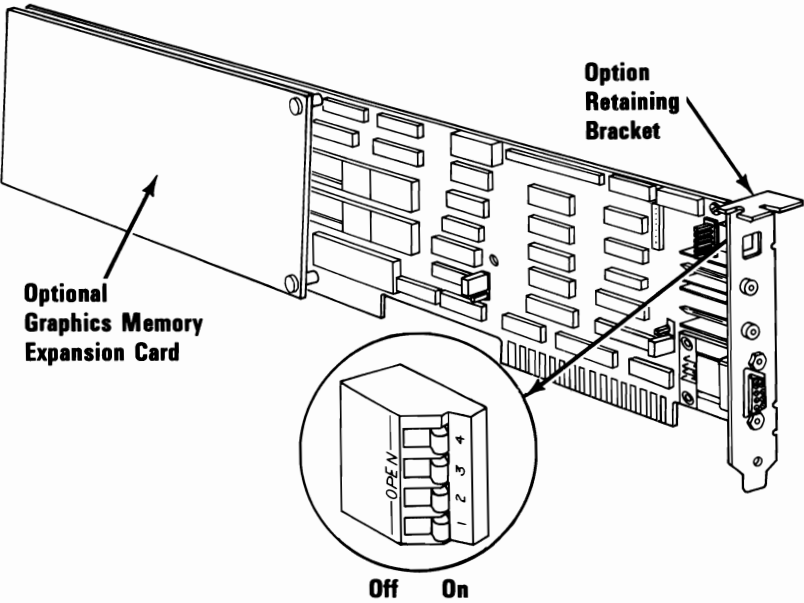


**Switch Block (1)**

**Note:** The DIP switches must be set as shown whenever the IBM Enhanced Graphics Adapter is installed, regardless of display type. This is true even when a second display adapter is installed in the system.

# Configuration Switches

The following diagram shows the location and orientation of the configuration switches on the Enhanced Graphics Adapter.





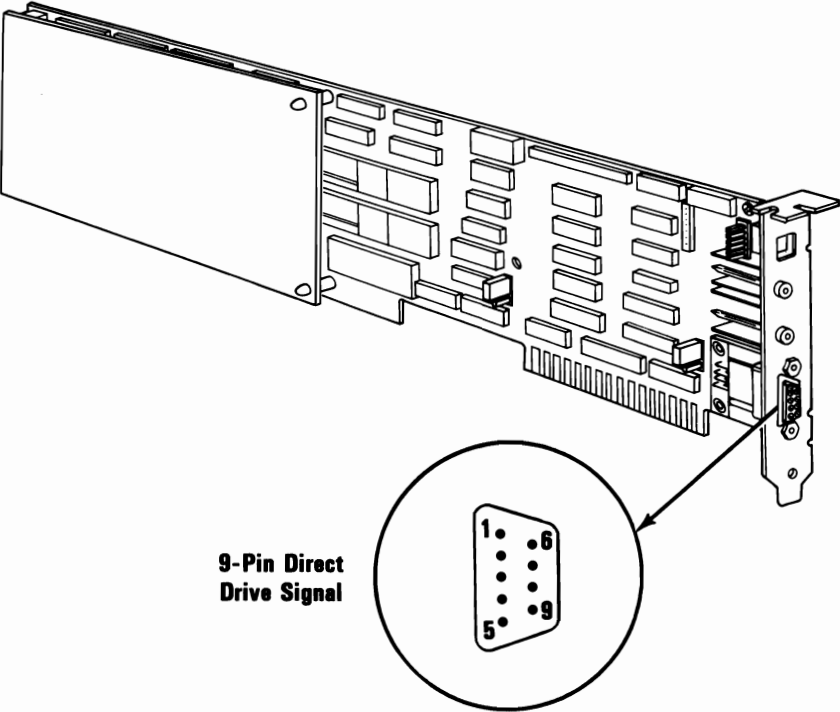
## Configuration Switch Settings

The configuration switches on the Enhanced Graphics Adapter determine the type of display support the adapter provides, as follows:

Switch Settings for Enhanced Graphics Adapter as Primary Display Adapter						
SW1	SW2	SW3	SW4	Configuration		
				Enhanced Adapter	Monochrome Adapter	Color/Graphics Adapter
On	Off	Off	On	Color Display 40x25	Secondary	—
Off	Off	Off	On	Color Display 80x25	Secondary	—
On	On	On	Off	Enhanced Display Emulation Mode	Secondary	—
Off	On	On	Off	Enhanced Display Hi Res Mode	Secondary	—
On	Off	On	Off	Monochrome	—	Secondary 40x25
Off	Off	On	Off	Monochrome	—	Secondary 80x25

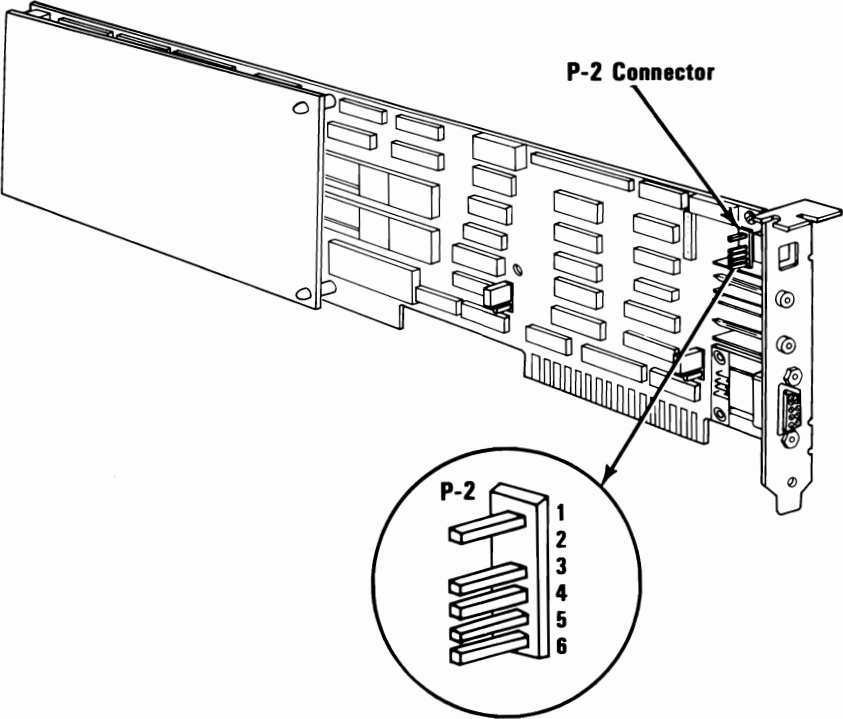
Switch Settings for Enhanced Graphics Adapter as Secondary Display Adapter						
SW1	SW2	SW3	SW4	Configuration		
				Enhanced Adapter	Monochrome Adapter	Color/Graphics Adapter
On	On	On	On	Color Display 40x25	Primary	—
Off	On	On	On	Color Display 80x25	Primary	—
On	Off	On	On	Enhanced Display Emulation Mode	Primary	—
Off	Off	On	On	Enhanced Display Hi Res Mode	Primary	—
On	On	Off	On	Monochrome	—	Primary 40x25
Off	On	Off	On	Monochrome	—	Primary 80x25

# Direct Drive Connector



Signal Name - Description		Pin	Enhanced Graphics Adapter
Direct Drive Display	Ground	1	
	Secondary Red	2	
	Primary Red	3	
	Primary Green	4	
	Primary Blue	5	
	Secondary Green/Intensity	6	
	Secondary Blue/Mono Video	7	
	Horizontal Retrace	8	
	Vertical Retrace	9	

# Light Pen Interface



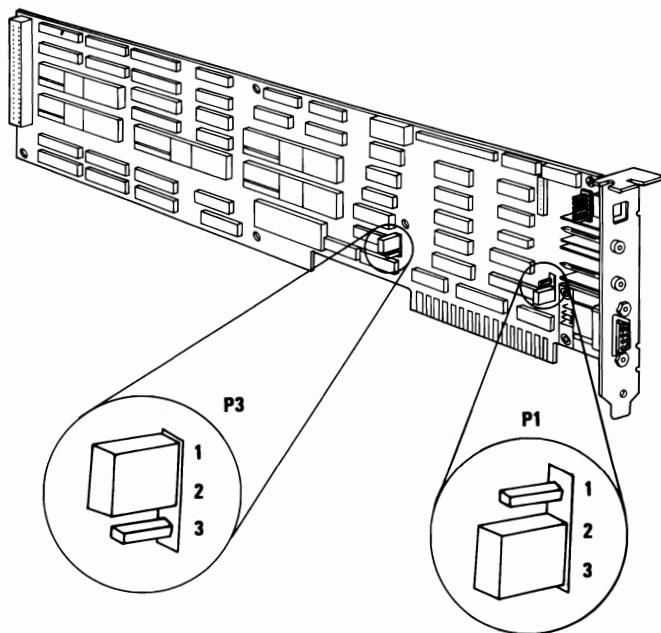
	P-2 Connector	Pin	
Light Pen Attachment	+Light Pen Input	1	Enhanced Graphics Adapter
	Not used	2	
	+Light Pen Switch	3	
	Ground	4	
	+5 Volts	5	
	12 Volts	6	

## Jumper Descriptions

Located on the adapter are two jumpers designated P1 and P3. Jumper P1 changes the function of pin 2 on the direct drive interface. When placed on pins 2 and 3, jumper P1 selects ground as the function of direct drive interface, pin 2. This selection is for displays that support five color outputs, such as the IBM Color Display. When P1 is placed on pins 1 and 2, red prime output is placed on pin 2 of the direct drive interface connector. This supports the IBM Enhanced Color Display, which utilizes six color outputs on the direct drive interface.

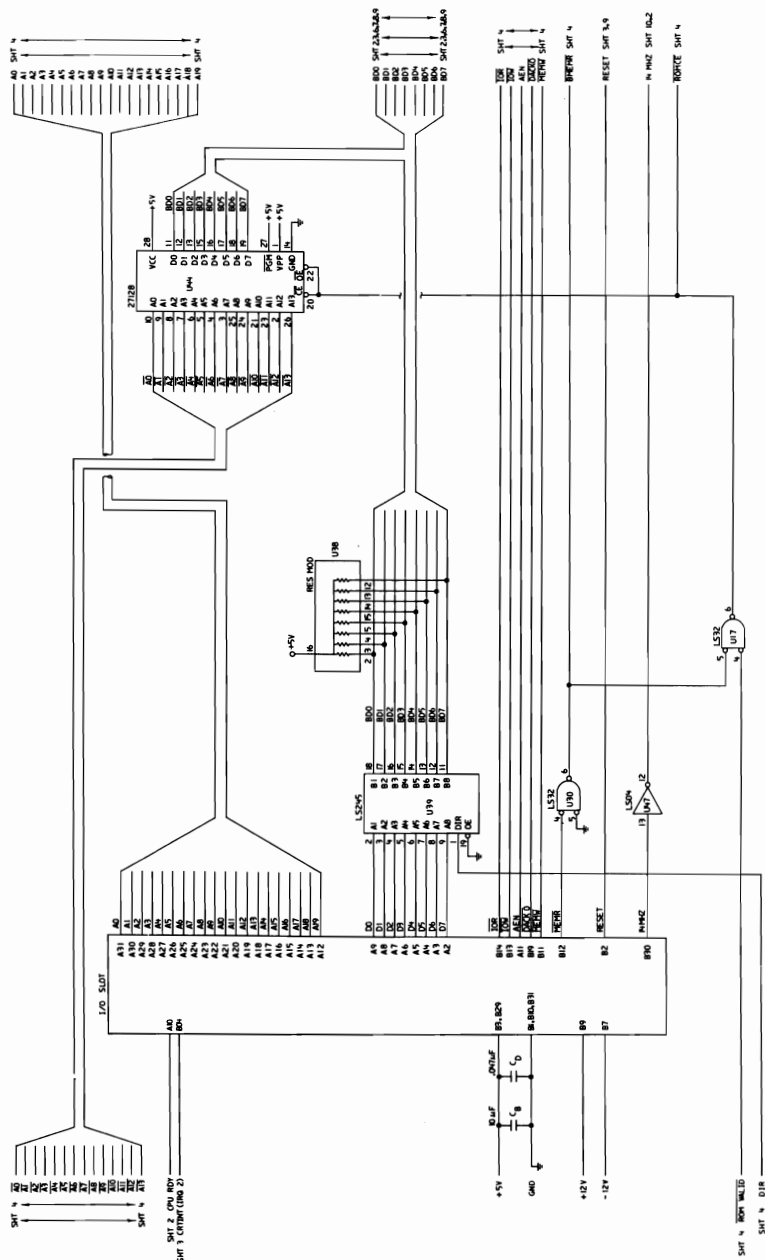
Jumper P3 changes the I/O address port of the Enhanced Graphics Adapter within the system. In its normal position, (pins 1 and 2), all Enhanced Graphics Adapter addresses are in the range 3XX. Moving jumper P3 to pins 2 and 3 changes the addresses to 2XX. Operation of the adapter in the 2XX mode is not supported in BIOS.

The following figure shows the location of the jumpers and numbering of the connectors.

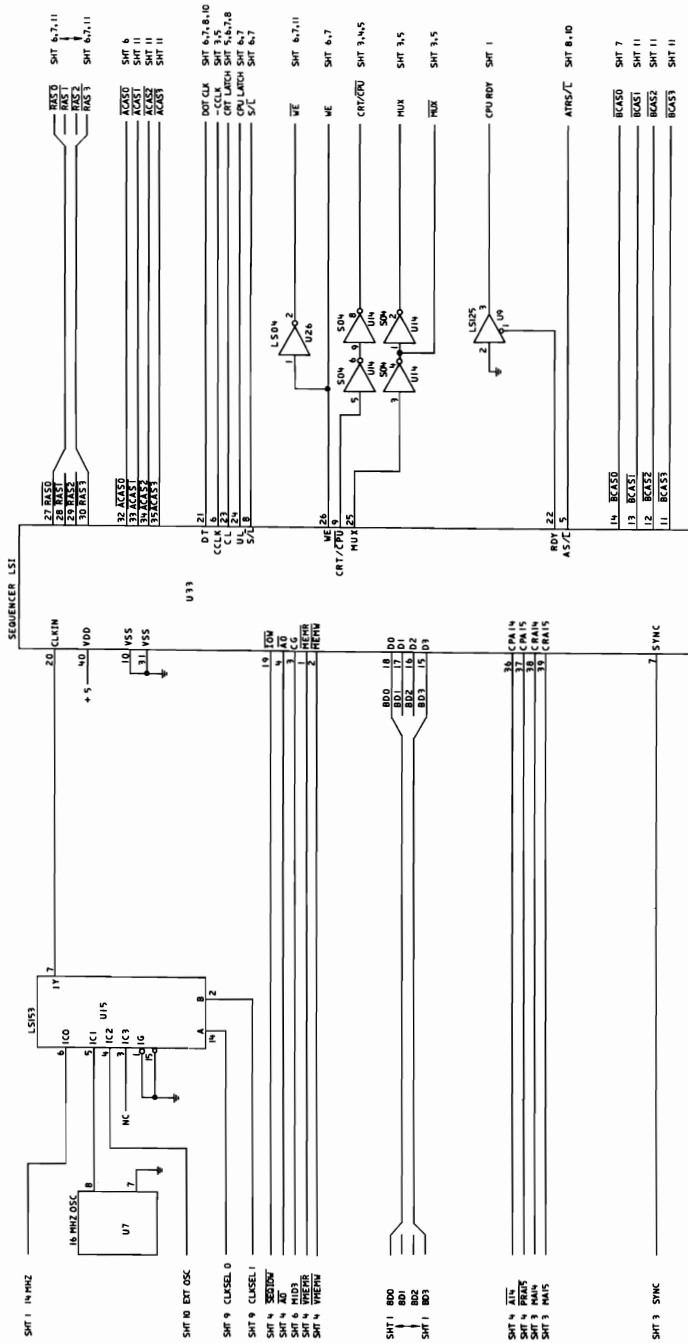




## ENHANCED GRAPHICS ADAPTER



ENHANCED GRAPHICS ADAPTER

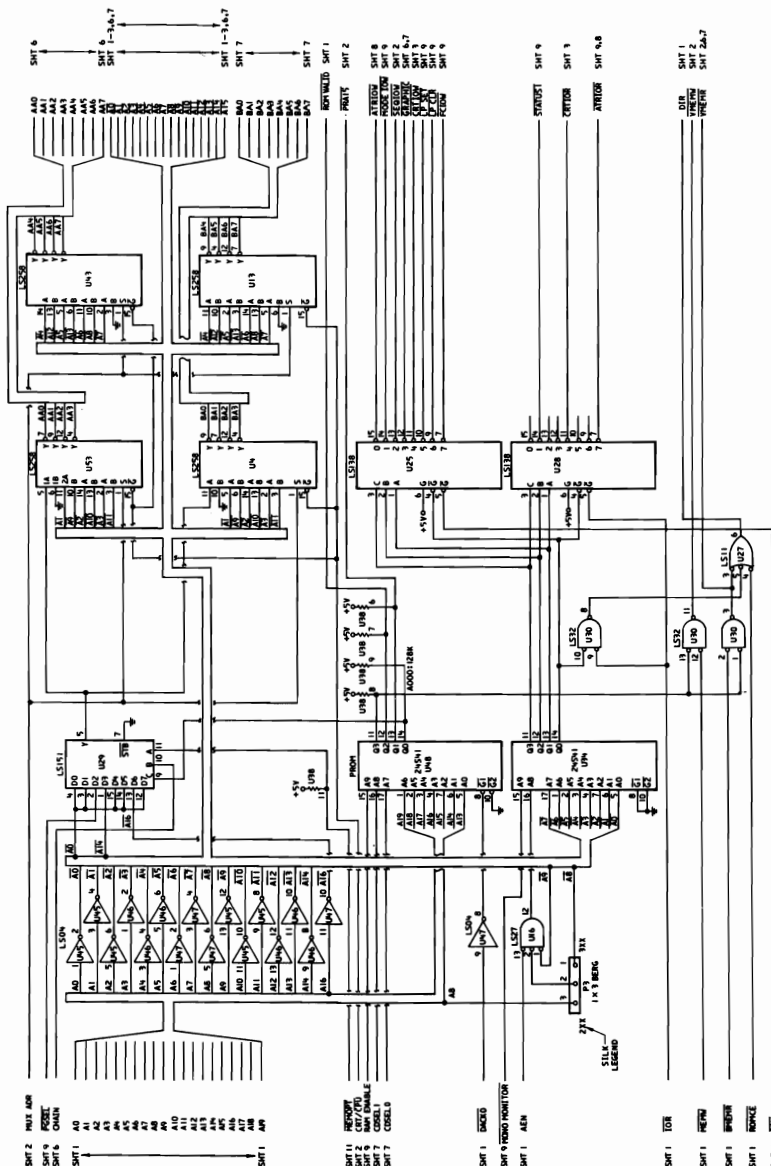




## IBM Enhanced Graphics Adapter 89



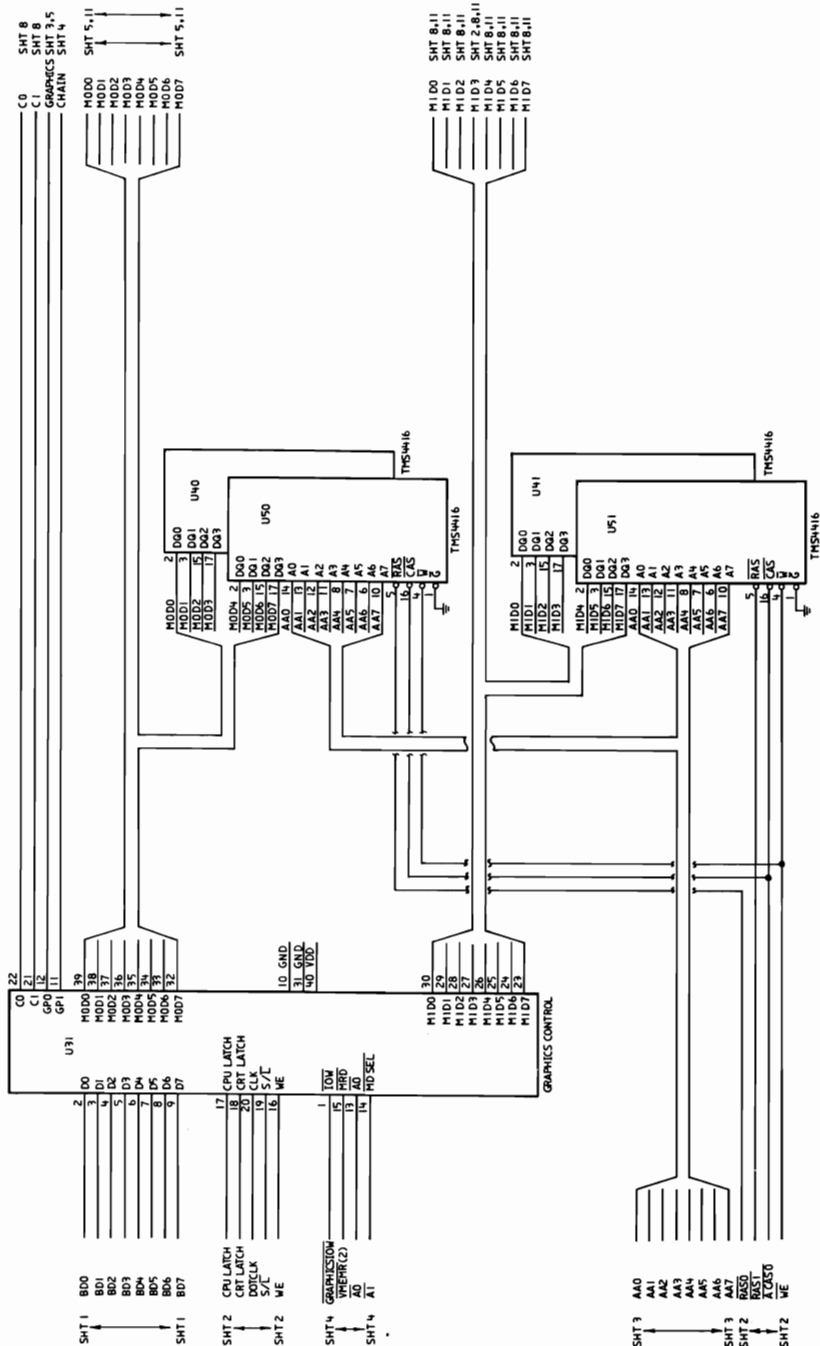
## Enhanced Graphics Adapter Sheet 4 of 11



## IBM Enhanced Graphics Adapter 91

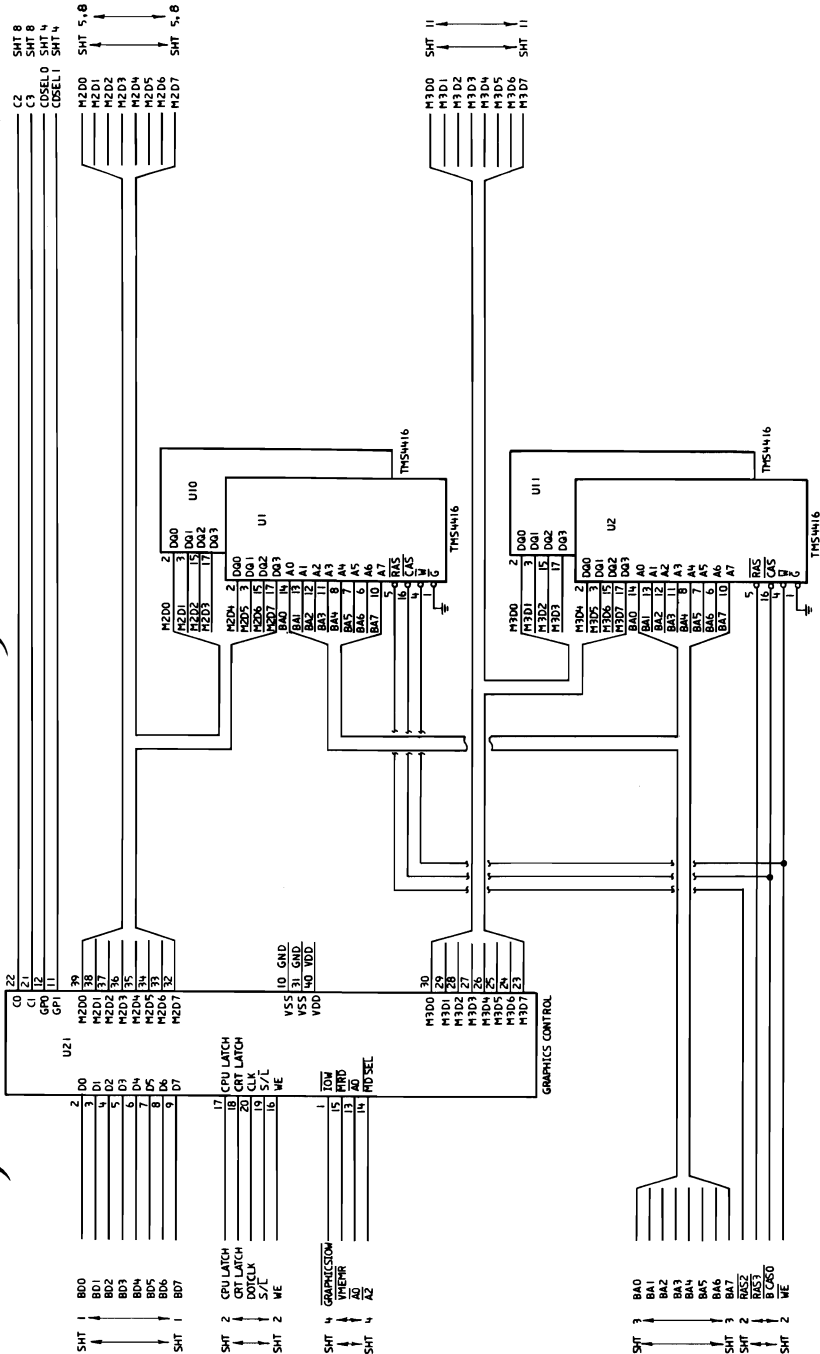


## ENHANCED GRAPHICS ADAPTER

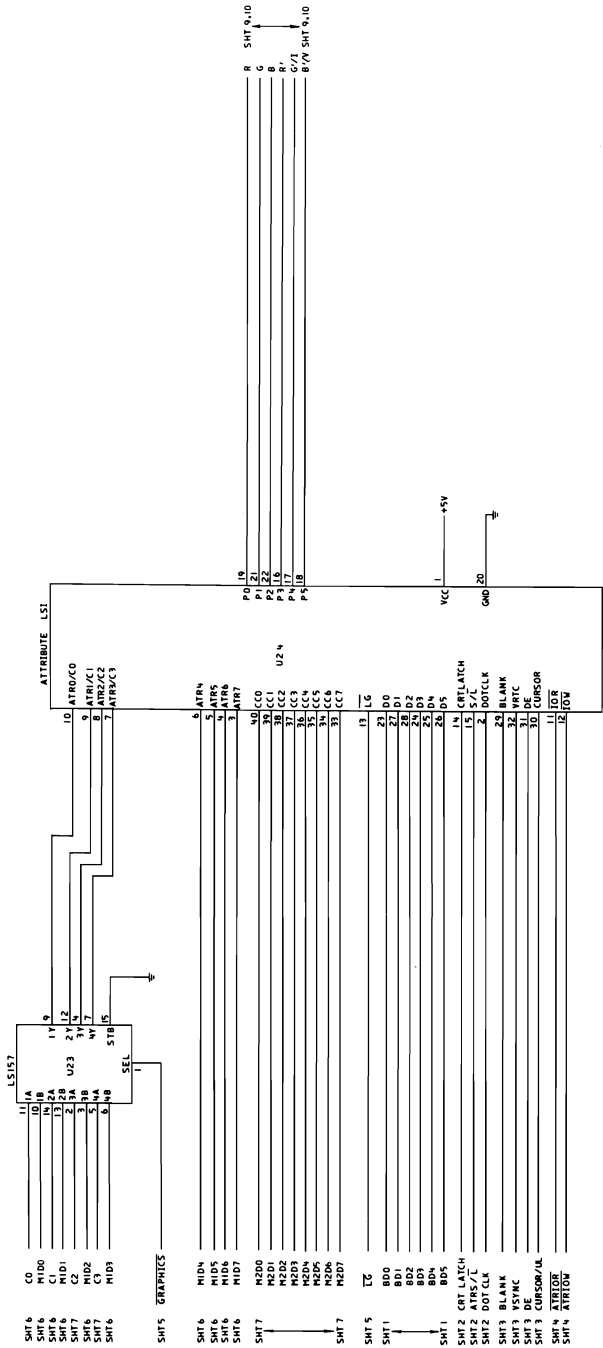


Enhanced Graphics Adapter Sheet 6 of 11

## ENHANCED GRAPHICS ADAPTER



Enhanced Graphics Adapter Sheet 7 of 11



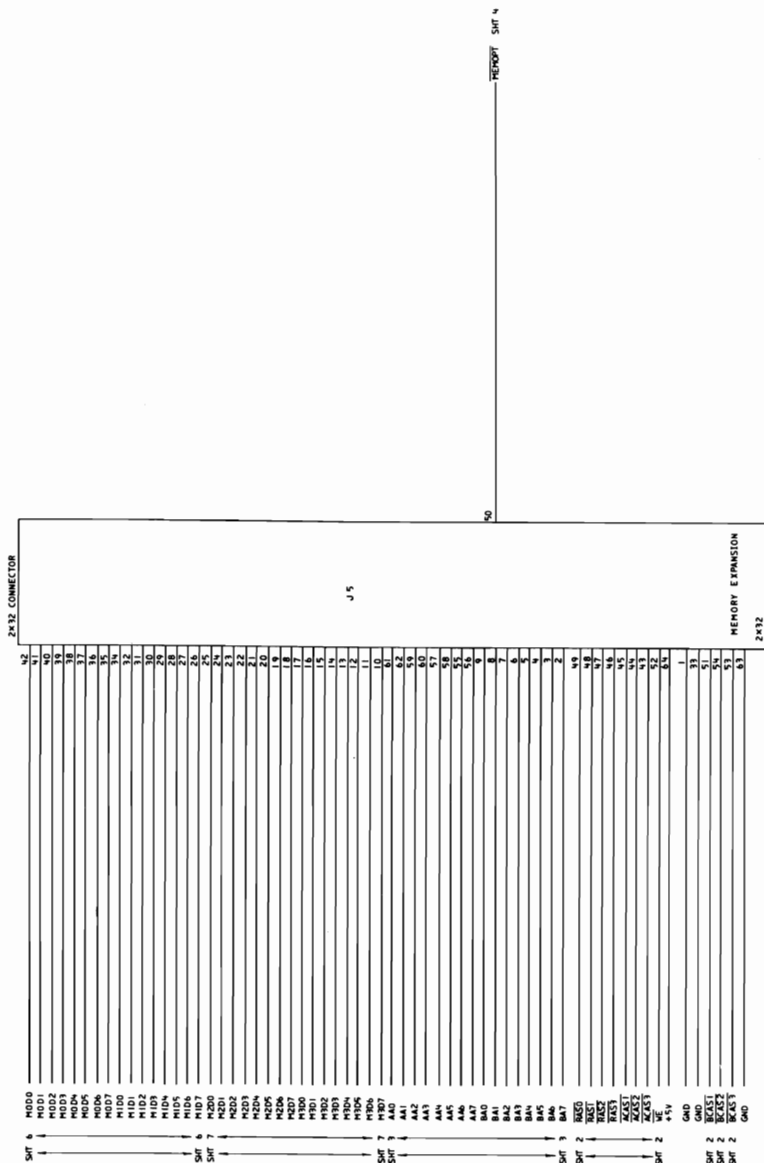
## IBM Enhanced Graphics Adapter 95

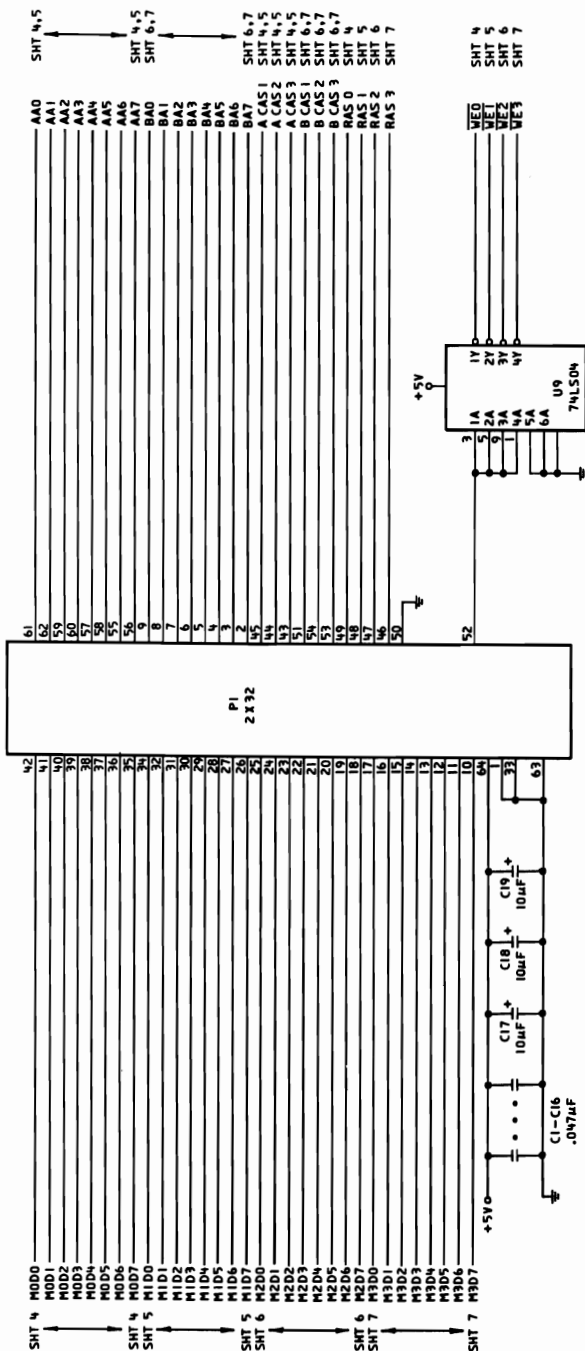


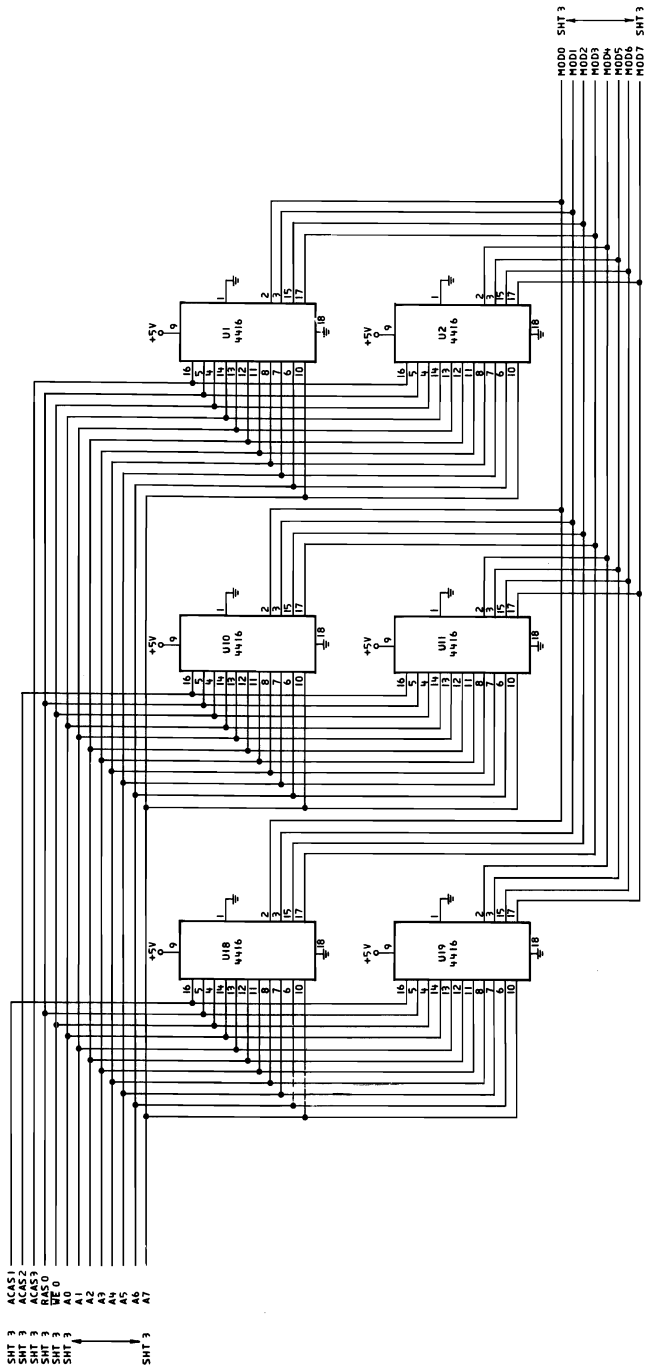
## Enhanced Graphics Adapter Sheet 10 of 11



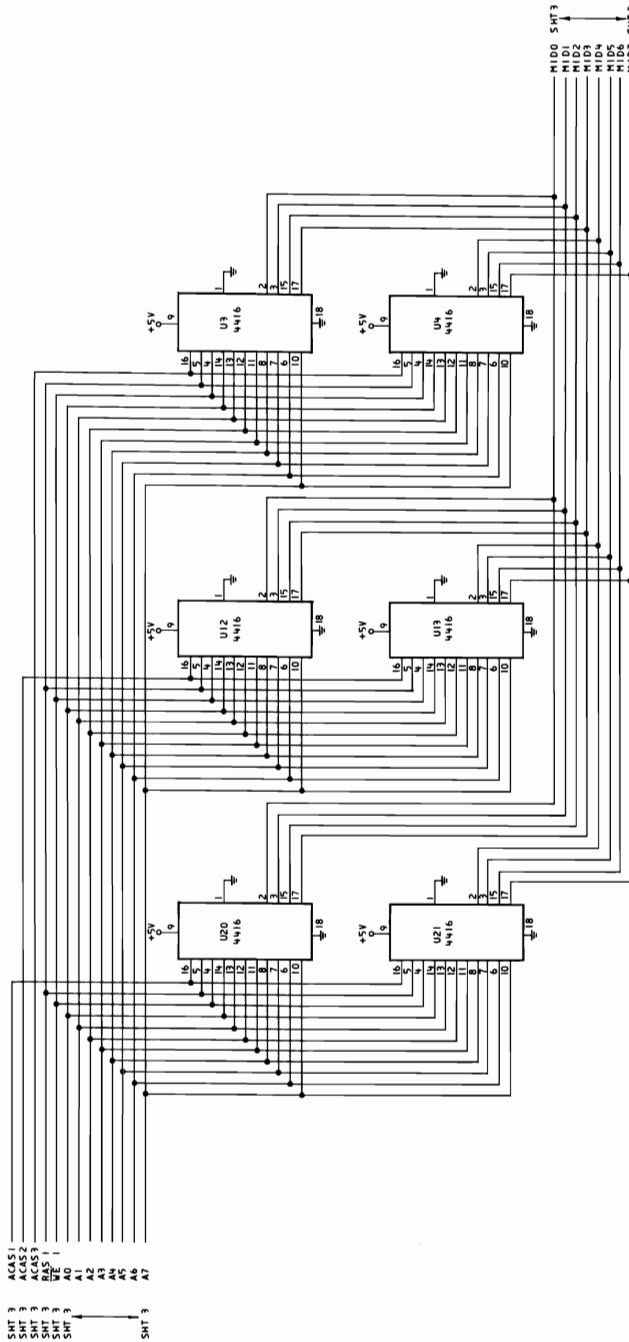








Graphics Memory Expansion Card Sheet 2 of 5



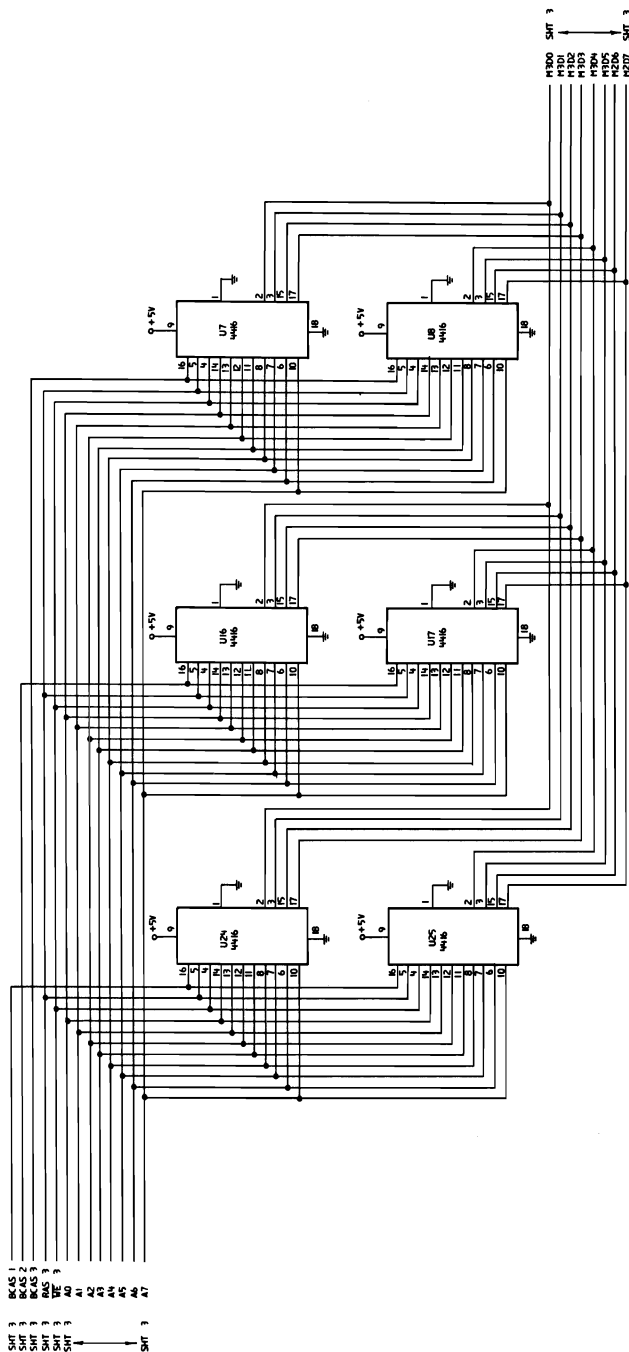
Graphics Memory Expansion Card Sheet 3 of 5

## IBM Enhanced Graphics Adapter 101



## 102 IBM Enhanced Graphics Adapter

**August 2, 1984**



## Graphics Memory Expansion Card Sheet 5 of 5

# BIOS Listing

## Vectors with Special Meanings

### Interrupt Hex 42 - Reserved

When an IBM Enhanced Graphics Adapter is installed, the BIOS routines use interrupt 42 to revector the video pointer.

### Interrupt Hex 43 - IBM Enhanced Graphics Video Parameters

When an IBM Enhanced Graphics Adapter is installed, the BIOS routines use this vector to point to a data region containing the parameters required for the initializing of the IBM Enhanced Graphics Adapter. Note that the format of the table must adhere to the BIOS conventions established in the listing. The power-on routines initialize this vector to point to the parameters contained in the IBM Enhanced Graphics Adapter ROM.

### Interrupt Hex 44 - Graphics Character Table

When an IBM Enhanced Graphics Adapter is installed the BIOS routines use this vector to point to a table of dot patterns that will be used when graphics characters are to be displayed. This table will be used for the first 128 code points in video modes 4, 5, and 6. This table will be used for 256 characters in all additional graphics modes. See the appropriate BIOS interface for additional information on setting and using the graphics character table pointer.

```

1      PAGE 120
2      TITLE  ENHANCED GRAPHICS ADAPTER BIOS
3      EXTRN  CGMN:NEAR, CDDOT:NEAR, INT_1F_1:NEAR, CGMN_FDC:NEAR
4      EXTRN  END_ADDRESS:NEAR
5
6
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117
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119
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121
122
123
124
125
126

```

```

-----
THE BIOS ROUTINES ARE MEANT TO BE ACCESSED THROUGH
SOFTWARE INTERRUPTS ONLY. ANY ADDRESSES PRESENT IN
THE LISTINGS ARE INCLUDED ONLY FOR COMPLETENESS.
NOT FOR REFERENCE. APPLICATIONS WHICH REFERENCE
ABSOLUTE ADDRESSES WITHIN THE CODE SEGMENT
VIOLATE THE STRUCTURE AND DESIGN OF BIOS.
-----

```

LIST

```

INCLUDE VFRONT.INC
SUBTTL VFRONT.INC
PAGE

```

INT 10

VIDEO\_IO

```

THESE ROUTINES PROVIDE THE CRT INTERFACE
THE FOLLOWING FUNCTIONS ARE PROVIDED:
(AH)=0 SET MODE (AL) CONTAINS MODE VALUE

```

AL	AD	TYPE	RES	NOTES	DF-DIM	DISPLAY	MAX PGS
* 0	B8	ALPHA	640X200	40X25	COLOR	- BW	8
* 1	B8	ALPHA	640X200	40X25	COLOR	- BW	8
* 2	B8	ALPHA	640X200	80X25	COLOR	- BW	8
* 3	B8	ALPHA	640X200	80X25	COLOR	- BW	8
* 4	B8	GRPHX	320X200	40X25	COLOR	- BW	1
* 5	B8	GRPHX	320X200	40X25	COLOR	- BW	1
* 6	B8	GRPHX	640X200	80X25	COLOR	- BW	1
* 7	B0	ALPHA	720X350	80X25	MONOCHROME		8
8		RESERVED					
9		RESERVED					
A		RESERVED					
B		RESERVED - INTERNAL USE					
C		RESERVED - INTERNAL USE					
D	A0	GRPHX	320X200	40X25	COLOR		8
E	A0	GRPHX	640X200	80X25	COLOR		4
F	A0	GRPHX	640X350	80X25	MONOCHROME		2
10	A0	GRPHX	640X350	80X25	H1 RES		2

NOTE : HIGH BIT AL SET PREVENTS REGEN BUFFER CLEAR ON MODES RUNNING ON THE COMBO VIDEO ADAPTER

\*\*\* NOTE BW MODES OPERATE SAME AS COLOR MODES, BUT COLOR BURST IS NOT ENABLED

(AH)=1 SET CURSOR TYPE

(CH) = BITS 4-0 = START LINE FOR CURSOR  
 \*\* HARDWARE WILL ALWAYS CAUSE BLINK  
 \*\* SETTING BIT 5 OR 6 WILL CAUSE ERRATIC BLINKING OR NO CURSOR AT ALL

(AH)=2 SET CURSOR POSITION

(DH,DL) = ROW,COLUMN (0,0) IS UPPER LEFT

(BH) = PAGE NUMBER

(AH)=3 READ CURSOR POSITION

(BH) = PAGE NUMBER

(DH,DL) = ROW,COLUMN OF CURRENT CURSOR

(CH,CL) = CURSOR MODE CURRENTLY SET

(AH)=4 READ LIGHT PEN POSITION

ON EXIT:

(AH) = 0

(AH) = 1 -- VALID LIGHT PEN VALUE IN REGISTERS

(DH,DL) = ROW,COLUMN OF CHARACTER LP POSN

(CH) = RASTER LINE (0-199)

(CX) = RASTER LINE (0-NNN) NEW GRAPHICS MODES

(BX) = PIXEL COLUMN (0-319,639)

(AH)=5 SELECT ACTIVE DISPLAY PAGE

(AL) = NEW PAGE VALUE, SEE AH=0 FOR PAGE INFO

(AH)=6 SCROLL ACTIVE PAGE UP

(AL) = NUMBER OF LINES, INPUT LINES BLANKED AT BOTTOM OF WINDOW

AL = 0 MEANS BLANK ENTIRE WINDOW

(CH,CL) = ROW,COLUMN OF UPPER LEFT CORNER OF SCROLL

(DH,DL) = ROW,COLUMN OF LOWER RIGHT CORNER OF SCROLL

(BH) = ATTRIBUTE TO BE USED ON BLANK LINE

(AH)=7 SCROLL ACTIVE PAGE DOWN

(AL) = NUMBER OF LINES, INPUT LINES BLANKED AT TOP OF WINDOW

AL = 0 MEANS BLANK ENTIRE WINDOW

(CH,CL) = ROW,COLUMN OF UPPER LEFT CORNER OF SCROLL

(DH,DL) = ROW,COLUMN OF LOWER RIGHT CORNER OF SCROLL

(BH) = ATTRIBUTE TO BE USED ON BLANK LINE

CHARACTER HANDLING ROUTINES

(AH) = 8 READ ATTRIBUTE/CHARACTER AT CURRENT CURSOR POSITION

(BH) = DISPLAY PAGE

ON EXIT:

(AL) = CHAR READ

(AH) = ATTRIBUTE OF CHARACTER READ (ALPHA MODES ONLY)

(AH) = 9 WRITE ATTRIBUTE/CHARACTER AT CURRENT CURSOR POSITION

(BH) = DISPLAY PAGE

(CX) = COUNT OF CHARACTERS TO WRITE

(AL) = CHAR TO WRITE

(BL) = ATTRIBUTE OF CHARACTER (ALPHA)/COLOR OF CHAR (GRAPHICS)

SEE NOTE ON WRITE DOT FOR BIT 7 OF BL = 1.

(AH) = A WRITE CHARACTER ONLY AT CURRENT CURSOR POSITION

(BH) = DISPLAY PAGE

(CX) = COUNT OF CHARACTERS TO WRITE

(AL) = CHAR TO WRITE

FOR READ/WRITE CHARACTER INTERFACE WHILE IN GRAPHICS MODE, THE CHARACTERS ARE FORMED FROM A CHARACTER GENERATOR IMAGE MAINTAINED IN THE SYSTEM ROM. ONLY THE 1ST 128 CHARS ARE CONTAINED THERE. TO READ/WRITE THE SECOND 128 CHARS, THE USER MUST INITIALIZE THE POINTER AT INTERRUPT\_1FH (LOCATION 0007CH) TO POINT TO THE 1K BYTE TABLE CONTAINING THE CODE POINTS FOR THE SECOND 128 CHARS (128-255).

FOR THE NEW GRAPHICS MODES 256 GRAPHICS CHARS ARE SUPPLIED IN THE SYSTEM ROM.

FOR WRITE CHARACTER INTERFACE IN GRAPHICS MODE, THE REPLICATION FACTOR CONTAINED IN (CX) ON ENTRY WILL PRODUCE VALID RESULTS ONLY FOR CHARACTERS CONTAINED ON THE SAME ROW. CONTINUATION TO SUCCEEDING LINES WILL NOT PRODUCE CORRECTLY.



```

127 C :
128 C :
129 C :
130 C :
131 C :
132 C :
133 C :
134 C :
135 C :
136 C :
137 C :
138 C :
139 C :
140 C :
141 C :
142 C :
143 C :
144 C :
145 C :
146 C :
147 C :
148 C :
149 C :
150 C :
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153 C :
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222 C :
223 C :
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226 C :
227 C :
228 C :
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237 C :
238 C :
239 C :
240 C :
241 C :
242 C :
243 C :
244 C :
245 C :
246 C :
247 C :
248 C :
249 C :
250 C :
251 C :
252 C :

```

GRAPHICS INTERFACE  
 (AH) = B SET COLOR PALETTE  
 FOR USE IN COMPATIBILITY MODES  
 (BH) = PALETTE COLOR ID BEING SET (0-127)  
 (BL) = COLOR VALUE TO BE USED WITH THAT COLOR ID  
 NOTE: FOR THE CURRENT COLOR CARD, THIS ENTRY POINT  
 HAS MEANING ONLY FOR 320X200 GRAPHICS.  
 COLOR ID = 0 SELECTS THE BACKGROUND COLOR (0-15);  
 COLOR ID = 1 SELECTS THE PALETTE TO BE USED:  
 0 = GREEN(1)/RED(2)/BROWN(3)  
 1 = CYAN(11)/MAGENTA(2)/WHITE(3)  
 IN 40X25 OR 80X25 ALPHA MODES, THE VALUE SET  
 FOR PALETTE COLOR 0 INDICATES THE  
 BORDER COLOR TO BE USED (VALUES 0-31,  
 WHERE 16-31 SELECT THE HIGH INTENSITY  
 BACKGROUND SET).

(AH) = C WRITE DOT  
 (BH) = PAGE  
 (DX) = ROW NUMBER  
 (CX) = COLUMN NUMBER  
 (AL) = COLOR VALUE  
 IF BIT 7 OF AL = 1, THEN THE COLOR VALUE IS  
 EXCLUSIVE OR'D WITH THE CURRENT CONTENTS OF  
 THE DOT

(AH) = D READ DOT  
 (BH) = PAGE  
 (DX) = ROW NUMBER  
 (CX) = COLUMN NUMBER  
 (AL) RETURNS THE DOT READ

ASCII TELETYPE ROUTINE FOR OUTPUT

(AH) = E WRITE TELETYPE TO ACTIVE PAGE  
 (AL) = CHAR TO WRITE  
 (BL) = FOREGROUND COLOR IN GRAPHICS MODE  
 NOTE -- SCREEN WIDTH IS CONTROLLED BY PREVIOUS MODE SET

(AH) = F CURRENT VIDEO STATE  
 RETURNS THE CURRENT VIDEO STATE  
 (AL) = MODE CURRENTLY SET (SEE AH=0 FOR EXPLANATION)  
 (AH) = NUMBER OF CHARACTER COLUMNS ON SCREEN  
 (BH) = CURRENT ACTIVE DISPLAY PAGE

(AH) = 10 SET PALETTE REGISTERS  
 (AL) = 0 SET INDIVIDUAL PALETTE REGISTER  
 BL = PALETTE REGISTER TO BE SET  
 BH = VALUE TO SET  
 AL = 1 SET OVERSCAN REGISTER  
 BH = VALUE TO SET  
 AL = 2 SET ALL PALETTE REGISTERS AND OVERSCAN  
 ES:DX POINTS TO A 17 BYTE TABLE  
 BYTES 0 - 15 ARE THE PALETTE VALUES, RESPECTIVELY  
 BYTE 16 IS THE OVERSCAN VALUE  
 AL = 3 TOGGLE INTENSIFY/BLINKING BIT  
 BL - 0 ENABLE INTENSIFY  
 BL - 1 ENABLE BLINKING

(AH) = 11 CHARACTER GENERATOR ROUTINE  
 NOTE : THIS CALL WILL INITIATE A MODE SET, COMPLETELY  
 RESETTING THE VIDEO ENVIRONMENT BUT MAINTAINING  
 THE REGEN BUFFER.

AL = 00 USER ALPHA LOAD  
 ES:BP - POINTER TO USER TABLE  
 CX - COUNT TO STORE  
 DX - CHARACTER OFFSET INTO TABLE  
 BL - BLOCK TO LOAD  
 BH - NUMBER OF BYTES PER CHARACTER  
 AL = 01 ROM MONOCHROME SET  
 BL - BLOCK TO LOAD  
 AL = 02 ROM 8X8 DOUBLE DOT  
 BL - BLOCK TO LOAD  
 AL = 03 SET BLOCK SPECIFIER  
 BL - CHAR GEN BLOCK SPECIFIER  
 D3-D2 ATTR BIT 3 ONE, CHAR GEN 0-3  
 D1-D0 ATTR BIT 3 ZERO, CHAR GEN 0-3  
 NOTE : WHEN USING AL = 03 A FUNCTION CALL  
 AX = 1000H  
 BX = 0712H  
 IS RECOMMENDED TO SET THE COLOR PLANES  
 RESULTING IN 512 CHARACTERS AND EIGHT  
 CONSISTENT COLORS.

NOTE : THE FOLLOWING INTERFACE (AL=1X) IS SIMILAR IN FUNCTION  
 TO (AL=0X) EXCEPT THAT :  
 - PAGE ZERO MUST BE ACTIVE  
 - POINTS (BYTES/CHAR) WILL BE RECALCULATED  
 - ROWS WILL BE CALCULATED FROM THE FOLLOWING:  

$$\text{INT}((200 \text{ OR } 350) / \text{POINTS}) - 1$$
 - CRT\_LEN WILL BE CALCULATED FROM :  

$$(\text{ROWS} + 1) * \text{CRT COLS} * 2$$
 - THE CRTIC WILL BE REPROGRAMMED AS FOLLOWS :  

$$\text{RO9H} = \text{POINTS} - \text{MAX SCAN LINE}$$

$$\text{RO9H} = \text{POINTS} - \text{RO9H DONE ONLY IN MODE 7}$$

$$\text{ROAH} = \text{POINTS} - 2 \quad \text{CURSOR START}$$

$$\text{ROBH} = 0 \quad \text{CURSOR END}$$

$$\text{R12H} = (\text{VERT DISP END} - \text{VERT DISP START}) / \text{POINTS} - 1$$

$$\text{R14H} = (\text{POINTS} - 1) \quad \text{UNDERLINE LOC}$$

THE ABOVE REGISTER CALCULATIONS MUST BE CLOSE TO THE  
 ORIGINAL TABLE VALUES OR UNDETERMINED RESULTS WILL  
 OCCUR.

NOTE : THE FOLLOWING INTERFACE IS DESIGNED TO BE  
 CALLED ONLY IMMEDIATELY AFTER A MODE SET HAS  
 BEEN ISSUED. FAILURE TO ADHERE TO THIS PRACTICE  
 MAY CAUSE UNDETERMINED RESULTS.

AL = 10 USER ALPHA LOAD  
 ES:BP - POINTER TO USER TABLE  
 CX - COUNT TO STORE  
 DX - CHARACTER OFFSET INTO TABLE  
 BL - BLOCK TO LOAD  
 BH - NUMBER OF BYTES PER CHARACTER  
 AL = 11 ROM MONOCHROME SET  
 BL - BLOCK TO LOAD  
 AL = 12 ROM 8X8 DOUBLE DOT  
 BL - BLOCK TO LOAD

```

253      C ;
254      C :
255      C :
256      C :
257      C :
258      C :
259      C :
260      C :
261      C :
262      C :
263      C :
264      C :
265      C :
266      C :
267      C :
268      C :
269      C :
270      C :
271      C :
272      C :
273      C :
274      C :
275      C :
276      C :
277      C :
278      C :
279      C :
280      C :
281      C :
282      C :
283      C :
284      C :
285      C :
286      C :
287      C :
288      C :
289      C :
290      C :
291      C :
292      C :
293      C :
294      C :
295      C :
296      C :
297      C :
298      C :
299      C :
300      C :
301      C :
302      C :
303      C :
304      C :
305      C :
306      C :
307      C :
308      C :
309      C :
310      C :
311      C :
312      C :
313      C :
314      C :
315      C :
316      C :
317      C :
318      C :
319      C :
320      C :
321      C :
322      C :
323      C :
324      C :
325      C :
326      C :
327      C :
328      C :
329      C :
330      C :
331      C :
332      C :
333      C :
334      C :
335      C :
336      C :
337      C :
338      C :
339      C :
340      C :
341      C :
342      C :
343      C :
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348      C :
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354      C :
355      C :
356      C :
357      C :
358      C :
359      C :
360      C :
361      C :
362      C :
363      C :
364      C :
365      C :
366      C :
367      C :
368      C :
369      C :
370      C :
371      C :
372      C :
373      C :
374      C :
375      C :
376      C :
377      C :
378      C :

```

NOTE : THE FOLLOWING INTERFACE IS DESIGNED TO BE CALLED ONLY IMMEDIATELY AFTER A MODE SET HAS BEEN ISSUED. FAILURE TO ADHERE TO THIS PRACTICE MAY CAUSE UNDETERMINED RESULTS.

AL = 20 USER GRAPHICS CHARS INT 01FH (8X8)  
ES:BP - POINTER TO USER TABLE

AL = 21 USER GRAPHICS CHARS  
ES:BP - POINTER TO USER TABLE  
CX - POINTS (BYTES PER CHARACTER)  
BL - ROW SPECIFIER

BL = 0 USER DL - ROWS  
BL = 1 14 (0EH)  
BL = 2 25 (19H)  
BL = 3 43 (2BH)

AL = 22 ROM 8 X 14 SET  
BL - ROW SPECIFIER

AL = 23 ROM 8 X 8 DOUBLE DOT  
BL - ROW SPECIFIER

AL = 30 INFORMATION  
CX - POINTS  
DL - ROWS

BH = 0 RETURN CURRENT INT 1FH PTR  
ES:BP - PTR TO TABLE

BH = 1 RETURN CURRENT INT 44H PTR  
ES:BP - PTR TO TABLE

BH = 2 RETURN ROM 8 X 14 PTR  
ES:BP - PTR TO TABLE

BH = 3 RETURN ROM DOUBLE DOT PTR  
ES:BP - PTR TO TABLE

BH = 4 RETURN ROM DOUBLE DOT PTR (TOP)  
ES:BP - PTR TO TABLE

BH = 5 RETURN ROM ALPHA ALTERNATE 9X14  
ES:BP - PTR TO TABLE

(AH) = 12 ALTERNATE SELECT

BL = 10 RETURN EGA INFORMATION  
BH = 0 - COLOR MODE IN EFFECT <3><D><D>  
1 - MONOC MODE IN EFFECT <3><B><D>

BL = MEMORY VALUE  
0 0 - 064K 0 1 - 128K  
1 0 - 192K 1 1 - 256K

CH = FEATURE BITS  
CL = SWITCH SETTING

BL = 20 SELECT ALTERNATE PRINT SCREEN ROUTINE

(AH) = 13 WRITE STRING  
ES:BP - POINTER TO STRING TO BE WRITTEN  
CX - CHARACTER ONLY COUNT  
DX - POSITION TO BEGIN STRING, IN CURSOR TERMS  
BH - PAGE NUMBER

AL = 0  
BL - ATTRIBUTE  
STRING - (CHAR, CHAR, CHAR, ...)  
CURSOR NOT MOVED

AL = 1  
BL - ATTRIBUTE  
STRING - (CHAR, CHAR, CHAR, ...)  
CURSOR IS MOVED

AL = 2  
STRING - (CHAR, ATTR, CHAR, ATTR, ...)  
CURSOR NOT MOVED

AL = 3  
STRING - (CHAR, ATTR, CHAR, ATTR, ...)  
CURSOR IS MOVED

NOTE : CHAR RET, LINE FEED, BACKSPACE, AND BELL ARE TREATED AS COMMANDS RATHER THAN PRINTABLE CHARACTERS.

---

```

SRLOAD MACRO SEGREG,VALUE
        IFNB <VALUE>
            IFIDN <VALUE>,<D>
                SUB DX,DX
            ELSE
                MOV DX,VALUE
            ENDIF
        MOV SEGREG,DX
        ENDM

```

```

;----- LOW MEMORY SEGMENT
ABS0 SEGMENT AT 0
    ORG 005H*4 ; PRINT SCREEN VECTOR
INT5_PTR LABEL DWORD
    ORG 010H*4 ; VIDEO I/O VECTOR
VIDEO LABEL DWORD
    ORG 01FH*4 ; GRAPHIC CHARS 128-255
EXT_PTR LABEL DWORD
    ORG 042H*4 ; REVECTORED 10H*4
PLANAR_VIDEO LABEL DWORD
    ORG 043H*4 ; GRAPHIC CHARS 0-255
GRX_SET LABEL DWORD
    ORG 0410H
EQUIP_LOW LABEL BYTE
EQUIP_FLAG DW ?
    ORG 0410H
    DW ?
;----- REUSE RAM FROM PLANAR BIOS
    ORG 449H
CRT_MODE DB ?
    ORG 449H
CRT_COLS DW ?
    ORG 44A
CRT_LEN DW ?
    ORG 44C
CRT_START DW ?
    ORG 44E
CURSOR_POSN DW 8 DUP(?)
    ORG 44F
CURSOR_MODE DW ?
    ORG 450
ACTIVE_PAGE DB ?

```

```

0000
0014
001A
0014
0040
0040
007C
007C
0108
0108
010C
010C
0410
0410
0410 ????
0449
0449 ??
044A ????
044C ????
044E ????
0450 0B [ ???? ]
0460 ????
0462 ??

```

```

0463 77??      379 C ADDR_6845 DW ?
0465 77      380 C CRT_MODE_SET DB ?
0466 77      381 C CRT_PALETTE DB ?
          382 C
          383 C ORG 0472H
0472 77??      384 C RESET_FLAG DW ?
0484 77      385 C ROWS DB ? ; ROWS ON THE SCREEN
0484 77      386 C POINTS DW ? ; BYTES PER CHARACTER
0485 77??      387 C
          388 C
          389 C INFO DB ?
0487 77      390 C
          391 C ; INFO
          392 C -D7 - HIGH BIT OF MODE SET. CLEAR/NOT CLEAR REGEN
          393 C D6 - MEMORY D6 D5 = 0 0 - 064K 0 1 - 128K
          394 C D5 - MEMORY 1 0 - 192K 1 1 - 256K
          395 C D4 - RESERVED
          396 C D3 - EGA ACTIVE MONITOR (0), EGA NOT ACTIVE (1)
          397 C D2 - WAIT FOR DISPLAY ENABLE (1)
          398 C D1 - EGA HAS A MONOCHROME ATTACHED (1)
          399 C D0 - SET C_TYPE EMULATE ACTIVE (0)
          400 C
0488 77      401 C INFO_3 DB ?
          402 C
          403 C ; INFO_3
          404 C D7-D4 FEATURE BITS
          405 C D3-D0 SWITCHES
          406 C
04A8      407 C ORG 04ABH
04A8      408 C SAVE_PTR LABEL DWORD
          409 C
          410 C ----- SAVE_PTR
          411 C
          412 C SAVE_PTR IS A POINTER TO A TABLE AS DESCRIBED AS FOLLOWS :
          413 C
          414 C DWORD_1 VIDEO PARAMETER TABLE POINTER
          415 C DWORD_2 DYNAMIC SAVE AREA POINTER
          416 C DWORD_3 ALPHA MODE AUXILIARY CHAR GEN POINTER
          417 C DWORD_4 GRAPHICS MODE AUXILIARY CHAR GEN POINTER
          418 C DWORD_5 RESERVED
          419 C DWORD_6 RESERVED
          420 C DWORD_7 RESERVED
          421 C
          422 C DWORD_1 PARAMETER TABLE POINTER
          423 C INITIALIZED TO 0000:0000, THIS VALUE IS OPTIONAL.
          424 C THIS VALUE MUST EXIST.
          425 C
          426 C DWORD_2 PARAMETER SAVE AREA POINTER
          427 C INITIALIZED TO 0000:0000, THIS VALUE IS OPTIONAL.
          428 C WHEN NON-ZERO, THIS POINTER WILL BE USED AS POINTER
          429 C TO A RAM AREA WHERE CERTAIN DYNAMIC VALUES ARE TO
          430 C BE SAVED. WHEN IN EGA OPERATION THIS RAM AREA WILL
          431 C HOLD THE 16 EGA PALETTE REGISTER VALUES PLUS
          432 C THE OVERSCAN VALUE IN BYTES 0-16D RESPECTIVELY.
          433 C AT LEAST 256 BYTES MUST BE ALLOCATED FOR THIS AREA.
          434 C
          435 C DWORD_3 ALPHA MODE AUXILIARY POINTER
          436 C INITIALIZED TO 0000:0000, THIS VALUE IS OPTIONAL.
          437 C WHEN NON-ZERO, THIS POINTER IS USED AS A POINTER
          438 C TO A TABLES DESCRIBED AS FOLLOWS :
          439 C
          440 C BYTE BYTES/CHARACTER
          441 C BYTE BLOCK TO LOAD, SHOULD BE ZERO FOR NORMAL
          442 C OPERATION
          443 C WORD COUNT TO STORE, SHOULD BE 256D FOR NORMAL
          444 C OPERATION
          445 C WORD CHARACTER OFFSET, SHOULD BE ZERO FOR NORMAL
          446 C OPERATION
          447 C DWORD POINTER TO A FONT TABLE
          448 C BYTE DISPLAYABLE ROWS
          449 C IF 'FF' THE MAXIMUM CALCULATED VALUE WILL BE
          450 C USED, ELSE THIS VALUE WILL BE USED
          451 C BYTE CONSECUTIVE BYTES OF MODE VALUES FOR WHICH
          452 C THIS FONT DESCRIPTION IS TO BE USED.
          453 C THE END OF THIS STREAM IS INDICATED BY A
          454 C BYTE CODE OF 'FF'
          455 C
          456 C NOTE : USE OF THIS POINTER MAY CAUSE UNEXPECTED
          457 C CURSOR TYPE OPERATION. FOR AN EXPLANATION
          458 C OF CURSOR TYPE SEE AH = 01 IN THE INTERFACE
          459 C SECTION.
          460 C
          461 C DWORD_4 GRAPHICS MODE AUXILIARY POINTER
          462 C INITIALIZED TO 0000:0000, THIS VALUE IS OPTIONAL.
          463 C WHEN NON-ZERO, THIS POINTER IS USED AS A POINTER
          464 C TO A TABLES DESCRIBED AS FOLLOWS :
          465 C
          466 C BYTE DISPLAYABLE ROWS
          467 C WORD BYTES PER CHARACTER
          468 C DWORD POINTER TO A FONT TABLE
          469 C BYTE CONSECUTIVE BYTES OF MODE VALUES FOR WHICH
          470 C THIS FONT DESCRIPTION IS TO BE USED.
          471 C THE END OF THIS STREAM IS INDICATED BY A
          472 C BYTE CODE OF 'FF'
          473 C
          474 C DWORD_5 THRU DWORD_7
          475 C RESERVED AND SET TO 0000:0000.
          476 C
          477 C
          478 C ORG 0500H
0500 77      479 C STATUS_BYTE DB ?
0500 77      480 C ABSO ENDS
0501      481 C
          482 C PORT_B EQU 61H ; 8255 PORT B ADDR
          483 C TIMER EQU 40H
          484 C
          485 C ;----- EQUATES FOR CARD PORT ADDRESSES
          486 C
          487 C SEQ_ADDR EQU 0C4H
          488 C SEQ_DATA EQU 0C5H
          489 C CRTC_ADDR EQU 0D4H
          490 C CRTC_ADDR_B EQU 0B4H
          491 C CRTC_DATA EQU 0D5H ; OR 0B5H
          492 C GRAPH_1_POS EQU 0CCH
          493 C GRAPH_2_POS EQU 0CAH
          494 C GRAPH_ADDR EQU 0CEH
          495 C GRAPH_DATA EQU 0CFH
          496 C MISC_OUTPUT EQU 0C2H
          497 C IN_STAT_0 EQU 0C2H
          498 C INPUT_STATUS_B EQU 0BAH
          499 C INPUT_STATUS EQU 0BAH
          500 C ATTR_READ EQU 0BAH
          501 C ATTR_WRITE EQU 0C0H
          502 C
          503 C ;----- EQUATES FOR ADDRESS REGISTER VALUES
          504 C

```

```

= 0000      505 C      S_RESET      EQU      00H
= 0001      506 C      S_CLOCK      EQU      01H
= 0002      507 C      S_MAP      EQU      02H
= 0003      508 C      S_CGEN      EQU      03H
= 0004      509 C      S_MEN      EQU      04H
= 0000      510 C
= 0001      511 C      C_HRZ_TOT      EQU      00H
= 0002      512 C      C_HRZ_DSP      EQU      01H
= 0003      513 C      C_STRT_HRZ_BLK      EQU      02H
= 0004      514 C      C_END_HRZ_BLK      EQU      03H
= 0005      515 C      C_STRT_HRZ_SYN      EQU      04H
= 0006      516 C      C_END_HRZ_SYN      EQU      05H
= 0007      517 C      C_VRT_TOT      EQU      06H
= 0008      518 C      C_OVERFLOW      EQU      07H
= 0009      519 C      C_PRE_ROW      EQU      08H
= 000A      520 C      C_MAX_SCAN_LN      EQU      09H
= 000B      521 C      C_CRSR_START      EQU      0AH
= 000C      522 C      C_CRSR_END      EQU      0BH
= 000D      523 C      C_STRT_HGH      EQU      0CH
= 000E      524 C      C_STRT_LOW      EQU      0DH
= 000F      525 C      C_CRSR_LOC_HGH      EQU      0EH
= 0010      526 C      C_CRSR_LOC_LOW      EQU      0FH
= 0011      527 C      C_VRT_SYN_STRT      EQU      10H
= 0012      528 C      C_LGHT_PEN_HGH      EQU      10H
= 0013      529 C      C_VRT_SYN_END      EQU      11H
= 0014      530 C      C_LGHT_PEN_LOW      EQU      11H
= 0015      531 C      C_VRT_DSP_END      EQU      12H
= 0016      532 C      C_OFFSET      EQU      13H
= 0017      533 C      C_UNDERLN_LOC      EQU      14H
= 0018      534 C      C_STRT_VRT_BLK      EQU      15H
= 0019      535 C      C_END_VRT_BLK      EQU      16H
= 001A      536 C      C_MODE_CNTL      EQU      17H
= 001B      537 C      C_LN_COMP      EQU      18H
= 0000      538 C
= 0001      539 C      G_SET_RESET      EQU      00H
= 0002      540 C      G_ENBL_SET      EQU      01H
= 0003      541 C      G_CLR_COMP      EQU      02H
= 0004      542 C      G_DATA_ROT      EQU      03H
= 0005      543 C      G_READ_MAP      EQU      04H
= 0006      544 C      G_MODE      EQU      05H
= 0007      545 C      G_MISC      EQU      06H
= 0008      546 C      G_COLOR      EQU      07H
= 0009      547 C      G_BIT_MASK      EQU      08H
= 0010      548 C
= 0011      549 C      F_MODE      EQU      10H
= 0012      550 C      F_OVERSC      EQU      11H
= 0013      551 C      F_CPLANE      EQU      12H
= 0014      552 C      F_HPFL      EQU      13H
= 0015      553 C
= 0016      554 C      SUBTTL
= 0017      555 C
= 0018      556 C      ;----- CODE SEGMENT
= 0019      557 C
= 0020      558 C      CODE      SEGMENT      PUBLIC
= 0021      559 C
= 0022      560 C      INCLUDE      VPOST.INC
= 0023      561 C      SUBTTL      VPOST.INC
= 0024      562 C      PAGE
= 0025      563 C
= 0026      564 C      ;----- POST
= 0027      565 C
= 0028      566 C      ASSUME      CS:CODE,DS:ABS0
= 0029      567 C      ORG      0H
= 0030      568 C      DB      055H      ; SIGNATURE
= 0031      569 C      DB      0A0H      ; BYTES
= 0032      570 C      DB      020H      ; LENGTH INDICATOR
= 0033      571 C
= 0034      572 C      ;----- NOTE : DO NOT USE THE SIGNATURE BYTES AS A PRESENCE TEST
= 0035      573 C
= 0036      574 C      ; PLANAR VIDEO SWITCH SETTINGS
= 0037      575 C
= 0038      576 C      ; 0 0 - UNUSED
= 0039      577 C      ; 0 1 - 40 X 25 COLOR
= 0040      578 C      ; 1 0 - 80 X 25 COLOR
= 0041      579 C      ; 1 1 - 80 X 25 MONOCHROME
= 0042      580 C      ; NOTE : 0 0 MUST BE SET WHEN THIS ADAPTER IS INSTALLED.
= 0043      581 C
= 0044      582 C      ; VIDEO ADAPTER SWITCH SETTINGS
= 0045      583 C
= 0046      584 C      ; 0 0 0 - MONOC PRIMARY, EGA COLOR, 40X25
= 0047      585 C      ; 0 0 1 - MONOC PRIMARY, EGA COLOR, 80X25
= 0048      586 C      ; 0 1 0 - MONOC PRIMARY, EGA HI RES EMULATE (SAME AS 0001)
= 0049      587 C      ; 0 0 1 - MONOC PRIMARY, EGA HI RES ENHANCED
= 0050      588 C      ; 0 1 0 - COLOR 40 PRIMARY, EGA MONOCHROME
= 0051      589 C      ; 0 1 1 - COLOR 80 PRIMARY, EGA MONOCHROME
= 0052      590 C
= 0053      591 C      ; 0 1 1 0 - MONOC SECONDARY, EGA COLOR, 40X25
= 0054      592 C      ; 0 1 1 1 - MONOC SECONDARY, EGA COLOR, 80X25
= 0055      593 C      ; 1 0 0 0 - MONOC SECONDARY, EGA HI RES EMULATE (SAME AS 0111)
= 0056      594 C      ; 1 0 0 1 - MONOC SECONDARY, EGA HI RES ENHANCED
= 0057      595 C      ; 1 0 1 0 - COLOR 40 SECONDARY, EGA MONOCHROME
= 0058      596 C      ; 1 0 1 1 - COLOR 80 SECONDARY, EGA MONOCHROME
= 0059      597 C
= 0060      598 C      ; 1 1 0 0 - RESERVED
= 0061      599 C      ; 1 1 0 1 - RESERVED
= 0062      600 C      ; 1 1 1 0 - RESERVED
= 0063      601 C      ; 1 1 1 1 - RESERVED
= 0064      602 C
= 0065      603 C      ;----- SETUP ROUTINE FOR THIS MODULE
= 0066      604 C
= 0067      605 C      VIDEO_SETUP      PROC      FAR
= 0068      606 C      JMP      SHORT      L1
= 0069      607 C      DB      '2400'
= 0070      608 C      DB      '6277356 (C)COPYRIGHT IBM 1984'
= 0071      609 C
= 0072      610 C
= 0073      611 C
= 0074      612 C      DB      '9/13/84'
= 0075      613 C
= 0076      614 C
= 0077      615 C
= 0078      616 C      ;----- SET UP VIDEO VECTORS
= 0079      617 C
= 0080      618 C      L1:
= 0081      619 C      MOV      DH,3
= 0082      620 C      MOV      IN,DX
= 0083      621 C      MOV      DL,INPUT_STATUS
= 0084      622 C      MOV      IN,DX
= 0085      623 C      MOV      DL,INPUT_STATUS_B
= 0086      624 C      MOV      DL,ATTR_WRITE
= 0087      625 C      MOV      AL,0
= 0088      626 C      OUT      DX,AL
= 0089      627 C
= 0090      628 C      SRLOAD      DS,0
= 0091      629 C      SUB      DX,DX
= 0092      630 C      MOV      DS,DX

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003E FA 631
003F C7 06 0040 R 0CD7 R 632
0045 8C 0E 0042 R 633
0049 C7 06 0108 R F065 634
004F C7 06 010A R F000 635
0055 C7 06 04A8 R 010C R 636
0058 8C 0E 04AA R 637
005F C7 06 007C R 0000 E 638
0065 8C 0E 007E R 639
0069 C7 06 010C R 0000 E 640
006F 8C 0E 010E R 641
0073 FB 642
643
644
645
;----- POST FOR COMBO VIDEO CARD
0074 C6 06 0487 R 04 646
0079 E8 009B R 647
007C 88 1E 0488 R 648
0080 E8 00CE R 649
0083 06 06 0488 R 650
0087 8A 1E 0488 R 651
008B E8 00F3 R 652
008E E9 0244 R 653
0091 654
0091 CB 655
0092 656
657
658
0092 EE 659
0093 50 660
0094 58 661
0095 EC 662
0096 24 10 663
0098 D0 E8 664
009A C3 665
009B 666
667
668
669
;----- READ THE SWITCH SETTINGS ON THE CARD
009B 670
009B B6 03 671
009D B2 C2 672
009F B0 01 673
00A1 EE 674
675
676
677
;----- COULD BE 0,4,8,C
00A2 B0 0D 678
00A4 E8 0092 R 679
00A7 D0 E8 680
00A9 D0 E8 681
00AB D0 E8 682
00AD 8A D8 683
684
685
686
00AF B0 09 687
00B1 E8 0092 R 688
00B4 D0 E8 689
00B6 D0 E8 690
00B8 0A D8 691
692
693
00BA B0 05 694
00BC E8 0092 R 695
00BF D0 E8 696
00C1 0A D8 697
698
00C3 B0 01 699
00C5 E8 0092 R 700
00C8 0A D8 701
702
00CA B0 E3 0F 703
00CD C3 704
00CE 705
706
707
00CE 708
00CE B6 03 709
00D0 B2 BA 710
00D2 B0 01 711
00D4 EE 712
00D5 B2 DA 713
00D7 EE 714
00D8 B2 C2 715
00DA EC 716
00DB 24 60 717
00DD D0 E8 718
00DF 8A D8 719
00E1 B2 BA 720
00E3 B0 02 721
00E5 EE 722
00E6 B2 DA 723
00E8 EE 724
00E9 B2 C2 725
00EB EC 726
00EC 24 60 727
00EE D0 E0 728
00F0 0A C3 729
00F2 C3 730
00F3 731
732
733
734
00F3 735
00F3 2A FF 736
00F5 B0 E3 0F 737
00F8 D1 E3 738
00FA 52 739
00FB B6 03 740
00FD 8A E6 741
00FF 5A 742
0100 B0 E4 01 743
0103 FE C4 744
0105 F6 D4 745
0107 2E: FF A7 0128 R 746
747
748
010C 749
010C 0717 R 750
010E C000 751
0110 0000 752
0112 0000 753
0114 0000 754
0116 0000 755
0118 0000 756
C
CL1
MOV WORD PTR VIDEO, OFFSET COMBO_VIDEO
MOV WORD PTR VIDEO+2, CS
MOV WORD PTR PLANAR_VIDEO, OF065H
MOV WORD PTR PLANAR_VIDEO+2, OF000H
MOV WORD PTR SAVE_PTR, OFFSET SAVE_TBL
MOV WORD PTR SAVE_PTR+2, CS
MOV WORD PTR EXT_PTR, OFFSET INT_1F_1
MOV WORD PTR EXT_PTR+2, CS
MOV WORD PTR GRX_SET, OFFSET CGDDOT
MOV WORD PTR GRX_SET+2, CS
STI
;----- POST FOR COMBO VIDEO CARD
MOV INFO, 00000100B
CALL RD_SWS
MOV INFO_3, BL
CALL F_BTS
OR INFO_3, AL
MOV BL, INFO_3
CALL MK_ENV
JMP POST
SKIP:
RET
VIDEO_SETUP ENDP
POR_1 PROC NEAR
OUT DX, AL
PUSH AX
POP AX
IN AL, DX
AND AL, 010H
SHR AL, 1
POR_1 ENDP
;----- READ THE SWITCH SETTINGS ON THE CARD
RD_SWS PROC NEAR
ASSUME DS:ABS0
MOV DH, 3
MOV DL, MISC_OUTPUT
MOV AL, 1
OUT DX, AL
;----- COULD BE 0,4,8,C
MOV AL, 0DH
CALL POR_1
SHR AL, 1
SHR AL, 1
SHR AL, 1
MOV AL, 9
CALL POR_1
SHR AL, 1
SHR AL, 1
OR BL, AL
MOV AL, 5
CALL POR_1
SHR AL, 1
OR BL, AL
MOV AL, 1
CALL POR_1
OR BL, AL
AND BL, 0FH
RD_SWS RET
ENDP
;----- OBTAIN THE FEATURE BITS FROM DAUGHTER CARD
F_BTS PROC NEAR
MOV DH, 3
MOV DL, 0BAH
MOV AL, 1
OUT DX, AL
MOV DL, 0DAH
OUT DX, AL
MOV DL, IN_STAT_0
IN AL, DX
AND AL, 060H
SHR AL, 1
MOV BL, AL
MOV DL, 0BAH
MOV AL, 2
OUT DX, AL
MOV DL, 0DAH
OUT DX, AL
MOV DL, IN_STAT_0
IN AL, DX
AND AL, 060H
SHL AL, 1
OR AL, BL
F_BTS RET
ENDP
;----- ESTABLISH THE VIDEO ENVIRONMENT, KEYED OFF OF THE SWITCHES
MK_ENV PROC NEAR
ASSUME DS:ABS0
SUB BH, 0FH
AND BL, 0FH
SAL BX, 1
PUSH DX
MOV DH, 3
MOV AH, DH
POP DX
AND AH, 1
INC AH
NOT AH
JMP WORD PTR CS:[BX + OFFSET T5]
SAVE_TBL LABEL DWORD
DW OFFSET VIDEO_PARMS
DW 0C000H
DW 0
DW 0
DW 0
DW 0
; PARMS
; PARMS
; PAL SAVE AREA
; PAL SAVE AREA
; ALPHA TABLES
; ALPHA TABLES
; GRAPHICS TABLES

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011A 0000	757	C	DW	0		
	758	C				; GRAPHICS TABLES
011C 0000	759	C	DW	0		
011E 0000	760	C	DW	0		
0120 0000	761	C	DW	0		
0122 0000	762	C	DW	0		
0124 0000	763	C	DW	0		
0126 0000	764	C	DW	0		
	765	C				
0128	766	C	T5	LABEL	WORD	
0128 0173 R	767	C	DW	OFFSET	PST_0	
012A 017E R	768	C	DW	OFFSET	PST_1	
012C 017E R	769	C	DW	OFFSET	PST_2	
012E 0189 R	770	C	DW	OFFSET	PST_3	
0130 0194 R	771	C	DW	OFFSET	PST_4	
0132 01A8 R	772	C	DW	OFFSET	PST_5	
0134 01BC R	773	C	DW	OFFSET	PST_6	
0136 01C7 R	774	C	DW	OFFSET	PST_7	
	775	C				
0138 01C7 R	776	C	DW	OFFSET	PST_8	
013A 01D2 R	777	C	DW	OFFSET	PST_9	
013C 01DD R	778	C	DW	OFFSET	PST_A	
013E 01F1 R	779	C	DW	OFFSET	PST_B	
0140 0204 R	780	C	DW	OFFSET	PST_OUT	
0142 0204 R	781	C	DW	OFFSET	PST_OUT	
0144 0204 R	782	C	DW	OFFSET	PST_OUT	
0146 0204 R	783	C	DW	OFFSET	PST_OUT	
	784	C				
0148	785	C	ENV_X	PROC	NEAR	
0148 80 26 0410 R CF	786	C	AND	EQUIP_LOW,OCFH		; SET 40X25 COLOR ALPHA
014D 80 0E 0410 R 10	787	C	OR	EQUIP_LOW,010H		
0152 B8 0001	788	C	MOV	AX,1H		
0155 CD 10	789	C	INT	10H		
0157 C3	790	C	RET			
0158	791	C	ENV_X	ENDP		
	792	C				
0158	793	C	ENV_0	PROC	NEAR	
0158 80 26 0410 R CF	794	C	AND	EQUIP_LOW,OCFH		; SET 80X25 COLOR ALPHA
015D 80 0E 0410 R 20	795	C	OR	EQUIP_LOW,020H		
0162 B8 0003	796	C	MOV	AX,03H		
0165 CD 10	797	C	INT	10H		
0167 C3	798	C	RET			
0168	799	C	ENV_0	ENDP		
	800	C				
0168	801	C	ENV_3	PROC	NEAR	
0168 80 0E 0410 R 30	802	C	OR	EQUIP_LOW,030H		; SET MONOCHROME ALPHA
016D B8 0007	803	C	MOV	AX,07H		
0170 CD 10	804	C	INT	10H		
0172 C3	805	C	RET			
0173	806	C	ENV_3	ENDP		
	807	C				
	808	C				
0173	809	C	PST_0:			
0173 20 26 0487 R	810	C	AND	INFO,AH		
0177 E8 0148 R	811	C	CALL	ENV_X		
017A E8 0168 R	812	C	CALL	ENV_3		
017D C3	813	C	RET			
017E	814	C	PST_1:			
017E	815	C	PST_2:			
017E 20 26 0487 R	816	C	AND	INFO,AH		
0182 E8 0158 R	817	C	CALL	ENV_0		
0185 E8 0168 R	818	C	CALL	ENV_3		
0188 C3	819	C	RET			
0189	820	C	PST_3:			
0189 20 26 0487 R	821	C	AND	INFO,AH		
018D E8 0158 R	822	C	CALL	ENV_0		
0190 E8 0168 R	823	C	CALL	ENV_3		
0193 C3	824	C	RET			
0194	825	C	PST_4:			
0194 B6 03	826	C	MOV	DH,3		
0196 B2 C2	827	C	MOV	DI,MISC_OUTPUT		
0198 B0 00	828	C	MOV	AL,0		
019A EE	829	C	OUT	DX,AL		
019B F6 04	830	C	NOT	AH		
019D 08 26 0487 R	831	C	OR	INFO,AH		
01A1 E8 0168 R	832	C	CALL	ENV_3		
01A4 E8 0148 R	833	C	CALL	ENV_X		
01A7 C3	834	C	RET			
01A8	835	C	PST_5:			
01A8 B6 03	836	C	MOV	DH,3		
01AA B2 C2	837	C	MOV	DI,MISC_OUTPUT		
01AC B0 00	838	C	MOV	AL,0		
01AE EE	839	C	OUT	DX,AL		
01AF F6 04	840	C	NOT	AH		
01B1 08 26 0487 R	841	C	OR	INFO,AH		
01B5 E8 0168 R	842	C	CALL	ENV_3		
01B8 E8 0158 R	843	C	CALL	ENV_0		
01BB C3	844	C	RET			
01BC	845	C	PST_6:			
01BC 20 26 0487 R	846	C	AND	INFO,AH		
01C0 E8 0168 R	847	C	CALL	ENV_3		
01C3 E8 0148 R	848	C	CALL	ENV_X		
01C6 C3	849	C	RET			
01C7	850	C	PST_7:			
01C7	851	C	PST_8:			
01C7 20 26 0487 R	852	C	AND	INFO,AH		
01C8 E8 0168 R	853	C	CALL	ENV_3		
01CE E8 0158 R	854	C	CALL	ENV_0		
01D1 C3	855	C	RET			
01D2	856	C	PST_9:			
01D2 20 26 0487 R	857	C	AND	INFO,AH		
01D6 E8 0168 R	858	C	CALL	ENV_3		
01D9 E8 0158 R	859	C	CALL	ENV_0		
01DC C3	860	C	RET			
01DD	861	C	PST_A:			
01DD B6 03	862	C	MOV	DH,3		
01DF B2 C2	863	C	MOV	DI,MISC_OUTPUT		
01E1 B0 00	864	C	MOV	AL,0		
01E3 EE	865	C	OUT	DX,AL		
01E4 F6 04	866	C	NOT	AH		
01E6 08 26 0487 R	867	C	OR	INFO,AH		
01EA E8 0148 R	868	C	CALL	ENV_X		
01ED E8 0168 R	869	C	CALL	ENV_3		
01F0 C3	870	C	RET			
01F1	871	C	PST_B:			
01F1 B6 03	872	C	MOV	DH,3		
01F3 B2 C2	873	C	MOV	DI,MISC_OUTPUT		
01F5 B0 00	874	C	MOV	AL,0		
01F7 EE	875	C	OUT	DX,AL		
01F8 F6 04	876	C	NOT	AH		
01FA 08 26 0487 R	877	C	OR	INFO,AH		
01FE E8 0158 R	878	C	CALL	ENV_0		
0201 E8 0168 R	879	C	CALL	ENV_3		
0204	880	C	PST_OUT:			
0204 C3	881	C	RET			
0205	882	C	MK_ENV	ENDP		



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0294 74 07      1009 C      JE      E10      ; YES - SKIP VIDEO RAM TEST
0296 8E 0B      1010 C      MOV     DS,BX      ; POINT DS TO VIDEO RAM STG
0298 E8 02DF R  1011 C      ASSUME  DS:NOTHING,ES:NOTHING
0298 75 2E      1012 C      CALL    STGTST_CNT      ; GO TEST VIDEO R/W STC
0298 75 2E      1013 C      LINE    E17      ; R/W STG FAILURE - BEEP SPK
0298 75 2E      1014 C      -----
0298 75 2E      1015 C      SETUP VIDEO DATA ON SCREEN FOR VIDEO LINE TEST.
0298 75 2E      1016 C      DESCRIPTION
0298 75 2E      1017 C      ENABLE VIDEO SIGNAL AND SET MODE.
0298 75 2E      1018 C      DISPLAY A HORIZONTAL BAR ON SCREEN.
0298 75 2E      1019 C      -----
029D 58         1020 C      E10:
029D 58         1021 C      POP     AX      ; GET VIDEO SENSE SWS (AH)
029D 58         1022 C      PUSH    AX      ; SAVE IT
029D 58         1023 C      MOV     AX,7020H ; WRT BLANKS IN REVERSE VIDEO
02A2 2B FF      1024 C      SUB     DI,DI      ; SETUP STARTING LOC
02A4 B9 0028    1025 C      MOV     CX,40      ; NO. OF BLANKS TO DISPLAY
02A7 F3/ AB     1026 C      REP     STOSW     ; WRITE VIDEO STORAGE
02A7 F3/ AB     1027 C      -----
02A7 F3/ AB     1028 C      CRT INTERFACE LINES TEST
02A7 F3/ AB     1029 C      DESCRIPTION
02A7 F3/ AB     1030 C      SENSE ON/OFF TRANSITION OF THE VIDEO ENABLE
02A7 F3/ AB     1031 C      AND HORIZONTAL SYNC LINES.
02A7 F3/ AB     1032 C      -----
02A9 58         1033 C      POP     AX      ; GET VIDEO SENSE SW INFO
02AA 50         1034 C      PUSH    AX      ; SAVE IT
02AB 80 FC 30   1035 C      CMP     AH,30H     ; B/W CARD ATTACHED?
02AE 8A 03BA    1036 C      MOV     DX,03BAH   ; SETUP ADDR OF BW STATUS PORT
02B1 74 02      1037 C      JE      E11      ; YES - GO TEST LINES
02B3 82 DA      1038 C      MOV     DL,0DAH     ; COLOR CARD IS ATTACHED
02B5 84 08      1039 C      MOV     AH,8       ; LINE_TST:
02B7 2B C9      1040 C      MOV     AH,8       ; OFLOOP_CNT:
02B7 2B C9      1041 C      SUB     CX,CX
02B9 EC         1042 C      IN      AL,DX
02BA 22 C4      1043 C      AND     AL,AH
02BC 75 04      1044 C      JNZ     E14
02BE E2 F9      1045 C      LOOP    E13
02C0 EB 09      1046 C      JMP     SHORT E17
02C2 2B C9      1047 C      SUB     CX,CX
02C4 EC         1048 C      IN      AL,DX
02C5 22 C4      1049 C      AND     AL,AH
02C7 74 0A      1050 C      JZ      E15
02C9 E2 F9      1051 C      LOOP    E15
02CB EC         1052 C      MOV     DX,102H
02CE E8 06C8 R  1053 C      CALL    ERR_BEEP   ; GO BEEP SPEAKER
02D1 EB 06      1054 C      JMP     SHORT E18
02D3 B1 03      1055 C      MOV     CL,3
02D5 D2 EC      1056 C      SHR     AH,CL
02D7 75 0E      1057 C      JNZ     E12
02D9 58         1058 C      POP     AX      ; GO CHECK HORIZONTAL LINE
02DA EB 38      1059 C      JMP     SHORT POD14  ; DISPLAY CURSOR:
02DA EB 38      1060 C      ; GET VIDEO SENSE SWS (AH)
02DA EB 38      1061 C      -----
02DA EB 38      1062 C      THIS SUBROUTINE PERFORMS A READ/WRITE STORAGE TEST ON
02DA EB 38      1063 C      A 16K BLOCK OF STORAGE.
02DA EB 38      1064 C      ENTRY
02DA EB 38      1065 C      ES = ADDRESS OF STORAGE SEGMENT BEING TESTED
02DA EB 38      1066 C      DS = ADDRESS OF STORAGE SEGMENT BEING TESTED
02DA EB 38      1067 C      WHEN ENTERING AT STGTST_CNT, CX MUST BE LOADED WITH
02DA EB 38      1068 C      THE BYTE COUNT.
02DA EB 38      1069 C      EXIT
02DA EB 38      1070 C      ZERO FLAG = 0 IF STORAGE ERROR (DATA COMPARE OR PARITY CHECK.
02DA EB 38      1071 C      AL = 0 DENOTES A PARITY CHECK. ELSE AL=XOR'D BIT
02DA EB 38      1072 C      PATTERN OF THE EXPECTED DATA PATTERN VS THE
02DA EB 38      1073 C      ACTUAL DATA READ.
02DA EB 38      1074 C      AX,BX,CX,DX,DI, AND SI ARE ALL DESTROYED.
02DA EB 38      1075 C      -----
02DC B9 4000    1076 C      STGTST  PROC     NEAR
02DC B9 4000    1077 C      MOV     CX,4000H   ; SETUP CNT TO TEST A 16K BLK
02DC B9 4000    1078 C      STGTST_CNT:
02DC B9 4000    1079 C      CLD
02DC B9 4000    1080 C      MOV     BX,CX
02DC B9 4000    1081 C      MOV     AX,AAAAH
02DC B9 4000    1082 C      MOV     DX,0FF55H
02DC B9 4000    1083 C      SUB     DI,DI
02DC B9 4000    1084 C      REP     STOSB
02DC B9 4000    1085 C      C3:      DEC     DI
02DC B9 4000    1086 C      STD
02DC B9 4000    1087 C      MOV     SI,DI
02DC B9 4000    1088 C      MOV     CX,BX
02DC B9 4000    1089 C      C4:      MOV     SI,DI
02DC B9 4000    1090 C      MOV     CX,BX
02DC B9 4000    1091 C      C5:      LODSB
02DC B9 4000    1092 C      XOR     AL,AH
02DC B9 4000    1093 C      JNC     C7
02DC B9 4000    1094 C      MOV     AL,DL
02DC B9 4000    1095 C      STOSB
02DC B9 4000    1096 C      LOOP    C5
02DC B9 4000    1097 C      AND     AH,AH
02DC B9 4000    1098 C      JCXZ    C6
02DC B9 4000    1099 C      MOV     AH,AL
02DC B9 4000    1100 C      XCHG    DH,DL
02DC B9 4000    1101 C      AND     AH,AH
02DC B9 4000    1102 C      JNZ     C6
02DC B9 4000    1103 C      MOV     DL,AH
02DC B9 4000    1104 C      JMP     C3
02DC B9 4000    1105 C      C6:      CLD
02DC B9 4000    1106 C      INC     DI
02DC B9 4000    1107 C      JZ      C4
02DC B9 4000    1108 C      DEC     DI
02DC B9 4000    1109 C      JMP     C3
02DC B9 4000    1110 C      C6X:     MOV     AL,000H
02DC B9 4000    1111 C      C7:      CLD
02DC B9 4000    1112 C      RET
02DC B9 4000    1113 C      STGTST  ENDP
02DC B9 4000    1114 C      -----
02DC B9 4000    1115 C      EGA CRT ATTACHMENT TEST
02DC B9 4000    1116 C      1. INIT CRT TO 40X25 - BW *****SET TO MODE*****
02DC B9 4000    1117 C      2. CHECK FOR VERTICAL AND VIDEO ENABLES, AND CHECK
02DC B9 4000    1118 C      TIMING OF SAME
02DC B9 4000    1119 C      3. CHECK VERTICAL INTERRUPT
02DC B9 4000    1120 C      4. CHECK RED, BLUE, GREEN, AND INTENSIFY DOTS
02DC B9 4000    1121 C
02DC B9 4000    1122 C
02DC B9 4000    1123 C
02DC B9 4000    1124 C
02DC B9 4000    1125 C
02DC B9 4000    1126 C
02DC B9 4000    1127 C
02DC B9 4000    1128 C
02DC B9 4000    1129 C
02DC B9 4000    1130 C
02DC B9 4000    1131 C
02DC B9 4000    1132 C
02DC B9 4000    1133 C
02DC B9 4000    1134 C

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1135 C ; 5. INIT TO 40X25 - COLOR/MONO ****SET TO MODE**** :
1136 C ;-----
1137 C ;----- NOMINAL TIME IS 826H FOR 60 HZ.
1138 C ;----- NOMINAL TIME IS A2FEH FOR 50 HZ.
1139 C
1140 C
1141 C MAX_VERT_COLOR EQU 0A0ACH ; MAX TIME FOR VERT/VERT
1142 C ; (NOMINAL + 10%)
1143 C MIN_VERT_COLOR EQU 0C460H ; MIN TIME FOR VERT/VERT
1144 C ; (NOMINAL - 10%)
1145 C CENAB_PER_FRAME EQU 200 ; NUM OF ENABLES PER FRAME
1146 C MAX_VERT_MONO EQU 08D99H ; MAX TIME FOR VERT/VERT
1147 C ; (NOMINAL + 10%)
1148 C MIN_VERT_MONO EQU 0B862H ; MIN TIME FOR VERT/VERT
1149 C ; (NOMINAL - 10%)
1150 C EENAB_PER_FRAME EQU 350 ; ENHANCED ENABLES PER FRAME
1151 C MENAB_PER_FRAME EQU 350 ; NUM OF ENABLES PER FRAME
1152 C
1153 C TIM_CTL EQU 043H ; 8253 TIMER CONTROL PORT
1154 C TIMERO EQU 040H ; 8253 TIMER/CNTLR 0 PORT
1155 C
1156 C
1157 C POD14 PROC NEAR
1158 C SUB SP,0AH ; RESERVE 5 WORDS ON STACK
1159 C MOV BP,SP ; INIT SCRATCH PAD POINTER
1160 C
1161 C ASSUME DS:ABSO,ES:ABSO
1162 C CALL DDS ; SET TIMER 0 TO MODE 0
1163 C MOV AL,00110000B
1164 C
1165 C OUT TIM_CTL,AL
1166 C MOV AL,00H ; SEND FIRST BYTE TO TIMER
1167 C OUT TIMERO,AL
1168 C TEST INFO,2
1169 C JZ COLOR_EGA_V ; SET UP IN MONOCHROME
1170 C CALL ENV ; NUM OF FRAMES FOR MONO
1171 C MOV WORD PTR[BP][2],MENAB_PER_FRAME ; MAX TIME FOR VERT/VERT
1172 C MOV WORD PTR[BP][4],MAX_VERT_MONO ; MIN TIME FOR VERT/VERT
1173 C MOV DL,CRTC_ADDR_B ; MONO CRTX REG
1174 C MOV AH,C_HRZ_DSP ; HORIZ. TOTAL DISPLAY
1175 C MOV AL,27H ; TO 40 COL
1176 C CALL OUT_DK ; 3BA
1177 C MOV DL,INPUT_STATUS_B
1178 C JMP COMMON
1179 C
1180 C COLOR_EGA_V:
1181 C CALL ENV_X ; SET UP IN 40X25 COLOR
1182 C BRST_DET ; ENHANCED DET
1183 C JNC COLOR_V ; NO,40X25
1184 C MOV DL,CRTC_ADDR_B ; BRST MODE ONLY!
1185 C MOV AH,1 ; HRZ DSP END
1186 C MOV AL,20 ; MODIFY FOR TEST ONLY
1187 C CALL OUT_DK
1188 C MOV WORD PTR[BP][2],EENAB_PER_FRAME ; NUM OF FRAMES FOR COLOR
1189 C MOV WORD PTR[BP][4],MAX_VERT_COLOR ; MAX TIME FOR VERT/VERT
1190 C BRST_DET ; MIN TIME FOR VERT/VERT
1191 C JNC COLOR_V ; SET ADDRESSING TO VIDEO
1192 C MOV DL,INPUT_STATUS_B ; ATTR STATUS
1193 C JMP COMMON
1194 C
1195 C COMMON:
1196 C MOV AX,0500H ; SET TO VIDEO PAGE 0
1197 C INT 10H
1198 C SUB CX,CX
1199 C
1200 C ;----- LOOK FOR VERTICAL
1201 C
1202 C
1203 C
1204 C POD14_1:
1205 C IN AL,DX ; GET STATUS
1206 C TEST AL,00001000B ; VERTICAL THERE YET?
1207 C JNZ POD14_2 ; CONTINUE IF IT IS
1208 C LOOP POD14_1 ; KEEP LOOKING TILL COUNT
1209 C MOV BL,00 ; EXHAUSTED
1210 C JMP POD14_ERR ; NO VERTICAL
1211 C
1212 C ;----- GOT VERTICAL - START TIMER
1213 C
1214 C
1215 C POD14_2:
1216 C MOV AL,0 ; SEND 2ND BYTE TO TIMER TO
1217 C OUT TIMERO,AL ; START IT
1218 C SUB BX,BX ; INIT. ENABLE COUNTER
1219 C XOR CX,CX
1220 C ;----- WAIT FOR VERTICAL TO GO AWAY
1221 C
1222 C POD14_25:
1223 C IN AL,DX ; GET STATUS
1224 C TEST AL,00001000B ; VERTICAL STILL THERE
1225 C JZ POD14_3 ; CONTINUE IF IT'S GONE
1226 C LOOP POD14_25 ; KEEP LOOKING TILL COUNT
1227 C MOV BL,01H ; EXHAUSTED
1228 C JMP POD14_ERR ; VERTICAL STUCK ON
1229 C
1230 C ;----- NOW START LOOKING FOR ENABLE TRANSITIONS
1231 C
1232 C
1233 C POD14_3:
1234 C SUB CX,CX
1235 C
1236 C POD14_4:
1237 C IN AL,DX ; GET STATUS
1238 C TEST AL,00000001B ; ENABLE ON YET?
1239 C JE POD14_5 ; GO ON IF IT IS
1240 C TEST AL,00001000B ; VERTICAL ON AGAIN?
1241 C JZ POD14_4 ; CONTINUE IF IT IS
1242 C MOV BL,02H ; KEEP LOOKING IF NOT
1243 C JMP POD14_ERR ; ENABLE STUCK OFF
1244 C
1245 C POD14_4A:
1246 C MOV BL,03H ; VERTICAL STUCK ON
1247 C JMP POD14_ERR
1248 C
1249 C POD14_4B:
1250 C MOV BL,04H ; ENABLE STUCK ON
1251 C JMP POD14_ERR
1252 C
1253 C ;----- MAKE SURE VERTICAL WENT OFF WITH ENABLE GOING ON
1254 C
1255 C
1256 C POD14_5:
1257 C TEST AL,00001000B ; VERTICAL OFF?
1258 C JNZ POD14_4A ; GO ON IF IT IS
1259 C ; (ERROR IF NOT)
1260 C ;----- NOW WAIT FOR ENABLE TO GO OFF
1261 C
1262 C POD14_6:
1263 C IN AL,DX ; GET STATUS
1264 C TEST AL,00000001B ; ENABLE OFF YET?
1265 C LOOP POD14_6 ; KEEP LOOKING IF NOT
1266 C JCXZ POD14_4B ; YET LOW
1267 C ;----- ENABLE HAS TOGGLED, BUMP COUNTER AND TEST FOR NEXT VERTICAL

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03C5      43      1261  C  POD14_7: INC BX ; BUMP ENABLE COUNTER
03C5      43      1262  C  JZ POD14_75 ; IF COUNTER WRAPS,
03C6      74 04      1263  C  ; SOMETHING IS WRONG
03C8      A8 08      1264  C  TEST AL,00001000B ; DID ENABLE GO LOW
03CA      74 02      1265  C  JZ POD14_3 ; BECAUSE OF VERTICAL
; IF NOT, LOOK FOR ANOTHER
; ENABLE TOGGLE
; NOW TEST RESULTS
03CC      1270  C  ;---- HAVE HAD COMPLETE VERTICAL-VERTICAL CYCLE,
03CC      80 00      1271  C  MOV AL,00 ; LATCH TIMER0
03CE      E6 43      1272  C  OUT TIM_CTL,AL
03D0      3B 5E 02      1273  C  CMP BX,WORD PTR[B*][2] ; NUMBER OF ENABLES BETWEEN
; VERTICALS O.K.?
03D3      74 04      1274  C  JE POD14_8
03D5      83 05      1275  C  MOV BL,05H
03D7      EB 6F      1276  C  JMP SHORT POD14_ERR
03D9      E4 40      1277  C  IN AL,TIMER0 ; GET TIMER VALUE LOW
03DB      8A E0      1278  C  MOV AH,AL ; SAVE IT
03DD      90      1279  C  NOP
03DE      E4 40      1280  C  IN AL,TIMER0 ; GET TIMER HIGH
03E0      86 E0      1281  C  MOV AH,AL
03E2      90      1282  C  NOP
03E3      90      1283  C  NOP
03E4      3B 46 04      1284  C  CMP AX,WORD PTR[B*][4] ; MAXIMUM VERTICAL TIMING
03E7      7D 04      1285  C  JGE POD14_9
03E9      83 06      1286  C  MOV BL,06H
03EB      EB 58      1287  C  JMP SHORT POD14_ERR
03ED      1288  C  ;
03ED      3B 46 06      1289  C  CMP AX,WORD PTR[B*][6] ; MINIMUM VERTICAL TIMING
03F0      7E 04      1290  C  JLE POD14_10
03F2      83 07      1291  C  MOV BL,07H
03F4      EB 52      1292  C  JMP SHORT POD14_ERR
;---- SEE IF RED, GREEN, BLUE AND INTENSIFY DOTS WORK
;---- FIRST, SET A LINE OF REVERSE VIDEO, INTENSIFIED BLANKS INTO BUFFER
03F6      88 090B      1293  C  MOV AX,090BH ; WRITE CHARS, BLANKS
03F9      BB 000F      1294  C  MOV BX,000FH ; PAGE 0, REVERSE VIDEO,
; HIGH INTENSITY
; 80 CHARACTERS
03FC      89 0050      1295  C  MOV CX,80
03FF      CD 10      1296  C  INT 10H
0401      EC      1297  C  IN AL,DX
0402      52      1298  C  PUSH DX ; SAVE INPUT STATUS
0403      B2 C0      1299  C  MOV DL,ATTR_WRITE ; ATTRIBUTE ADDRESS
0405      B4 0F      1300  C  MOV AH,0FH ; PALETTE REG 1F
0407      B0 3F      1301  C  MOV AL,03FH ; TEST VALUE
0409      EB 0D15 R      1302  C  CALL OUT_DK ; VIDEO STATUS MUX
040C      BB 000F      1303  C  MOV AX,0FH ; START WITH BLUE DOTS
040F      5A      1304  C  POP DX
0410      50      1305  C  ;
0410      50      1306  C  PUSH AX ; SAVE
0411      52      1307  C  PUSH DX ; SAVE INPUT STATUS
0412      B2 C0      1308  C  MOV DL,ATTR_WRITE ; ATTRIBUTE ADDRESS
0414      B4 32      1309  C  MOV AH,32H ; COLOR PLANE ENABLE
0416      EB 0D15 R      1310  C  CALL OUT_DK ; VIDEO STATUS MUX
0419      5A      1311  C  POP DX ; RECOVER INPUT STATUS
041A      58      1312  C  POP AX
041B      2B C9      1313  C  SUB CX,CX
;---- SEE IF DOT COMES ON
041D      1314  C  ;
041D      EC      1315  C  IN AL,DX
041E      A8 30      1316  C  TEST AL,00110000B ; GET STATUS
0420      75 09      1317  C  JNZ POD14_15 ; DOT THERE?
0422      E2 F9      1318  C  LOOP POD14_14 ; LOOK FOR DOT TO TURN OFF
0424      B3 10      1319  C  MOV BL,10H ; CONTINUE TEST FOR DOT ON
0426      0A DC      1320  C  OR BL,AH ; OR IN DOT BEING TESTED
0428      EB 1E 90      1321  C  JMP SHORT POD14_ERR ; DOT NOT COMING ON
;---- SEE IF DOT GOES OFF
042B      2B C9      1322  C  SUB CX,CX
042D      1323  C  ;
042D      EC      1324  C  IN AL,DX
042E      A8 30      1325  C  TEST AL,00110000B ; GET STATUS
0430      75 09      1326  C  JNZ POD14_15 ; DOT THERE?
0432      E2 F9      1327  C  LOOP POD14_14 ; LOOK FOR DOT TO TURN OFF
0434      B3 20      1328  C  MOV BL,20H ; CONTINUE TEST FOR DOT ON
0436      0A DC      1329  C  OR BL,AH ; OR IN DOT BEING TESTED
0438      EB 0E      1330  C  JMP SHORT POD14_ERR ; DOT NOT COMING ON
;---- ADJUST TO POINT TO NEXT DOT
043A      1331  C  ;
043A      FE C4      1332  C  INC AH
043C      80 FC 30      1333  C  CMP AH,030H ; ALL 3 DOTS DONE?
043F      74 25      1334  C  JE POD14_18 ; GO END
0441      80 CC 0F      1335  C  OR AH,0FH ; MAKE OF,1F,2F
0444      8A C4      1336  C  MOV AL,AH
0446      EB C8      1337  C  JMP POD14_13 ; GO LOOK FOR ANOTHER DOT
0448      1338  C  ;
0448      B9 0006      1339  C  MOV CX,6
044B      8A 0103      1340  C  MOV DX,0103H
044E      EB 06C8 R      1341  C  CALL ERR_BEEP ; ONE LONG AND THREE SHORT
0451      83 C4 0A      1342  C  ADD SP,0AH ; BALANCE STACK
0454      B0 36      1343  C  MOV AL,00110110B ; RE-INIT TIMER 0
0456      E6 43      1344  C  OUT TIM_CTL,AL
0458      2A C0      1345  C  SUB AL,AL
045A      E6 40      1346  C  OUT TIMERO,AL
045C      90      1347  C  NOP
045D      90      1348  C  NOP
045E      E6 40      1349  C  OUT TIMERO,AL
0460      BD 0001      1350  C  MOV BP,1
0463      E9 0091 R      1351  C  JMP SKIP
0466      1352  C  ASSUME DS:ABS0
0466      1353  C  ;
0466      E8 0CFE R      1354  C  CALL DDS
0469      B8 0500      1355  C  MOV AX,0500H ; SET TO VIDEO PAGE 0
046C      CD 10      1356  C  INT 10H
046E      B0 36      1357  C  MOV AL,00110110B ; RE-INIT TIMER 0
0470      E6 43      1358  C  OUT TIM_CTL,AL
0472      2A C0      1359  C  SUB AL,AL
0474      E6 40      1360  C  OUT TIMERO,AL
0476      90      1361  C  NOP
0477      90      1362  C  NOP
0478      E6 40      1363  C  OUT TIMERO,AL
047A      83 C4 0A      1364  C  ADD SP,0AH ; REMOVE SCRATCH PAD
047D      BD 0000      1365  C  MOV BP,0 ; MAKE BP NON ZERO
0480      1366  C  ;
;---- TEST STORAGE
0480      1367  C  MEM_TEST:
0480      1E      1368  C  PUSH DS

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0481	E8	OCFE	R	1387	C	CALL	DDS
				1388	C	ASSUME	DS:ABSO
0484	F6	06	0487 R 02	1389	C	TEST	INFO_2
0489	74	12		1390	C	JZ	D_COLOR_M
0488	80	0E	0410 R 30	1391	C	OR	EQUIP_LOW,030H
AX:901	B8	000F		1392	C	MOV	AX,0FH
0493	80	0E	0487 R 60	1393	C	OR	INFO,060H
0498	B8	000F		1394	C	MOV	AX,0FH
0498	EB	0D		1395	C	JMP	SHORT D_OUT_M
049D				1396	C		
049D	80	26	0410 R CF	1397	C		
04A2	80	0E	0410 R 20	1398	C	AND	EQUIP_LOW,020H
04A7	B8	000E		1399	C	MOV	AX,0EH
04AA				1400	C		
04AA	CD	10		1401	C	INT	10H
04AC	83	EC	06	1402	C	SUB	SP,6
04AF	8B	EC		1403	C	MOV	BP,SP
04B1	B8	A000		1404	C	MOV	AX,0A000H
				1405	C	ASSUME	DS:NOTHING,ES:NOTHING
04B4	8E	D8		1406	C	MOV	DS,AX
04B6	8E	C0		1407	C	MOV	ES,AX
04B8	C7	46	02 0000	1408	C	WORD	PTR[BP][2],0
04BD	C7	46	04 0000	1409	C	WORD	PTR[BP][4],0
04C2	B6	03		1410	C	MOV	DH,3
04C4	B2	C4		1411	C	MOV	DL,SEQ_ADDR
04C6	B8	0201		1412	C	MOV	AX,0201H
04C9	E8	0D15	R	1413	C	CALL	OUT_DX
04CC	B2	CE		1414	C	MOV	DL,GRAPH_ADDR
04CE	B8	0400		1415	C	MOV	AX,0400H
04D1	E8	0D15	R	1416	C	CALL	OUT_DX
04D4	52			1417	C	DX	PUSH
04D5	B2	DA		1418	C	MOV	DL,ATTR_READ
04D7	EC			1419	C	IN	AL,DX
04DB	B2	C0		1420	C	MOV	DL,ATTR_WRITE
04DA	B8	3200		1421	C	MOV	AX,3200H
04DD	E8	0D15	R	1422	C	CALL	OUT_DX
04E0	E8	068F	R	1423	C	CALL	HOW_BIG
04E3	80	FC	00	1424	C	CMF	AH,0
04E6	74	03		1425	C	JZ	AA1
04E8	E9	05CD	R	1426	C	JMP	EGA_MEM_ERROR
04EB				1427	C		
04EB	E8	05D9	R	1428	C	CALL	MEMORY_OK
04EE	80	FC	00	1429	C	CMF	AH,0
04F1	74	03		1430	C	JZ	AA2
04F3	E9	05CD	R	1431	C	JMP	EGA_MEM_ERROR
04F6				1432	C		
04F6	5A			1433	C	POP	DX
04F7	B2	C4		1434	C	MOV	DL,SEQ_ADDR
04F9	B8	0202		1435	C	MOV	AX,0202H
04FC	E8	0D15	R	1436	C	CALL	OUT_DX
04FF	B2	CE		1437	C	MOV	DL,GRAPH_ADDR
0501	B8	0401		1438	C	MOV	AX,0401H
0504	E8	0D15	R	1439	C	CALL	OUT_DX
0507	52			1440	C	DX	PUSH
0508	B2	DA		1441	C	MOV	DL,ATTR_READ
050A	EC			1442	C	IN	AL,DX
050B	B2	C0		1443	C	MOV	DL,ATTR_WRITE
050D	B8	3200		1444	C	MOV	AX,3200H
0510	E8	0D15	R	1445	C	CALL	OUT_DX
0513	C7	46	04 0000	1446	C	WORD	PTR[BP][4],0
0518	E8	068F	R	1447	C	CALL	HOW_BIG
051B	80	FC	00	1448	C	CMF	AH,0
051E	74	03		1449	C	JZ	AA3
0520	E9	05CD	R	1450	C	JMP	EGA_MEM_ERROR
0523				1451	C		
0523	E8	05D9	R	1452	C	CALL	MEMORY_OK
0526	80	FC	00	1453	C	CMF	AH,0
0529	74	03		1454	C	JZ	AA4
052B	E9	05CD	R	1455	C	JMP	EGA_MEM_ERROR
052E				1456	C		
052E	5A			1457	C	POP	DX
052F	B2	C4		1458	C	MOV	DL,SEQ_ADDR
0531	B8	0204		1459	C	MOV	AX,0204H
0534	E8	0D15	R	1460	C	CALL	OUT_DX
0537	52			1461	C	DX	PUSH
0538	B2	CE		1462	C	MOV	DL,GRAPH_ADDR
053A	B8	0402		1463	C	MOV	AX,0402H
053D	E8	0D15	R	1464	C	CALL	OUT_DX
0540	B2	DA		1465	C	MOV	DL,ATTR_READ
0542	EC			1466	C	IN	AL,DX
0543	B2	C0		1467	C	MOV	DL,ATTR_WRITE
0545	B8	3200		1468	C	MOV	AX,3200H
0548	E8	0D15	R	1469	C	CALL	OUT_DX
0548	C7	46	04 0000	1470	C	WORD	PTR[BP][4],0
0550	E8	068F	R	1471	C	CALL	HOW_BIG
0553	80	FC	00	1472	C	CMF	AH,0
0556	74	03		1473	C	JZ	AA5
0558	E8	73 90		1474	C	JMP	EGA_MEM_ERROR
055B				1475	C		
0558	E8	05D9	R	1476	C	CALL	MEMORY_OK
055E	80	FC	00	1477	C	CMF	AH,0
0561	74	03		1478	C	JZ	AA6
0563	E8	68 90		1479	C	JMP	EGA_MEM_ERROR
0566				1480	C		
0566	5A			1481	C	POP	DX
0567	B2	C4		1482	C	MOV	DL,SEQ_ADDR
0569	B8	0208		1483	C	MOV	AX,0208H
056C	E8	0D15	R	1484	C	CALL	OUT_DX
056F	B2	CE		1485	C	MOV	DL,GRAPH_ADDR
0571	B8	0403		1486	C	MOV	AX,0403H
0574	E8	0D15	R	1487	C	CALL	OUT_DX
0577	52			1488	C	DX	PUSH
0578	B2	DA		1489	C	MOV	DL,ATTR_READ
057A	EC			1490	C	IN	AL,DX
057B	B2	C0		1491	C	MOV	DL,ATTR_WRITE
057D	B8	3200		1492	C	MOV	AX,3200H
0580	E8	0D15	R	1493	C	CALL	OUT_DX
0583	C7	46	04 0000	1494	C	WORD	PTR[BP][4],0
0588	E8	068F	R	1495	C	CALL	HOW_BIG
058B	80	FC	00	1496	C	CMF	AH,0
058E	75	3D		1497	C	JNZ	EGA_MEM_ERROR
0590	E8	05D9	R	1498	C	CALL	MEMORY_OK
0593	80	FC	00	1499	C	CMF	AH,0
0596	75	35		1500	C	JNZ	EGA_MEM_ERROR
0598	55			1501	C	PUSH	BP
0599	B0	0000		1502	C	MOV	BP,0
059C				1503	C		
059C	5E			1504	C	POP	SI
059D	5A			1505	C	POP	DX
059E	E8	OCFE	R	1506	C	CALL	DDS
				1507	C	ASSUME	DS:ABSO
05A1	36:	BB 5C 02		1508	C	MOV	BX,WORD PTR SS:[SI][2]
05A5	B1	06		1509	C	MOV	CL,06H
05A7	D3	EB		1510	C	SHR	BX,CL
05A9	4B			1511	C	DEC	BX
05AA	B1	05		1512	C	MOV	CL,05H

```

; INTERNAL COLOR MODE
; TEST IN COLOR

; RESERVE 3 WORDS ON STACK
; SET BP
; PUT BUFFER ADDRESS IN AX

; SET UP SEG REGS TO POINT
; TO BUFFER AREA
; INITIALIZE
; INITIALIZE

; ADDRESS READ MAP SELECT

; SET UP ATTRIBUTE
; ATTRIBUTE WRITE ADDRESS

; GO FIND AMOUNT OF MEMORY

; GO TEST IT

; ADDRESS OF READ MAP

; SET UP ATTRIBUTE
; ATTRIBUTE WRITE ADDRESS

; INITIALIZE
; GO FIND AMOUNT OF MEMORY

; GO TEST IT

; ADDRESS OF READ MAP

; SET UP ATTRIBUTE
; ATTRIBUTE WRITE ADDRESS

; INITIALIZE
; GO FIND AMOUNT OF MEMORY

; GO TEST IT

; ADDRESS OF READ MAP

; SET UP ATTRIBUTE
; ATTRIBUTE WRITE ADDRESS

; INITIALIZE
; GO FIND AMOUNT OF MEMORY

; GO TEST IT

; SAVE SCRATCH PAD POINTER
; RESET BP FOR XT

; RESTORE

; SET DATA SEGMENT

; GET EGA MEMORY SIZE
; DIVIDE BY 64 TO GET
; NUMBER OF 64KB BLOCKS

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05AC D3 E3          1513 C SHL    BX,CL
05AE 80 E3 60      1514 C AND    BL,01100000B      ; ISOLATE BITS 5 AND 6
05B1 80 26 0487 R 9F 1515 C AND    INFO,10011111B
05B6 08 1E 0487 R  1516 C OR     INFO,BL
05BA 80 0E 0487 R 04 1517 C OR     INFO,00000100B      ; 04H SET 3XX ACTIVE
05BF 8A 1E 0488 R  1518 C MOV    BL,INFO_3
05C3 E8 00F3 R      1519 C CALL   MK_ENV
05C6 83 C4 06      1520 C ADD    SP,6
05C9 1F             1521 C POP    DS
05CA E9 0091 R      1522 C JMP     SKIP
05CD 8A 0103        1523 C EGA_MEM_ERROR: MOV    DX,0103H
05D0 E8 06C8 R      1524 C CALL   ERR_BEEP
05D3 55             1525 C PUSH   BP
05D4 80 0001        1526 C MOV    BP,1
05D7 EB C3          1527 C JMP     EGA_MEM_EXIT
05D8 EB C3          1528 C
05D9 EB C3          1529 C
05DA EB C3          1530 C
05DB EB C3          1531 C
05DC EB C3          1532 C
05DD EB C3          1533 C
05DE EB C3          1534 C
05DF EB C3          1535 C
05E0 EB C3          1536 C
05E1 EB C3          1537 C
05E2 EB C3          1538 C
05E3 EB C3          1539 C
05E4 EB C3          1540 C
05E5 EB C3          1541 C
05E6 EB C3          1542 C
05E7 EB C3          1543 C
05E8 EB C3          1544 C
05E9 EB C3          1545 C
05EA EB C3          1546 C
05EB EB C3          1547 C
05EC EB C3          1548 C
05ED EB C3          1549 C
05EE EB C3          1550 C
05EF EB C3          1551 C
05F0 EB C3          1552 C
05F1 EB C3          1553 C
05F2 EB C3          1554 C
05F3 EB C3          1555 C
05F4 EB C3          1556 C
05F5 EB C3          1557 C
05F6 EB C3          1558 C
05F7 EB C3          1559 C
05F8 EB C3          1560 C
05F9 EB C3          1561 C
05FA EB C3          1562 C
05FB EB C3          1563 C
05FC EB C3          1564 C
05FD EB C3          1565 C
05FE EB C3          1566 C
05FF EB C3          1567 C
0600 EB C3          1568 C
0601 EB C3          1569 C
0602 EB C3          1570 C
0603 EB C3          1571 C
0604 EB C3          1572 C
0605 EB C3          1573 C
0606 EB C3          1574 C
0607 EB C3          1575 C
0608 EB C3          1576 C
0609 EB C3          1577 C
060A EB C3          1578 C
060B EB C3          1579 C
060C EB C3          1580 C
060D EB C3          1581 C
060E EB C3          1582 C
060F EB C3          1583 C
0610 EB C3          1584 C
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0664 EB C3          1668 C

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0666 74 02 1639 C JZ PODSTG_ERR1
0668 B4 01 1640 C MOV AH,1 ; SET HIGH BYTE ERROR
066A 0A C9 1641 C OR CL,CL ; LOW BYTE ERROR?
066C 74 03 1643 C JZ PODSTG_ERR2
066E 80 C4 02 1644 C ADD AH,2
0671 50 1645 C PODSTG_ERR2:
0673 FC 1646 C POP BP
0675 C3 1647 C CLOD
0677 1648 C RET
0679 1649 C PODSTG_5:
067B 50 1650 C PUSH AX
067D 52 1651 C PUSH DX
067F B6 03 1652 C MOV DH,3
0681 B2 C4 1653 C MOV DL,SEQ_ADDR
0683 BB 020F 1654 C MOV AX,020FH
0685 E8 0D15 R 1655 C OUT_DX
0687 5A 1656 C POP DX
0689 58 1657 C POP AX
068B F3 AB 1658 C REP STOSW
068D E8 0CFE R 1659 C CALL DDS
068F 1660 C ASSUME DS:ABSO
0691 1661 C DS:RESET_FLAG,BX
0693 1662 C MOV DS,DX
0695 1663 C JMP PODSTG_ERR2
0697 1664 C PODSTG_ENDP
0699 1665 C ;----- DETERMINE SIZE OF BUFFER
069B 1666 C
069D 1667 C
069F 1668 C
06A1 8C DA 1669 C HOW_BIG PROC NEAR
06A3 2B DB 1670 C SUB DX,DS
06A5 1671 C MOV BX,BX
06A7 1672 C
06A9 8E C2 1673 C
06AB 2B FF 1674 C
06AD 8A A55 1675 C
06AF BB C8 1676 C
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0F71 2751 C
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1765
1766 C INCLUDE VPARMS. INC
1767 C SUBTTL VPARMS. INC
1768 C PAGE
1769 C VIDEO_PARMS LABEL BYTE
1770
1771 C ; STRUCTURE OF THIS TABLE
1772 C ;
1773 C ; COLUMNS, ROWS, PELS PER CHARACTER
1774 C ; PAGE LENGTH
1775 C ; SEQUENCER PARAMETERS
1776 C ; MISCELLANEOUS REGISTER
1777 C ; CRIC PARAMETERS
1778 C ; ATTRIBUTE PARAMETERS
1779 C ; GRAPHICS PARAMETERS
1780
1781 C BASE_1 EQU $ - VIDEO_PARMS
1782 C BASE_1_L LABEL BYTE
1783
1784 C ;----- DEFAULT MODES
1785 C ;
1786 C ;--0--
1787 C DB 400,240,08D
1788 C DW 00800H
1789
1790 C TFS_LEN EQU $ - BASE_1_L
1791 C
1792 C SEQ_PARMS LABEL BYTE
1793 C DB 00BH,003H,000H,003H
1794 C M1 EQU $ - SEQ_PARMS
1795 C
1796 C DB 023H
1797 C
1798 C CRT_PARMS LABEL BYTE
1799 C DB 037H,027H,02DH,037H,031H,015H
1800 C DB 004H,011H,000H,007H,006H,007H
1801 C DB 000H,000H,000H,000H,0E1H,024H
1802 C DB 0C7H,014H,008H,0E0H,0F0H,0A3H
1803 C DB 0FFH
1804 C M4 EQU $-CRT_PARMS
1805 C
1806 C LN_4 EQU $ - BASE_1_L
1807 C
1808 C ATTR_PARMS LABEL BYTE
1809 C DB 000H,001H,002H,003H,004H,005H
1810 C DB 006H,007H,010H,011H,012H,013H
1811 C DB 014H,015H,016H,017H,008H,000H
1812 C DB 00FH,000H
1813 C M5 EQU $-ATTR_PARMS
1814 C
1815 C LN_2 EQU $ - BASE_1_L
1816 C GRAPH_PARMS LABEL BYTE
1817 C DB 000H,000H,000H,000H,000H,010H
1818 C DB 00CH,000H,0FFH
1819 C M6 EQU $-GRAPH_PARMS
1820 C
1821 C M_TBL_LEN EQU $ - BASE_1_L
1822 C
1823 C ;--1--
1824 C DB 400,240,08D
1825 C DW 00800H
1826 C
1827 C DB 00BH,003H,000H,003H
1828 C
1829 C DB 023H
1830 C
1831 C DB 037H,027H,02DH,037H,031H,015H
1832 C DB 004H,011H,000H,007H,006H,007H
1833 C DB 000H,000H,000H,000H,0E1H,024H
1834 C DB 0C7H,014H,008H,0E0H,0F0H,0A3H
1835 C DB 0FFH
1836 C
1837 C DB 000H,001H,002H,003H,004H,005H
1838 C DB 006H,007H,010H,011H,012H,013H
1839 C DB 014H,015H,016H,017H,008H,000H
1840 C DB 00FH,000H
1841 C
1842 C DB 000H,000H,000H,000H,000H,010H
1843 C DB 00EH,000H,0FFH
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1845 C ;--2--
1846 C DB 80D,24D,08D
1847 C DW 01000H
1848 C
1849 C DB 001H,003H,000H,003H
1850 C
1851 C DB 023H
1852 C
1853 C DB 070H,04FH,05CH,02FH,05FH,007H
1854 C DB 004H,011H,000H,007H,006H,007H
1855 C DB 000H,000H,000H,000H,0E1H,024H
1856 C DB 0C7H,028H,008H,0E0H,0F0H,0A3H
1857 C DB 0FFH
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1859 C DB 000H,001H,002H,003H,004H,005H
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1863 C
1864 C DB 000H,000H,000H,000H,000H,010H
1865 C DB 00EH,000H,0FFH
1866 C
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1869 C DW 01000H
1870 C
1871 C DB 001H,003H,000H,003H
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1873 C DB 023H
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1875 C DB 070H,04FH,05CH,02FH,05FH,007H
1876 C DB 004H,011H,000H,007H,006H,007H
1877 C DB 000H,000H,000H,000H,0E1H,024H
1878 C DB 0C7H,028H,008H,0E0H,0F0H,0A3H
1879 C DB 0FFH
1880 C
1881 C DB 000H,001H,002H,003H,004H,005H
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1884 C DB 00FH,000H
1885 C
1886 C DB 000H,000H,000H,000H,000H,010H
1887 C DB 00EH,000H,0FFH
1888 C
1889 C ;--4--
1890 C DB 400,240,08D

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081A	4000	1891	C	DW	04000H
081C	08 03 00 02	1892	C	DB	00BH, 003H, 000H, 002H
0820	23	1893	C	DB	023H
0821	37 27 20 37 30 14	1894	C	DB	037H, 027H, 020H, 037H, 030H, 014H
0827	04 11 00 01 00 00	1895	C	DB	004H, 011H, 000H, 001H, 000H, 000H
082D	00 00 00 00 E1 24	1896	C	DB	000H, 000H, 000H, 000H, 0E1H, 024H
0833	C7 14 00 E0 F0 A2	1897	C	DB	0C7H, 014H, 000H, 0E0H, 0F0H, 0A2H
0839	FF	1898	C	DB	0FFH
083A	00 13 15 17 02 04	1902	C	DB	000H, 013H, 015H, 017H, 002H, 004H
0840	06 07 10 11 12 13	1903	C	DB	006H, 007H, 010H, 011H, 012H, 013H
0846	14 15 16 17 01 00	1904	C	DB	014H, 015H, 016H, 017H, 001H, 000H
084C	03 00	1905	C	DB	003H, 000H
084E	00 00 00 00 00 30	1906	C	DB	000H, 000H, 000H, 000H, 000H, 030H
0854	0F 00 FF	1907	C	DB	00FH, 000H, 0FFH
0857	28 18 08	1908	C	DB	40D, 24D, 08D
085A	4000	1909	C	DW	04000H
085C	08 03 00 02	1910	C	DB	00BH, 003H, 000H, 002H
0860	23	1911	C	DB	023H
0861	37 27 20 37 30 14	1912	C	DB	037H, 027H, 020H, 037H, 030H, 014H
0867	04 11 00 01 00 00	1913	C	DB	004H, 011H, 000H, 001H, 000H, 000H
086D	00 00 00 00 E1 24	1914	C	DB	000H, 000H, 000H, 000H, 0E1H, 024H
0873	C7 14 00 E0 F0 A2	1915	C	DB	0C7H, 014H, 000H, 0E0H, 0F0H, 0A2H
0879	FF	1916	C	DB	0FFH
087A	00 13 15 17 02 04	1917	C	DB	000H, 013H, 015H, 017H, 002H, 004H
0880	06 07 10 11 12 13	1918	C	DB	006H, 007H, 010H, 011H, 012H, 013H
0886	14 15 16 17 01 00	1919	C	DB	014H, 015H, 016H, 017H, 001H, 000H
088C	03 00	1920	C	DB	003H, 000H
088E	00 00 00 00 00 30	1921	C	DB	000H, 000H, 000H, 000H, 000H, 030H
0894	0F 00 FF	1922	C	DB	00FH, 000H, 0FFH
0897	50 18 08	1923	C	DB	80D, 24D, 08D
089A	4000	1924	C	DW	04000H
089C	01 01 00 06	1925	C	DB	001H, 001H, 000H, 006H
08A0	23	1926	C	DB	023H
08A1	70 4F 59 20 5E 06	1927	C	DB	070H, 04FH, 059H, 020H, 05EH, 006H
08A7	04 11 00 01 00 00	1928	C	DB	004H, 011H, 000H, 001H, 000H, 000H
08AD	00 00 00 00 E0 23	1929	C	DB	000H, 000H, 000H, 000H, 0E0H, 023H
08B3	C7 28 00 0F EF 02	1930	C	DB	0C7H, 028H, 000H, 00FH, 0EFH, 0C2H
08B9	FF	1931	C	DB	0FFH
08BA	00 17 17 17 17 17	1932	C	DB	000H, 017H, 017H, 017H, 017H, 017H
08C0	17 17 17 17 17 17	1933	C	DB	017H, 017H, 017H, 017H, 017H, 017H
08C6	17 17 17 17 01 00	1934	C	DB	017H, 017H, 017H, 017H, 001H, 000H
08CC	01 00	1935	C	DB	001H, 000H
08CE	00 00 00 00 00 00	1936	C	DB	000H, 000H, 000H, 000H, 000H, 000H
08D4	0D 00 FF	1937	C	DB	00DH, 000H, 0FFH
08D7	50 18 0E	1938	C	DB	80D, 24D, 14D
08DA	1000	1939	C	DW	01000H
08DC	00 03 00 03	1940	C	DB	000H, 003H, 000H, 003H
08E0	A6	1941	C	DB	0A6H
08E1	60 4F 56 3A 51 60	1942	C	DB	060H, 04FH, 056H, 03AH, 051H, 060H
08E7	70 1F 00 0D 0B 0C	1943	C	DB	070H, 01FH, 000H, 000H, 00BH, 00CH
08ED	00 00 00 00 5E 2E	1944	C	DB	000H, 000H, 000H, 000H, 05EH, 02EH
08F3	50 28 0D 5E 6E A3	1945	C	DB	05DH, 028H, 000H, 05EH, 06EH, 0A3H
08F9	FF	1946	C	DB	0FFH
08FA	00 08 08 08 08 08	1947	C	DB	000H, 008H, 008H, 008H, 008H, 008H
0900	08 08 10 18 18 18	1948	C	DB	008H, 008H, 010H, 018H, 018H, 018H
0906	18 18 18 18 0E 00	1949	C	DB	018H, 018H, 018H, 018H, 00EH, 000H
090C	0F 0E	1950	C	DB	00FH, 00EH
090E	00 00 00 00 00 10	1951	C	DB	000H, 000H, 000H, 000H, 000H, 010H
0914	0A 00 FF	1952	C	DB	00AH, 000H, 0FFH
0917	28 18 08	1953	C	DB	40D, 24D, 08D
091A	4000	1954	C	DW	04000H
091C	00 00 00 03	1955	C	DB	000H, 000H, 000H, 003H
0920	23	1956	C	DB	023H
0921	37 27 20 37 31 15	1957	C	DB	037H, 027H, 020H, 037H, 031H, 015H
0927	04 11 00 07 06 07	1958	C	DB	004H, 011H, 000H, 007H, 006H, 007H
092D	00 00 00 00 E1 24	1959	C	DB	000H, 000H, 000H, 000H, 0E1H, 024H
0933	C7 14 08 E0 F0 A3	1960	C	DB	0C7H, 014H, 008H, 0E0H, 0F0H, 0A3H
0939	FF	1961	C	DB	0FFH
093A	00 01 02 03 04 05	1962	C	DB	000H, 001H, 002H, 003H, 004H, 005H
0940	06 07 10 11 12 13	1963	C	DB	006H, 007H, 010H, 011H, 012H, 013H
0946	14 15 16 17 08 00	1964	C	DB	014H, 015H, 016H, 017H, 008H, 000H
094C	0F 00	1965	C	DB	00FH, 000H
094E	00 00 00 00 00 10	1966	C	DB	000H, 000H, 000H, 000H, 000H, 010H
0954	0E 00 FF	1967	C	DB	00EH, 000H, 0FFH
0957	28 18 08	1968	C	DB	40D, 24D, 08D
095A	4000	1969	C	DW	04000H
095C	00 00 00 03	1970	C	DB	000H, 000H, 000H, 003H
0960	23	1971	C	DB	023H
0961	37 27 20 37 31 15	1972	C	DB	037H, 027H, 020H, 037H, 031H, 015H
0967	04 11 00 07 06 07	1973	C	DB	004H, 011H, 000H, 007H, 006H, 007H
096D	00 00 00 00 E1 24	1974	C	DB	000H, 000H, 000H, 000H, 0E1H, 024H
0973	C7 14 08 E0 F0 A3	1975	C	DB	0C7H, 014H, 008H, 0E0H, 0F0H, 0A3H
0979	FF	1976	C	DB	0FFH
097A	00 01 02 03 04 05	1977	C	DB	000H, 001H, 002H, 003H, 004H, 005H
0980	06 07 10 11 12 13	1978	C	DB	006H, 007H, 010H, 011H, 012H, 013H
0986	14 15 16 17 08 00	1979	C	DB	014H, 015H, 016H, 017H, 008H, 000H
098C	0F 00	1980	C	DB	00FH, 000H

098E	00 00 00 00 00 10	2017	C		
099A	0E 00 FF	2018	C	DB	000H,000H,000H,000H,010H
		2019	C	DB	00EH,000H,0FFH
		2020	C		
		2021	C	;-A--	
0997	28 18 08	2022	C	DB	40D,24D,08D
099A	4000	2023	C	DW	04000H
		2024	C		
099C	00 00 00 03	2025	C	DB	000H,000H,000H,003H
		2026	C		
09A0	23	2027	C	DB	023H
		2028	C		
09A1	37 27 2D 37 31 15	2029	C	DB	037H,027H,02DH,037H,031H,015H
09A7	04 11 00 07 06 07	2030	C	DB	004H,011H,000H,007H,006H,007H
09AD	00 00 00 00 E1 24	2031	C	DB	000H,000H,000H,000H,0E1H,024H
09B3	C7 14 08 0E F0 A3	2032	C	DB	0C7H,014H,008H,0E0H,0F0H,0A3H
09B9	FF	2033	C	DB	0FFH
		2034	C		
09BA	00 01 02 03 04 05	2035	C	DB	000H,001H,002H,003H,004H,005H
09C0	06 07 10 11 12 13	2036	C	DB	006H,007H,010H,011H,012H,013H
09C6	14 15 16 17 08 00	2037	C	DB	014H,015H,016H,017H,008H,000H
09CC	0F 00	2038	C	DB	00FH,000H
		2039	C		
09CE	00 00 00 00 00 10	2040	C	DB	000H,000H,000H,000H,000H,010H
09D4	0E 00 FF	2041	C	DB	00EH,000H,0FFH
		2042	C		
		2043	C	;-B--	
09D7	50 18 08	2044	C	DB	80D,24D,08D
09DA	1000	2045	C	DW	01000H
		2046	C		
09DC	01 04 00 07	2047	C	DB	001H,004H,000H,007H
		2048	C		
09E0	23	2049	C	DB	023H
		2050	C		
09E1	70 4F 5C 2F 5F 07	2051	C	DB	070H,04FH,05CH,02FH,05FH,007H
09E7	04 11 00 07 06 07	2052	C	DB	004H,011H,000H,007H,006H,007H
09ED	00 00 00 00 E1 24	2053	C	DB	000H,000H,000H,000H,0E1H,024H
09F3	C7 28 08 0E F0 A3	2054	C	DB	0C7H,028H,008H,0E0H,0F0H,0A3H
09F9	FF	2055	C	DB	0FFH
		2056	C		
09FA	00 00 00 00 00 00	2057	C	DB	000H,000H,000H,000H,000H,000H
0A00	00 00 00 00 00 00	2058	C	DB	000H,000H,000H,000H,000H,000H
0A06	00 00 00 00 00 00	2059	C	DB	000H,000H,000H,000H,000H,000H
0A0C	0F 00	2060	C	DB	00FH,000H
		2061	C		
0A0E	00 00 00 00 00 00	2062	C	DB	000H,000H,000H,000H,000H,000H
0A14	04 00 FF	2063	C	DB	004H,000H,0FFH
		2064	C		
		2065	C	;-C--	
0A17	50 18 0E	2066	C	DB	80D,24D,14D
0A1A	1000	2067	C	DW	01000H
		2068	C		
0A1C	00 04 00 07	2069	C	DB	000H,004H,000H,007H
		2070	C		
0A20	A6	2071	C	DB	0A6H
		2072	C		
0A21	60 4F 56 3A 51 60	2073	C	DB	060H,04FH,056H,03AH,051H,060H
0A27	70 1F 00 00 08 0C	2074	C	DB	070H,01FH,000H,000H,008H,00CH
0A2D	00 00 00 00 5E 2E	2075	C	DB	000H,000H,000H,000H,05EH,02EH
0A33	50 28 00 5E 6E A3	2076	C	DB	05DH,028H,00DH,05EH,06EH,0A3H
0A39	FF	2077	C	DB	0FFH
		2078	C		
0A3A	00 00 00 00 00 00	2079	C	DB	000H,000H,000H,000H,000H,000H
0A40	00 00 00 00 0E 00	2080	C	DB	000H,000H,000H,000H,00EH,000H
0A4C	0F 08	2081	C	DB	00FH,008H
		2082	C		
0A4E	00 00 00 00 00 00	2083	C	DB	000H,000H,000H,000H,000H,000H
0A54	04 00 FF	2084	C	DB	004H,000H,0FFH
		2085	C		
		2086	C	;-D--	
0A57	28 18 08	2087	C	DB	40D,24D,08D
0A5A	2000	2088	C	DW	02000H
		2089	C		
0A5C	08 0F 00 06	2090	C	DB	008H,00FH,000H,006H
		2091	C		
0A60	23	2092	C	DB	023H
		2093	C		
0A61	37 27 2D 37 30 14	2094	C	DB	037H,027H,02DH,037H,030H,014H
0A67	04 11 00 00 00 00	2095	C	DB	004H,011H,000H,000H,000H,000H
0A6D	00 00 00 00 E1 24	2096	C	DB	000H,000H,000H,000H,0E1H,024H
0A73	C7 14 08 0E F0 E3	2097	C	DB	0C7H,014H,008H,0E0H,0F0H,0E3H
0A79	FF	2098	C	DB	0FFH
		2099	C		
0A7A	00 01 02 03 04 05	2100	C	DB	000H,001H,002H,003H,004H,005H
0A80	06 07 10 11 12 13	2101	C	DB	006H,007H,010H,011H,012H,013H
0A86	14 15 16 17 01 00	2102	C	DB	014H,015H,016H,017H,001H,000H
0A8C	0F 00	2103	C	DB	00FH,000H
		2104	C		
0A8E	00 00 00 00 00 00	2105	C	DB	000H,000H,000H,000H,000H,000H
0A94	05 0F FF	2106	C	DB	005H,00FH,0FFH
		2107	C		
		2108	C	;-E--	
0A97	50 18 08	2109	C	DB	80D,24D,08D
0A9A	4000	2110	C	DW	04000H
		2111	C		
0A9C	01 0F 00 06	2112	C	DB	001H,00FH,000H,006H
		2113	C		
0AA0	23	2114	C	DB	023H
		2115	C		
0AA1	70 4F 59 2D 5E 06	2116	C	DB	070H,04FH,059H,02DH,05EH,006H
0AA7	04 11 00 00 00 00	2117	C	DB	004H,011H,000H,000H,000H,000H
0AAD	00 00 00 00 E0 23	2118	C	DB	000H,000H,000H,000H,0E0H,023H
0AB3	C7 28 00 0F EF E3	2119	C	DB	0C7H,028H,000H,00FH,0EFH,0E3H
0AB9	FF	2120	C	DB	0FFH
		2121	C		
0ABA	00 01 02 03 04 05	2122	C	DB	000H,001H,002H,003H,004H,005H
0AC0	06 07 10 11 12 13	2123	C	DB	006H,007H,010H,011H,012H,013H
0AC6	14 15 16 17 01 00	2124	C	DB	014H,015H,016H,017H,001H,000H
0ACC	0F 00	2125	C	DB	00FH,000H
		2126	C		
0ACE	00 00 00 00 00 00	2127	C	DB	000H,000H,000H,000H,000H,000H
0AD4	05 0F FF	2128	C	DB	005H,00FH,0FFH
		2129	C		
		2130	C	;-F--	
0AD7	50 18 0E	2131	C	DB	80D,24D,14D
0ADA	8000	2132	C	DW	08000H
		2133	C		
0ADC	05 0F 00 00	2134	C	DB	005H,00FH,000H,000H
		2135	C		
0AEO	A2	2136	C	DB	0A2H
		2137	C		
0AE1	60 4F 56 1A 50 E0	2138	C	DB	060H,04FH,056H,01AH,050H,0E0H
0AE7	70 1F 00 00 08 0C	2139	C	DB	070H,01FH,000H,000H,008H,00CH
0AE9	00 00 00 00 5E 2E	2140	C	DB	000H,000H,000H,000H,05EH,02EH
0AF3	50 14 00 5E 6E 8B	2141	C	DB	05DH,014H,00DH,05EH,06EH,08BH
0AF9	FF	2142	C	DB	0FFH
			C		
0AFA	00 08 00 00 18 18		C	DB	000H,008H,000H,000H,018H,018H
0B00	00 00 00 08 00 00		C	DB	000H,000H,000H,008H,000H,000H



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0B06 00 18 00 00 0B 00 2143 C DB 000H,018H,000H,000H,00BH,000H
0B0C 05 00 2144 C DB 005H,000H
0B0E 00 00 00 00 00 10 2145 C DB 000H,000H,000H,000H,000H,010H
0B14 07 0F FF 2146 C DB 007H,00FH,0FFH
0B17 50 18 0E 2147 C ;--10-- DB 80D,24D,14D
0B1A 8000 2148 C DW 08000H
0B1C 05 0F 00 00 2149 C DB 005H,00FH,000H,000H
0B20 A7 2150 C DB 0A7H
0B21 5B 4F 53 17 50 8A 2151 C DB 05BH,04FH,053H,017H,050H,0BAH
0B27 6C 1F 00 00 00 00 2152 C DB 06CH,01FH,000H,000H,000H,000H
0B2D 00 00 00 00 5E 2B 2153 C DB 000H,000H,000H,000H,05EH,02BH
0B31 50 14 0F 5F 0A 8B 2154 C DB 05DH,014H,00FH,05FH,00AH,08BH
0B39 FF 2155 C DB 0FFH
0B3A 00 01 00 00 04 07 2156 C DB 000H,001H,000H,000H,004H,007H
0B40 00 00 00 01 00 00 2157 C DB 000H,000H,000H,001H,000H,000H
0B46 04 07 00 00 01 00 2158 C DB 004H,007H,000H,000H,001H,000H
0B4C 05 00 2159 C DB 005H,000H
0B4E 00 00 00 00 00 10 2160 C DB 000H,000H,000H,000H,000H,010H
0B54 07 0F FF 2161 C DB 007H,00FH,0FFH
= 0440 2162 C
2170 C BASE_2 EQU $ - VIDEO_PARMS
2171 C ;----- > 16K MODE VALUES
2172 C ;--F-- DB 80D,24D,14D
2173 C DW 08000H
0B57 50 18 0E 2174 C DB 001H,00FH,000H,006H
0B5A 8000 2175 C DB 0A2H
0B5C 01 0F 00 06 2176 C DB 060H,04FH,056H,03AH,050H,060H
0B60 A2 2177 C DB 070H,01FH,000H,000H,000H,000H
0B61 60 4F 56 3A 50 60 2178 C DB 000H,000H,000H,000H,05EH,02EH
0B67 70 1F 00 00 00 00 2179 C DB 05DH,02BH,00DH,05EH,06EH,0E3H
0B6D 00 00 00 00 5E 2E 2180 C DB 0FFH
0B73 50 28 0D 5E 6E E3 2181 C DB 000H,00BH,000H,000H,018H,018H
0B79 FF 2182 C DB 000H,000H,000H,00BH,000H,000H
0B7A 00 08 00 00 18 18 2183 C DB 000H,018H,000H,000H,00BH,000H
0B80 00 00 00 08 00 00 2184 C DB 005H,000H
0B86 00 18 00 00 0B 00 2185 C DB 000H,000H,000H,000H,000H,000H
0B8C 05 00 2186 C DB 000H,000H,000H,000H,000H,000H
0B8E 00 00 00 00 00 00 2187 C DB 005H,00FH,0FFH
0B94 05 0F FF 2188 C ;--10-- DB 80D,24D,14D
0B97 50 18 0E 2189 C DW 08000H
0B9A 8000 2190 C DB 001H,00FH,000H,006H
0B9C 01 0F 00 06 2191 C DB 0A7H
0BA0 A7 2192 C DB 05BH,04FH,053H,037H,052H,000H
0BA1 5B 4F 53 37 52 00 2193 C DB 06CH,01FH,000H,000H,006H,007H
0BA7 6C 1F 00 00 00 00 2194 C DB 000H,000H,000H,000H,05EH,02BH
0BA8 00 00 00 00 5E 2B 2195 C DB 05DH,02BH,00FH,05FH,00AH,0E3H
0BB3 5D 28 0F 5F 0A E3 2196 C DB 0FFH
0BB9 FF 2197 C DB 000H,001H,002H,003H,004H,005H
0BBA 00 01 02 03 04 05 2198 C DB 014H,007H,03BH,039H,03AH,03BH
0BC0 14 07 38 39 3A 3B 2199 C DB 03CH,03DH,03EH,03FH,001H,000H
0BC6 3C 3D 3E 3F 08 00 2200 C DB 00FH,000H
0BCC 0F 00 2201 C DB 000H,000H,000H,000H,000H,000H
0BCE 00 00 00 00 00 00 2202 C DB 005H,00FH,0FFH
0BD4 05 0F FF 2203 C
2204 C BASE_3 EQU $ - VIDEO_PARMS
2205 C ;----- HI RES ALTERNATE VALUES
2206 C ;--0-- DB 40D,24D,14D
2207 C DW 08000H
0BD7 28 18 0E 2208 C DB 00BH,003H,000H,003H
0BDA 0800 2209 C DB 0A7H
0BDC 0B 03 00 03 2210 C DB 02DH,027H,02BH,02DH,02BH,06DH
0BE0 A7 2211 C DB 06CH,01FH,000H,00DH,006H,007H
0BE1 2D 27 2B 2D 2B 6D 2212 C DB 000H,000H,000H,000H,05EH,02BH
0BE7 6C 1F 00 00 06 07 2213 C DB 05DH,014H,00FH,05EH,00AH,0A3H
0BED 00 00 00 00 5E 2B 2214 C DB 0FFH
0BF3 5D 14 0F 5E 0A A3 2215 C DB 000H,001H,002H,003H,004H,005H
0BF9 FF 2216 C DB 014H,007H,03BH,039H,03AH,03BH
0BFA 00 01 02 03 04 05 2217 C DB 03CH,03DH,03EH,03FH,00BH,000H
0C00 14 07 38 39 3A 3B 2218 C DB 00FH,000H
0C06 3C 3D 3E 3F 08 00 2219 C DB 000H,000H,000H,000H,000H,010H
0C0C 0F 00 2220 C DB 00EH,000H,0FFH
0C0E 00 00 00 00 00 10 2221 C ;--1-- DB 40D,24D,14D
0C14 0E 00 FF 2222 C DW 08000H
0C17 28 18 0E 2223 C DB 00BH,003H,000H,003H
0C1A 0800 2224 C DB 0A7H
0C1C 0B 03 00 03 2225 C DB 02DH,027H,02BH,02DH,02BH,06DH
0C20 A7 2226 C DB 06CH,01FH,000H,00DH,006H,007H
0C21 2D 27 2B 2D 2B 6D 2227 C DB 000H,000H,000H,000H,05EH,02BH
0C27 6C 1F 00 00 06 07 2228 C DB 05DH,014H,00FH,05EH,00AH,0A3H
0C2D 00 00 00 00 5E 2B 2229 C DB 0FFH
0C31 5D 14 0F 5E 0A A3 2230 C DB 000H,001H,002H,003H,004H,005H
0C39 FF 2231 C DB 014H,007H,03BH,039H,03AH,03BH
0C3A 00 01 02 03 04 05 2232 C DB 03CH,03DH,03EH,03FH,00BH,000H
0C40 14 07 38 39 3A 3B 2233 C DB 00FH,000H
0C46 3C 3D 3E 3F 08 00 2234 C DB 000H,000H,000H,000H,000H,010H
0C4C 0F 00 2235 C DB 00EH,000H,0FFH
0C4E 00 00 00 00 00 10 2236 C ;--2-- DB 80D,24D,14D
0C54 0E 00 FF 2237 C
0C57 50 18 0E 2238 C
2239 C
2240 C
2241 C
2242 C
2243 C
2244 C
2245 C
2246 C
2247 C
2248 C
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2250 C
2251 C
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0038 EC 2395 IN AL,DX ; GET SETTING OF PORT
0039 8A E0 2396 MOV AH,AL ; SAVE THAT SETTING
003B 0C 03 2397 OR AL,03 ; TURN SPEAKER ON
003D E8 0D1E R 2398 CALL BP,1 ; SET CNT TO WAIT 500 MS
0040 2B C9 2399 SUB CX,CX
0042 2400
0042 E2 FE 2401 LOOP G7 ; DELAY BEFORE TURNING OFF
0044 FE CB 2402 DEC BL ; DELAY CNT EXPIRED?
0046 75 FA 2403 JNZ C7 ; NO-CONTINUE BEEPING SPK
0048 8A C4 2404 MOV AL,AH ; RECOVER VALUE OF PORT
004A E8 0D1E R 2405 CALL BP,1
004D 5A 2406 POP DX
004E C3 2407 RET ; RETURN TO CALLER
004F 2408 ENDP
2409
2410 ;----- FIND THE PARAMETER TABLE VECTOR IN THE SAVE TABLE
2411
2412
2413 SET_BASE PROC NEAR
2414 ASSUME DS:ABS0
2415 CALL DDS
2416 LES BX,SAVE_PTR ; GET PTR TP PTR TABLE
2417 LES BX,DWORD PTR ES:[BX] ; GET PARAMETER PTR
2418 RET
2419 SET_BASE ENDP
2420
2421 ;----- ESTABLISH ADDRESSING TO THE CORRECT MODE TABLE ENTRY
2422
2423 MAKE_BASE PROC NEAR
2424 ASSUME DS:ABS0
2425 PUSH CX
2426 CALL DLX
2427 MOV AH,CRT_MODE ; GET PARM TBL PTR
2428 TEST INFO,060H ; TEST FOR BASE CARD
2429 JZ B_M_1 ; MIN MEMORY
2430
2431 ;----- WE HAVE A MEMORY EXPANSION OPTION HERE
2432
2433 CMP AH,0FH
2434 JNE B_M_2
2435 ADD BX,BASE_2 - BASE_1
2436 JMP B_M_OUT
2437
2438 B_M_2: CMP AH,010H
2439 JNE B_M_1
2440 ADD BX,BASE_2 + M_TBL_LEN - BASE_1
2441 JMP B_M_OUT
2442
2443 B_M_1: CMP AH,03H
2444 JA B_M_3 ; SKIP ENHANCED PORTION
2445
2446 ;----- CHECK THE SWITCH SETTING FOR ENHANCEMENT
2447
2448 MOV AL,INFO_3
2449 AND AL,0FH
2450 CMP AL,03H ; SECONDARY EMULATE SETTING
2451 JE BRS ; PRIMARY EMULATE SETTING
2452 CMP AL,09H
2453 JE BRS
2454 JMP B_M_3
2455
2456 ;----- WE WILL PERFORM ENHANCEMENT
2457
2458 BRS: ADD BX,BASE_3 - BASE_1 ; VECTOR TO ENHANCEMENT TBL
2459
2460 B_M_3: MOV CL,CRT_MODE
2461 SUB CH,CH
2462 JCKZ B_M_4
2463
2464 ;----- THIS LOOP WILL MOVE THE PTR TO THE INDIVIDUAL MODE ENTRY
2465
2466 B_M_5: ADD BX,M_TBL_LEN ; LENGTH OF ONE MODE ENTRY
2467 LOOP B_M_5
2468
2469 B_M_4: B_M_OUT: POP DX
2470 POP CX
2471 RET
2472 MAKE_BASE ENDP
2473
2474 ;----- PROGRAM THE EGA REGISTERS FROM THE PARAMETER TABLE
2475
2476 SET_REGS PROC NEAR
2477 ASSUME DS:ABS0,ES:NOTHING
2478
2479 ;----- PROGRAM THE SEQUENCER
2480
2481
2482
2483
2484 ODA8 E8 005A R 2484 CALL MAKE_BASE ; GET TABLE PTR
2485 ODAE 83 C3 05 2485 ADD BX,TFS_LEN ; MODE TO SEQUENCER PARMS
2486 ODB1 B6 03 2486 MOV DH,3
2487 ODB3 B2 C4 2487 MOV DL,SEQ_ADDR
2488 ODB5 B8 0001 2488 MOV AX,0001H
2489 ODB8 FA 2489 CLI ; RESET SEQUENCER
2490 ODB9 E8 0D15 R 2490 CALL OUT_DX ; DISABLE INTERRUPTS
2491 ODBC 26: 8A 07 2491 MOV AL,ES:[BX] ; GET SEQUENCER VALUE
2492 ODBF FE C4 2492 INC AL ; NEXT INDEX
2493 ODC1 E8 0D15 R 2493 CALL OUT_DX ; SET IT
2494 ODC4 2494
2495 ODC6 FE C4 2495
2496 ODC7 26: 8A 07 2496 INC AH ; NEXT INDEX REGISTER
2497 ODC8 E8 0D15 R 2497 MOV BX,AL,ES:[BX] ; NEXT TABLE ENTRY
2498 ODCD 80 FC 05 2498 CALL OUT_DX
2499 ODD0 72 F2 2500 CMP AH,M1+1
2501 JB D1
2502
2503 MOV AL,ES:[BX]
2504 INC BX
2505 MOV DL,MISC_OUTPUT
2506 OUT DX,AL
2507 MOV DL,SEQ_ADDR
2508 MOV AX,0003H
2509 CALL OUT_DX ; START SEQUENCER
2510 STI ; ENABLE INTERRUPTS
2511
2512 ;----- PROGRAM THE CRT CONTROLLER
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0DF4 72 F2 2521 JB X1 ; DO THE REST
0DF6 26: 8B 47 F1 2522 MOV AX,ES:[BX][(-OFH)] ; GET CURSOR MODE
0DF8 86 E0 2523 XCHG AH,AL
0DFC A3 0460 R 2524 MOV CURSOR_MODE,AX ; SET LOW RAM VALUE
2525
;----- PROGRAM THE ATTRIBUTE CHIP
2526
0DF5 8B F3 2527 MOV SI,BX
0E01 E8 0D05 R 2528 CALL WHAT_BASE
0E04 EC 2529 IN AL,DX
0E05 B2 C0 2530 MOV DL,ATTR_WRITE
0E07 2A E4 2531 SUB AH,AH ; INDEX COUNTER
0E09 2532
0E09 26: 8A 07 2533 MOV AL,ES:[BX] ; GET DATA VALUE
0E0C 86 E0 2534 XCHG AH,AL
0E0E EE 2535 OUT DX,AL
0E0F 86 E0 2536 XCHG AH,AL
0E11 EE 2537 OUT DX,AL
0E12 43 2538 INC BX
0E13 FE C4 2539 INC AH ; NEXT DATA VALUE
0E15 80 FC 14 2540 INC AH ; NEXT INDEX VALUE
0E18 72 EF 2541 CMP AH,M5 ; TEST REGISTER COUNT
2542 JB D3 ; DO THE REST
2543
0E1A 80 00 2544 MOV AL,0
0E1C EE 2545 OUT DX,AL
2546
;----- CHECK IF PALETTE REGISTER VALUES ARE TO BE SAVED
2547
0E1D 1E 2548 PUSH DS
0E1E 06 2549 PUSH ES
0E1F C4 3E 04A8 R 2550 LES DI,SAVE_PTR ; GET TABLE PTR
0E23 26: C4 7D 04 2551 LES DI,DWORD PTR ES:[DI][4] ; GET PALETTE PTR
0E27 8C C0 2552 MOV AX,ES
0E29 0B C7 2553 OR AX,DI
0E2B 74 09 2554 JZ SAVE_OUT ; IF ZERO, NO SAVE OCCURS
2555
;----- STORE AWAY THE PALETTE VALUES IN RAM SAVE AREA
2556
0E2D 1F 2557 POP DS
0E2E 1E 2558 PUSH DS
0E2F B9 0010 2559 MOV CX,16D ; SAVE THE PALETTE REGS
0E32 F3/ A4 2560 REP MOVSB
0E34 46 2561 INC SI ; SAVE THE OVERSCAN REG
0E35 A4 2562 MOVSB
0E36 07 2563 SAVE_OUT: POP ES
0E37 1F 2564 POP DS
2565
;----- PROGRAM THE GRAPHICS CHIPS
2566
0E38 B2 CC 2567 MOV DL,GRAPH_1_POS
0E3A B0 00 2568 MOV AL,0
0E3C EE 2569 OUT DX,AL
0E3D B2 CA 2570 MOV DL,GRAPH_2_POS
0E3F B0 01 2571 MOV AL,0
0E41 EE 2572 OUT DX,AL
0E42 B2 CE 2573 MOV DL,GRAPH_ADDR
0E44 2A E4 2574 SUB AH,AH
0E46 2575
D4: 0E46 26: 8A 07 2580 MOV AL,ES:[BX] ; PARAMETER BYTE
0E49 E8 0D15 R 2581 CALL OUT_DX ; SET IT
0E4C 43 2582 INC BX ; NEXT BYTE
0E4F FE C4 09 2583 INC AH ; NEXT REGISTER
0E52 72 F2 2584 CMP AH,M6
0E54 C3 2585 JB D4 ; CONTINUE
0E55 2586 RET
2587
SET_REGS ENDP
2588
;----- MODE SET REGEN CLEAR ROUTINE
2589
0E55 2590
BLANK PROC NEAR ; FILL REGEN WITH BLANKS
2591 ASSUME DS:ABSO,ES:NOTHING
2592 MOV AL,INFO ; SEE IF BLANK IS TO OCCUR
0E58 A8 80 2593 TEST AL,080H ; MODE SET HIGH BIT
0E5A 75 39 2594 JNZ OUT_1 ; SKIP BLANK FOR REGEN
0E5C BA B800 2595 MOV DX,0B800H ; COLOR MODE REGEN ADDRESS
0E5F A0 0449 R 2596 MOV AL,CRT_MODE ; CURRENT MODE SET
0E62 3C 06 2597 CMP AL,6 ; 0-6 ARE COLOR MODES
0E64 76 0A 2598 JBE CGO ; MONOCHROME REGEN ADDRESS
0E66 BA 8000 2600 MOV DX,08000H ; MONOCHROME MODE
0E69 3C 07 2601 CMP AL,7 ; REMAINING MODES
0E6B 74 03 2602 JE CGO
0E6D BA A000 2603 MOV DX,0A000H
0E70 2604
CGO: 0E70 BB 0720 2605 MOV BX,0720H ; ALPHA BLANK VALUE
0E73 3C 04 2606 CMP AL,4 ; ALPHAMODES 0-3
0E75 72 06 2607 JB W1
0E77 3C 07 2608 CMP AL,7 ; ALPHA MODE
0E79 74 02 2609 JE W1
0E7B 2B DB 2610 SUB BX,BX ; GRAPHICS BLANK VALUE
0E7D 2611
W1: 0E7D 8E C2 2612 SRLoad ES ; SET THE REGEN SEGMENT
0E7F 8B 0E 044C R 2613 MOV ES,DX
0E83 E3 10 2614 MOV CX,CRT_LEN
0E85 B9 8000 2615 JCKZ OUT_1
0E88 80 FE A0 2616 MOV CX,0A000H
0E8B 74 02 2617 CMP DH,0A0H
0E8D B5 40 2618 JE N_BA
0E8F 8B C3 2619 MOV CH,0A0H
0E91 2B FF 2620 N_BA: MOV AX,BX ; BLANK VALUE
0E93 F3/ AB 2621 SUB SI,SI ; CLEAR POINTER
0E95 2622 REP STOSW ; CLEAR THE PAGE
0E96 2623
OUT_1: 0E96 2624 RET ; RETURN TO CALLER
2625
BLANK ENDP
2626
PH_5 PROC NEAR
2627 CALL PAL_ON
0E96 E8 10B7 R 2628 RET
0E99 C3 2629
PH_5 ENDP
2630
;----- SEE IF WE ARE TO SUPPORT 640 X 350 ON A 640 X 200 MODE
2631
0E9A 2632
BRST_DET PROC NEAR
2633 ASSUME DS:ABSO
0E9A 50 2634 PUSH AX
0E9B 1E 2635 PUSH DS
0E9C E8 0CFE R 2636 CALL DDS
0E9F A0 0488 R 2637 MOV AL,INFO_3
0EA2 1F 2638 POP DS
0EA3 24 0F 2639 AND AL,0FH
0EA5 3C 03 2640 CMP B,YES ; EMULATE MODE
0EA7 74 07 2641 JE YES
0EA9 3C 09 2642 CMP AL,09H ; EMULATE MODE
0EAB 74 03 2643 JE YES
2644

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OEAD 58      2647
OEAE F8      2648
OEAF C3      2649
OEBO         2650
OEBO 58      2651
OEBI F9      2652
OEBC C3      2653
OEBC         2654
OEBC         2655
OEBC         2656
OEBC         2657
OEBC         2658
OEBC         2659
OEBC         2660
OEBC FA 06 010C R 0000 E 2661
OEBA 8C 0E 010E R 2662
OEBC FB      2663
OEBC 80 26 0487 R F3 2664
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OEBC         2772

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POP AX
CLC
RET

B_YES: POP AX
        STC
        RET

BRST_DET ENDP

;----- MODE SET

AHO: ASSUME DS:ABS0
      CLI
      MOV WORD PTR GRX_SET, OFFSET CGDDOT
      MOV WORD PTR GRX_SET+2, CS
      STI
      AND INFO,11110011B
      ; TURN OFF RETRACE BIT
      ; EGA ACTIVE BIT
      ; SAVE
      PUSH AX
      TEST INFO,2
      JZ ST_1
      MOV AX,EQUIP_FLAG
      AND AL,030H
      CMP AL,030H
      JE ST_2
      ; THERE IS NO MONOCHROME
      ; THERE IS A MONOCHROME
      ; CHECK THE EQUIPMENT FLAG
      ; FOR MONOCHROME CALL
      ; IT IS A MONOCHROME CALL

;----- FALL THROUGH => REGULAR COLOR CARD SETUP
      MOV ROWS,0240
      MOV POINTS,8
      POP AX
      OR INFO,00001000B
      CMP AL,1
      ST_7: JBE ST_7
      CMP AL,4
      JAE ST_7
      OR INFO,00000100B
      INT 42H
      JMP V_RET
      ; RECOVER
      ; EGA NOT ACTIVE
      ; WAIT FOR RETRACE ON
      ; MODES 2,3 ONLY
      ; DO RETRACE
      ; OTHER ADAPTER MODE CALL
      ; BACK TO CALLER

;----- AT THIS POINT THERE IS NO MONOCHROME ATTACHED TO THE ADAPTER

ST_1: MOV AX,EQUIP_FLAG
      AND AL,030H
      CMP AL,030H
      JNE ST_3
      ; TEST THE EQUIPMENT FLAG
      ; TO SEE IF THIS IS A
      ; MONOCHROME SETUP CALL
      ; MUST BE COLOR TO CARD

;----- FALL THROUGH => REGULAR MONOCHROME CARD SETUP
      MOV ROWS,0240
      MOV POINTS,0140
      POP AX
      INT 42H
      MOV CURSOR_MODE,OBOCH
      OR INFO,8
      JMP V_RET
      ; RECOVER
      ; OTHER ADAPTER MODE CALL
      ; FIX PLANAR VALUE
      ; THE EGA IS NOT ACTIVE
      ; BACK TO CALLER

;----- MONOCHROME SETUP TO THE ADAPTER

ST_2: POP AX
      PUSH AX
      MOV DH,3
      AND AL,080H
      AND INFO,07FH
      OR INFO,AL
      POP AX
      AND AL,07FH
      CMP AL,0FH
      JE ST_2A
      MOV AL,7
      ; RECOVER
      ; SAVE
      ; PICK OFF THE CLEAR BIT
      ; MASK OFF THE OTHER BITS
      ; SAVE REGEN CLEAR BIT
      ; RECOVER TRUE CALL VALUE
      ; ALREADY DEALT WITH D7
      ; A MONOCHROME MODE
      ; DO THIS MODE
      ; REGULAR MONOCHROME

ST_2A: MOV CRT_MODE,AL
      MOV DL,CRTC_ADDR_B
      MOV ADDR_6845,DX
      JMP QQ1
      ; SAVE MODE VALUE
      ; IT IS 3-B-X
      ; SAVE CRTC ADDRESS
      ; CONTINUE THE MODE SET

;----- COLOR SETUP TO THE ADAPTER

ST_3: POP AX
      PUSH AX
      MOV DH,3
      AND AL,080H
      AND INFO,07FH
      OR INFO,AL
      POP AX
      AND AL,07FH
      MOV CRT_MODE,AL
      MOV DL,CRTC_ADDR
      MOV ADDR_6845,DX
      MOV CRT_START,0
      MOV ACTIVE_PAGE,0
      MOV ES,NOTHING
      ASSUME CX,8
      MOV DI,OFFSET CURSOR_POSN
      PUSH DS
      POP ES
      SUB AX,AX
      REP STOSW
      CALL MAKE_BASE
      MOV AL,ES:[BX]
      SUB AH,AH
      MOV CRT_COLS,AX
      MOV AL,ES:[BX][1]
      MOV ROWS,AL
      MOV AL,ES:[BX][2]
      SUB AH,AH
      MOV POINTS,AX
      MOV AX,ES:[BX][3]
      MOV CRT_LEN,AX
      SUB BX,BX
      AL,1
      MOV AH,CRT_MODE
      CMP AH,7
      JE ENTRY_2
      ; RECOVER PARAMETER VALUE
      ; SAVE IT
      ; ISOLATE REGEN CLEAR BIT
      ; PREPARE INFO BYTE
      ; SET IT, OR NOT
      ; RECOVER TRUE MODE CALL
      ; DONE WITH D7
      ; SAVE THIS MODE
      ; 3-D-X
      ; SAVE CRTC ADDRESS
      ; SAVE START ADDRESS
      ; RESET PAGE VALUE TO ZERO
      ; 8 PAGES OF CURSOR VALUES
      ; OFFSET
      ; ESTABLISH
      ; ADDRESSING
      ; 0 THOSE CURSOR LOCATIONS
      ; CLEAR OUT SAVED VALUES
      ; GET COLUMN COUNT
      ; ZERO HIGH BYTE
      ; STORE COLUMN VALUE
      ; GET ROW VALUE
      ; STORE ROW VALUE
      ; GET THE BYTES/CHAR
      ; ZERO HIGH BYTE
      ; STORE BYTES/CHAR
      ; GET PAGE SIZE
      ; STORE PAGE LENGTH
      ; ZERO
      ; MONOCHROME ALPHA CHAR GEN
      ; GET CURRENT MODE
      ; IS IT MONOCHROME
      ; 9X14 FONT

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0FA2	80 FC 03	2773	CMP	AH,03H	
0FA5	77 35	2774	JA	ENTRY_1	
		2775			
0FA7	E8 0E9A R	2776	CALL	BRST_DET	
0FAA	72 02	2777	JC	ENTRY_2	
		2778			
0FAC	80 02	2779	MOV	AL,2	; COLOR ALPHA CHAR GEN
0FAE		2780			
0FAE	E8 1EAE R	2781	CALL	CH_GEN	; LOAD ALPHA CHAR GEN
0FB1	E8 0CFE R	2782	CALL	DDS	
0FB4	8A 26 0449 R	2783	MOV	AH,CRT_MODE	
0FB8	80 FC 07	2784	CMP	AH,7	
0FB8	74 03	2785	JE	FDG_IT	; GET CURRENT MODE
0FB0	EB 1D 90	2786	JMP	ENTRY_1	; IS IT MONOCHROME
0FC0		2787			; 9X14 FONT
0FC0	BD 0000 E	2788	FDG_IT:	BP,OFFSET CGMN_FDG	; TABLE POINTER
0FC3	BB 0E00	2789	MOV	BX,0E00H	; 14 BYTES PER CHAR
0FC6		2790			
0FC6	0E	2791	FDG:	PUSH	
0FC7	07	2792		CS	; GET THE ROM SEGMENT
0FC8	26: 8B 56 00	2793	POP	ES	; INTO ES
0FCC	0B 02	2794	MOV	DX,ES:[BP]	; GET THE CHAR HEX CODE
0FCE	74 03	2795	OR	DX,DX	; ZERO = NO MORE CHARS
0FDD	89 0001	2796	JZ	ENTRY_1	; NO MORE
0FD3	45	2797	MOV	CX,1	; DO ONE CHAR AT A TIME
0FD4	E8 1EF6 R	2798	INC	BP	; MOVE TO FIRST CODE POINT
0FD7	83 C5 0E	2799	CALL	DO_MAP2	; STORE THE CODE POINT
0FDA	EB EA	2800	ADD	BP,0140	; ADJUST BP TO NEXT CODE
0FDC		2801	JMP	FDG	; DO ANOTHER
0FDC	E8 0DAB R	2802	ENTRY_1:	CALL	SET_REGS
0FDF	E8 0E55 R	2803		CALL	BLANK
0FE2	E8 0E96 R	2804		CALL	PH_5
		2805			; CLEAR OUT THE BUFFER
0FE5	E8 0CFE R	2806			
0FE8	80 3E 0449 R OF	2807	ASSUME	DS:ABSO	
0FED	72 06	2808	CALL	DDS	
0FEF	C7 06 010C R 0000 E	2809	CMP	CRT_MODE,OFH	
0FF5		2810	JB	MS_1	
0FF5	80 3E 0449 R 07	2811	MOV	WORD PTR GRX_SET , OFFSET CGMN	
0FFA	77 09	2812	CMP	CRT_MODE,7	
0FFC	74 4B	2813	JA	SAVE_GRP	
0FFE	80 3E 0449 R 03	2814	JE	SAVE_ALPH	
1003	76 44	2815	CMP	CRT_MODE,3	
1005		2816	JBE	SAVE_ALPH	
1005	C4 1E 0448 R	2817	SAVE_GRP:	BX,SAVE_PTR	
1009	83 C3 0C	2818		LES	BX,0CH
100C	26: C4 1F	2819	ADD	BX,0CH	
100F	8C C0	2820	LES	BX,DWORD PTR ES:[BX]	
1011	0B C3	2821	MOV	AX,ES	
1013	74 32	2822	OR	AX,BX	
1015	BE 0007	2823	JZ	J4J	; JMP AHO_DONE
1018		2824	MOV	SI,07H	
1018	26: 8A 00	2825	SG_1:	MOV	AL,ES:[BX][SI]
101B	3C FF	2826		CMP	AL,OFFH
101D	74 7A	2827		JE	AHO_DONE
101F	3A 06 0449 R	2828	CMP	AL,CRT_MODE	
1023	74 03	2829	JE	SG_2	
1025	46	2830	INC	SI	
1026	EB F0	2831	JMP	SG_1	
1028		2832			
1028	FA	2833	SG_2:	CLI	
1029	26: 8A 07	2834		MOV	AL,BYTE PTR ES:[BX]
102C	FE C8	2835		DEC	AL
102E	A2 0484 R	2836		MOV	ROWS,AL
1031	26: 8B 47 01	2837		MOV	AX,WORD PTR ES:[BX][1]
1035	A3 0485 R	2838		MOV	POINTS,AX
1038	26: 8B 47 03	2839		MOV	AX,WORD PTR ES:[BX][3]
103C	A3 010C R	2840		MOV	WORD PTR GRX_SET,AX
103F	26: 8B 47 05	2841		MOV	AX,WORD PTR ES:[BX][5]
1043	A3 010E R	2842		MOV	WORD PTR GRX_SET + 2,AX
1046	FB	2843		STI	
1047		2844	J4J:	JMP	SHORT AHO_DONE
1047	EB 50	2845			
1049		2846	SAVE_ALPH:	JMP	SHORT AHO_DONE
1049	C4 1E 0448 R	2847		LES	BX,SAVE_PTR
104D	83 C3 08	2848		ADD	BX,0BH
1050	26: C4 1F	2849		LES	BX,DWORD PTR ES:[BX]
1053	8C C0	2850		MOV	AX,ES
1055	0B C3	2851		OR	AX,BX
1057	74 40	2852		JZ	AHO_DONE
1059	BE 000B	2853		MOV	SI,0BH
105C		2854	SA_1:	MOV	AL,ES:[BX][SI]
105C	26: 8A 00	2855		CMP	AL,OFFH
105F	3C FF	2856		JE	AHO_DONE
1061	74 36	2857		CMP	AL,CRT_MODE
1063	3A 06 0449 R	2858		JE	SA_2
1067	74 03	2859		INC	SI
1069	46	2860		JMP	SA_1
106A	EB F0	2861			
106C		2862	SA_2:	MOV	AH,ES:[BX]
106C	26: 8A 27	2863		MOV	AL,ES:[BX][1]
106F	26: 8A 47 01	2864		MOV	CX,ES:[BX][2]
1073	26: 8B 4F 02	2865		MOV	DX,ES:[BX][4]
1077	26: 8B 57 04	2866		MOV	BP,ES:[BX][6]
1078	26: 8B 4F 06	2867		MOV	ES,ES:[BX][8]
107F	26: 8E 47 08	2868		PUSH	BX
1083	53	2869		MOV	BX,AX
1084	8B 06	2870		MOV	AX,1110H
1086	8B 1110	2871		INT	10H
1089	CD 10	2872		POP	BX
108B	5B	2873		MOV	AL,ES:[BX][0AH]
108C	26: 8A 47 0A	2874		CMP	AL,OFFH
1090	3C FF	2875		JE	AHO_DONE
1092	74 05	2876		AL	DEC
1094	FE C8	2877		MOV	ROWS,AL
1096	A2 0484 R	2878			
		2879			
		2880			
		2881			
		2882			
1099		2883			
1099	E8 0CFE R	2884	AHO_DONE:	CALL	DDS
109C	80 3E 0449 R 07	2885		CMP	CRT_MODE,7
10A1	77 1E	2886		JA	DNDOS
10A3	BB 10C8 R	2887		MOV	BX,OFFSET COMPAT_MODE
10A6	A0 0449 R	2888		MOV	AL,CRT_MODE
10A9	2A E4	2889		SUB	AH,AH
10AB	03 08	2890		ADD	BX,AX
10AD	2E: 8A 07	2891		MOV	AL,CS:[BX]
10B0	A2 0465 R	2892		MOV	CRT_MODE_SET,AL
10B3	80 30	2893		MOV	AL,030H
10B5	80 3E 0449 R 06	2894		CMP	CRT_MODE,6
10B8	75 02	2895		JNE	DO_PAL
10BC	80 3F	2896		MOV	AL,03FH
10BE		2897	DO_PAL:	MOV	CRT_PALETTE,AL
10BE	A2 0466 R	2898			

;----- SET THE LOW RAM VALUES FOR COMPATIBILITY (308 AND 309 SAVE BYTES)

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00000000 ONDCS:      MOV      CX,CURSOR_MODE
00000001      JMP      AH1
00000002 COMPAT_MODE LABEL    BYTE
00000003 DB          02CH,028H,02DH,029H,02AH,02EH
00000004 DB          01EH,029H
00000005 INCLUDE V1-5.INC
00000006 SUBTTL V1-5.INC
00000007 PAGE
00000008
00000009 CALC_CURSOR PROC     NEAR
00000010 ASSUME  DS:ABS0
00000011 CMP     CC,0          ; CHECK FOR FULL HEIGHT
00000012 JNE     CL,1          ; NORMAL CHECK
00000013 INC     CL             ; ADJUST END VALUE
00000014 JMP     SHORT  CALC_OUT
00000015
00000016 CC_1:    INC     CL             ; ADJUST FOR EGA REGISTERS
00000017 CMP     CL,BYTE PTR POINTS ; WILL IT WRAP
00000018 JB      CALC_OUT      ; NO, ITS OK
00000019 SUB     CL,CL          ; EGA METHOD FOR CURSOR END
00000020
00000021 CALC_OUT: PUSH    CX          ; SAVE CURSOR TYPE VALUE
00000022 SUB     CL,CH          ; END - START
00000023 CMP     CL,010H       ; LOW NIBBLE EQUAL
00000024 POP     CX             ; RESTORE
00000025 JNE     COMP_4         ;
00000026 INC     CL             ; ADD 1 FOR CORRECT CURSOR
00000027
00000028 COMP_4:  RET          ; BACK TO CALLER
00000029 CALC_CURSOR ENDP
00000030
00000031 -----
00000032 SET_CTYPE SET CURSOR TYPE :
00000033 INPUT     THIS ROUTINE SETS THE CURSOR VALUE :
00000034 (CX) HAS CURSOR VALUE CH-START LINE, CL-STOP LINE :
00000035 OUTPUT    NONE :
00000036 -----
00000037 CUT_OFF EQU 4
00000038
00000039 AH1:     ASSUME  DS:ABS0
00000040 MOV     AH,C CRSR_START ; CRTC REG FOR CURSOR SET
00000041 MOV     CURSOR_MODE,CX  ; SAVE IN DATA AREA
00000042 TEST    INFO,8          ; EGA ACTIVE BIT
00000043 JNZ     DO_SET          ; 0=EGA, 1=OLD CARDS
00000044
00000045 ;----- THIS SECTION WILL EMULATE CURSOR OFF ON THE EGA
00000046
00000047 MOV     AL,CH           ; GET START VALUE
00000048 AND     AL,060H        ; TURN OFF CURSOR ?
00000049 CMP     AL,020H        ; TEST THE BITS
00000050 JNE     AH1_A          ; SKIP CURSOR OFF
00000051 MOV     CX,01E00H      ; EMULATE CURSOR OFF
00000052 JMP     SHORT DO_SET
00000053
00000054 ;----- THIS SECTION : ADJUST THE CURSOR AND TEST FOR ENHANCED OPERATION
00000055
00000056 AH1_A:   TEST     INFO,1          ; CURSOR EMULATE BIT
00000057 JNZ     DO_SET          ; 0=EMULATE, 1=VALUE AS-IS
00000058 CMP     CRT_MODE,3      ; POSSIBLE EMULATION
00000059 AH1_A   CALL     BRST_DET ; NO, SET THE CURSOR TYPE
00000060 JNC     AH1_S           ; SEE IF EMULATE MODE
00000061 CMP     CH,CUT_OFF      ; NOT EMULATING
00000062 JBE     AH1_B           ; TEST START
00000063 ADD     CH,5            ; SKIP ADJUST
00000064
00000065 AH1_B:   CMP     CL,CUT_OFF      ; TEST END
00000066 JBE     AH1_S           ; SKIP ADJUST
00000067 ADD     CL,5
00000068
00000069 AH1_S:   CALL     CALC_CURSOR      ; ADJUST END REGISTER
00000070
00000071 DO_SET:  CALL     M16             ; OUTPUT CX REG
00000072 JMP     V_RET              ; RETURN TO CALLER
00000073
00000074 ;----- THIS ROUTINE OUTPUTS THE CX REGISTER TO THE CRTC REGS NAMED IN AH
00000075
00000076 M16:     MOV     DX,ADDR_6845    ; ADDRESS REGISTER
00000077 MOV     AL,CH                ; DATA
00000078 CALL    OUT_DX              ; OUTPUT THE VALUE
00000079 INC     AH                  ; NEXT REGISTER
00000080 MOV     AL,CL                ; SECOND DATA VALUE
00000081 CALL    OUT_DX              ; OUTPUT THE VALUE
00000082 RET                          ; ALL DONE
00000083
00000084 -----
00000085 POSITION  SET CURSOR POSITION :
00000086 THIS SERVICE ROUTINE CALCULATES THE REGEN BUFFER :
00000087 ADDRESS OF A CHARACTER IN THE ALPHA MODE :
00000088 INPUT    AX = ROW, COLUMN POSITION :
00000089 OUTPUT   AX = OFFSET OF CHAR POSITION IN REGEN BUFFER :
00000090
00000091 POSITION  PROC     NEAR
00000092 PUSH    BX                ; SAVE REGISTER
00000093 MOV     BX,AX
00000094 MOV     AL,AH
00000095 MUL     BYTE PTR CRT_COLS ; DETERMINE BYTES TO ROW
00000096 XOR     BH,BH             ; ZERO OUT
00000097 ADD     AX,BX             ; ADD IN COLUMN VALUE
00000098 SAL     AX,1              ; * 2 FOR ATTRIBUTE BYTES
00000099 POP     BX                ; RESTORE REGISTER
00000100 RET
00000101
00000102 POSITION  ENDP
00000103
00000104 -----
00000105 SET_CPOS SET CURSOR POSITION :
00000106 THIS ROUTINE SETS THE CURRENT CURSOR POSITION TO THE :
00000107 NEW X-Y VALUES PASSED :
00000108 INPUT    DX - ROW,COLUMN OF NEW CURSOR :
00000109 BH - DISPLAY PAGE OF CURSOR :
00000110 OUTPUT   CURSOR IS SET AT CRTC IF DISPLAY PAGE IS CURRENT :
00000111 DISPLAY :
00000112
00000113 AH2:     CALL     SET_CPOS

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115A E9 219E R      3025 C      JMP      V_RET
115D                3026 C
115D 8A CF          3027 C      SET_CPOS:
115F 32 ED          3028 C      MOV      CL,BH
1161 D1 E1          3029 C      XOR      CH,CH
1163 8B F1          3030 C      SAL      CX,1
1165 89 94 0450 R   3031 C      MOV      SI,CX
1169 38 3E 0462 R   3032 C      MOV      [SI+OFFSET CURSOR_POSN],DX
116D 75 05          3033 C      CMP      ACTIVE_PAGE,BH
116F 8B C2          3034 C      JNZ      M17
1171 E8 1175 R      3035 C      MOV      AX,DX
1173                3036 C      CALL     M18
1175                3037 C      RET
1177                3038 C
117A C3            3039 C
117A                3040 C
117A                3041 C
1175                3042 C
1175 E8 1146 R      3043 C      M18 PROC NEAR
1178 8B C8          3044 C      CALL     POSITION
117A 03 0E 044E R   3045 C      ADD      CX,CRT_START
117A                3046 C
117E D1 F9          3047 C      SAR      CX,1
1180 B4 0E          3048 C      MOV      AH,C_CRSR_LOC_HGH
1182 E8 1135 R      3049 C      CALL     M16
1185 C3            3050 C      RET
1186                3051 C      M18 ENDP
1186                3052 C
1186                3053 C
1186                3054 C      READ_CURSOR
1186                3055 C      THIS ROUTINE READS THE CURRENT CURSOR VALUE FROM
1186                3056 C      MEMORY AND SENDS IT BACK TO THE CALLER
1186                3057 C
1186                3058 C      INPUT
1186                3059 C      BH - PAGE OF CURSOR
1186                3060 C
1186                3061 C      OUTPUT
1186                3062 C      DX - ROW, COLUMN OF THE CURRENT CURSOR POSITION
1186                3063 C      CX - CURRENT CURSOR MODE
1186                3064 C
1186 8A DF          3065 C      AH3:
1188 32 FF          3066 C      MOV      BL,BH
118A D1 E3          3067 C      XOR      BH,BH
118C 8B 97 0450 R   3068 C      SAL      BX,1
1190 8B 0E 0460 R   3069 C      MOV      DX,[BX + OFFSET CURSOR_POSN]
1192 5F            3070 C      MOV      CX,CURSOR_MODE
1194 5E            3071 C      POP      DI
1196 5B            3072 C      POP      SI
1198 58            3073 C      POP      BX
119A 57            3074 C      POP      AX
119C 56            3075 C      POP      AX
119E 55            3076 C      POP      DS
11A0 54            3077 C      POP      ES
11A2 53            3078 C      POP      BP
11A4 52            3079 C      IRET
11A6                3080 C
11A6                3081 C      READ LIGHT PEN POSITION
11A6                3082 C
11A6                3083 C      AH4:
11A6                3084 C      MOV      AL,CRT_MODE
11A6                3085 C      CMP      AL,07H
11A6                3086 C      JA      READ_LPEN
11A6                3087 C
11A6                3088 C      TEST     INFO,2
11A6                3089 C      JZ      EGA_IS_COLOR
11A6                3090 C
11A6                3091 C      MONOCHROME HERE (MONOC BIT 1)
11A6                3092 C
11A6                3093 C      CMP      AL,07H
11A6                3094 C      JE      READ_LPEN
11A6                3095 C      JMP      OLD_LP
11A6                3096 C
11A6                3097 C      EGA IS COLOR HERE (MONOC BIT 0)
11A6                3098 C
11A6                3099 C      EGA_IS_COLOR:
11A6                3100 C      CMP      AL,06H
11A6                3101 C      JBE      READ_LPEN
11A6                3102 C
11A6                3103 C      OLD_LP:
11A6                3104 C      INT      42H
11A6                3105 C      POP      DI
11A6                3106 C      POP      SI
11A6                3107 C      ADD      SP,6
11A6                3108 C      POP      DS
11A6                3109 C      POP      ES
11A6                3110 C      POP      BP
11A6                3111 C      IRET
11A6                3112 C
11A6                3113 C      LIGHT PEN
11A6                3114 C      THIS ROUTINE TESTS THE LIGHT PEN SWITCH AND THE LIGHT
11A6                3115 C      PEN TRIGGER. IF BOTH ARE SET, THE LOCATION OF THE LIGHT
11A6                3116 C      PEN IS DETERMINED. OTHERWISE, A RETURN WITH NO
11A6                3117 C      INFORMATION IS MADE.
11A6                3118 C
11A6                3119 C      ON EXIT
11A6                3120 C      (AH) = 0 IF NO LIGHT PEN INFORMATION IS AVAILABLE
11A6                3121 C      BX,CX,DX ARE DESTROYED
11A6                3122 C      (AH) = 1 IF LIGHT PEN IS AVAILABLE
11A6                3123 C      (DH,DL) = ROW,COLUMN OF CURRENT LIGHT PEN
11A6                3124 C      POSITION
11A6                3125 C      (CH) = RASTER POSITION (OLD MODES)
11A6                3126 C      (CX) = RASTER POSITION (NEW MODES)
11A6                3127 C      (BX) = BEST GUESS AT PIXEL HORIZONTAL POSITION
11A6                3128 C
11A6                3129 C      ASSUME CS:CODE,DS:ABS0
11A6                3130 C
11A6                3131 C      SUBTRACT TABLE
11A6                3132 C      V1 LABEL BYTE
11A6                3133 C      DB      006H,006H,007H,007H,005H,005H
11A6                3134 C      DB      004H,005H,000H,000H,000H,000H
11A6                3135 C      DB      000H,005H,006H,004H,004H,004H
11A6                3136 C      DB      004H,006H,006H,004H,007H,004H
11A6                3137 C      DB      007H,004H
11A6                3138 C
11A6                3139 C      READ_LPEN PROC NEAR
11A6                3140 C
11A6                3141 C      WAIT FOR LIGHT PEN TO BE DEPRESSED
11A6                3142 C
11A6                3143 C      MOV      DX,ADDR_6845
11A6                3144 C      ADD      DX,6
11A6                3145 C      IN      AL,DX
11A6                3146 C      TEST     AL,4
11A6                3147 C      MOV      AH,0
11A6                3148 C      JZ      V9
11A6                3149 C      JMP      V6
11A6                3150 C
11A6                3151 C      NOW TEST FOR LIGHT PEN TRIGGER
11A6                3152 C
11A6                3153 C      V9:
11A6                3154 C      TEST     AL,2
11A6                3155 C      ; TEST LIGHT PEN TRIGGER

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11EE 75 03          3151 C      JNZ      V7A          ; RETURN WITHOUT RESETTNG
11F0 E9 129B R     3152 C      JMP      V7          ; TRIGGER
                      3153 C      MOV      V7          ; EXIT LIGHT PEN ROUTINE
                      3154 C
                      3155 C ;----- TRIGGER HAS BEEN SET, READ THE VALUE IN
                      3156 C
11F3 B4 10          3157 C      V7A:    MOV      AH,16          ; LIGHT PEN REGISTERS
11F3 B4 10          3158 C      MOV      AH,16          ; LIGHT PEN REGISTERS
                      3159 C
                      3160 C ;----- INPUT REGS POINTED TO BY AH, AND CONVERT TO ROW COLUMN IN DX
                      3161 C
11F5 8B 16 0463 R   3162 C      MOV      DX,ADDR_6845      ; ADDRESS REGISTER
11F9 8A C4          3163 C      MOV      AL,BH          ; REGISTER TO READ
11FB EE            3164 C      OUT      DX,AL          ; SET IT UP
11FC 42            3165 C      INC      DX          ; DATA REGISTER
11FD 50            3166 C      AX      PUSH      AX          ;
11FE EC            3167 C      IN      AL,DX          ; GET THE VALUE
11FF 8A E8          3168 C      MOV      CH,AL          ; SAVE IN CX
1201 58            3169 C      POP      AX          ;
1202 4A            3170 C      DEC      DX          ; ADDRESS REGISTER
1203 FE C4          3171 C      INC      AH          ;
1205 8A C4          3172 C      MOV      AL,AH          ; SECOND DATA REGISTER
1207 EE            3173 C      OUT      DX,AL          ;
1208 42            3174 C      INC      DX          ; POINT TO DATA REGISTER
1209 EC            3175 C      IN      AL,DX          ; GET THE 2ND DATA VALUE
120A 8A E5          3176 C      MOV      AH,CH          ; AX HAS INPUT VALUE
                      3177 C
                      3178 C ;----- AX HAS THE VALUE READ IN FROM THE 6845
                      3179 C
120C 8A 1E 0449 R   3180 C      MOV      BL,CRT_MODE        ;
1210 2A FF          3181 C      SUB      BH,BH          ; MODE VALUE TO BX
1212 2E: 8A 9F 11C1 R 3182 C      MOV      BL,CS-V1[BX]      ; AMOUNT TO SUBTRACT
1217 2B C3          3183 C      SUB      AX,BX          ; TAKE IT AWAY
1219 8B 1E 044E R   3184 C      MOV      BX,CRT_START      ; SCREEN ADDRESS
121D D1 E8          3185 C      SHR      BX,1          ; DIVIDE BY 2
121F 2B C3          3186 C      SUB      AX,BX          ; ADJUST TO ZERO START
1221 79 02          3187 C      JNS      V2          ; IF POSITIVE, GET MODE
1223 2B C0          3188 C      SUB      AX,AX          ; <0 PLAYS AS 0
                      3189 C
                      3190 C ;----- DETERMINE MODE OF OPERATION
                      3191 C
1225                3192 C      V2:    MOV      CL,3          ; DETERMINE_MODE
1225 B1 03          3193 C      MOV      CL,3          ; SET *8 SHIFT COUNT
1227 80 3E 0449 R 04 3194 C      CMP      CRT_MODE,4      ; GRAPHICS OR ALPHA
122C 72 4D          3195 C      JB      V4          ; ALPHA_PEN
122E 80 3E 0449 R 07 3196 C      CMP      CRT_MODE,7      ;
1233 74 46          3197 C      JE      V4          ; ALPHA_PEN
                      3198 C
1235 80 3E 0449 R 06 3199 C      CMP      CRT_MODE,06H      ;
123A 77 28          3200 C      JA      V8          ;
123C 75 02          3201 C      JNE      V8X          ;
123E D1 E8          3202 C      SHR      AX,1          ;
                      3203 C
                      3204 C ;----- OLD GRAPHICS MODES
                      3205 C
1240                3206 C      V8X:    MOV      DL,40          ; DIVISOR FOR GRAPHICS
1240 B2 28          3207 C      DIV      DL          ; ROW(AL) AND COLUMN(AH)
1242 F6 F2          3208 C      ; AL RANGE 0-99,
                      3209 C      ; AH RANGE 0-39,
                      3210 C
                      3211 C ;----- DETERMINE GRAPHIC ROW POSITION
                      3212 C
1244 8A E8          3213 C      MOV      CH,AL          ; SAVE ROW VALUE IN CH
1246 02 ED          3214 C      ADD      CH,CH          ; *2 FOR EVEN/ODD FIELD
1248 8A DC          3215 C      MOV      BL,AH          ; COLUMN VALUE TO BX
124A 2A FF          3216 C      SUB      BH,BH          ; *8 FOR MEDIUM RES
124C 80 3E 0449 R 06 3217 C      CMP      CRT_MODE,6      ; MEDIUM OR HIGH RES
1251 75 04          3218 C      JNE      V3          ; NOT HIGH RES
1253 B1 04          3219 C      MOV      CL,4          ; SHIFT VALUE FOR HIGH RES
1255 D0 E4          3220 C      SAL      AH,1          ; COLUMN VALUE *2 FOR HIGH RES
1257                3221 C      V3:    SHL      BX,CL          ; NOT HIGH RES
                      3222 C      ; *16 FOR HIGH RES
                      3223 C
                      3224 C ;----- DETERMINE ALPHA CHAR POSITION
                      3225 C
1259 8A D4          3226 C      MOV      DL,AH          ; COLUMN VALUE FOR RETURN
125B 8A F0          3227 C      MOV      DH,AL          ; ROW VALUE
125D D0 EE          3228 C      SHR      DH,1          ; DIVIDE BY 4
125F D0 EE          3229 C      SHR      DH,1          ; FOR VALUE IN 0-2H RANGE
1261 EB 2C 90        3230 C      JMP      V5          ; LIGHT_PEN_RETURN_SET
1264                3231 C
                      3232 C ;----- NEW GRAPHICS MODES
                      3233 C
1264 99            3234 C      CWD          ; PREPARE TO DIVIDE
1265 F7 36 044A R   3235 C      DIV      CRT_COLS        ; AX = ROW, DX = COLUMN
1269 8B DA          3237 C      MOV      BX,DX          ; SAVE REMAINDER
126B D3 E3          3238 C      SAL      BX,CL          ;
126D 8B C8          3239 C      MOV      CX,AX          ; PEL COLUMN
126F 52            3240 C      PUSH      DX          ;
1270 99            3241 C      CWD          ; SAVE FROM DIVIDE
1271 F7 36 0485 R   3242 C      DIV      POINTS        ; PREPARE TO DIVIDE
1275 5A            3243 C      POP      DX          ; DIVIDE BY BYTES/CHAR
1276 8A F0          3244 C      MOV      DH,AL          ; RECOVER
1278 EB 15 90        3245 C      JMP      V5          ; CHARACTER ROW
                      3246 C
                      3247 C ;----- ALPHA MODE ON LIGHT PEN
                      3248 C
127B                3249 C      V4:    MOV      DH,AL          ; ALPHA_PEN
127B F6 36 044A R   3250 C      DIV      BYTE PTR CRT_COLS ; ROW,COLUMN VALUE
127F 8A F0          3251 C      MOV      DH,AL          ; ROWS TO DH
1281 8A D4          3252 C      MOV      DL,AH          ; COLS TO DL
1283 8A DC          3253 C      MOV      BL,AH          ; COLUMN VALUE
1285 32 FF          3254 C      XOR      BH,BH          ;
1287 D3 E3          3255 C      SAL      BX,CL          ; TO BX
1289 F6 26 0485 R   3256 C      MUL      BYTE PTR POINTS ;
128D 8B C8          3257 C      MOV      CX,AX          ;
128F                3258 C
128F B4 01          3259 C      V5:    MOV      AH,1          ; LIGHT_PEN_RETURN_SET
1291 52            3260 C      V6:    PUSH      DX          ; INDICATE EVERYTHING SET
1291 52            3261 C      ; LIGHT_PEN_RETURN
1291 52            3262 C      ; SAVE RETURN VALUE
1292 8B 16 0463 R   3263 C      MOV      DX,ADDR_6845      ; (IN CASE)
1296 83 C2 07        3264 C      ADD      DX,7          ; GET BASE ADDRESS
1299 EE            3265 C      OUT      DX,AL          ; POINT TO RESET PARM
1299 EE            3266 C      ; ADDRESS, NOT DATA,
129A 5A            3267 C      POP      DX          ; IS IMPORTANT
129B                3268 C      V7:    POP      D1          ; RETURN_NO_RESET
129B 5F            3269 C      POP      SI          ;
129C 5E            3270 C      POP      DI          ;
129D 83 C4 06        3271 C      ADD      SP,6          ; DISCARD SAVED BX,CX,DX
12A0 1F            3272 C      POP      DS          ;
12A1 07            3273 C      POP      ES          ;
12A2 5D            3274 C      POP      BP          ;
12A3 CF            3275 C      RET          ;
12A4                3276 C      READ_LPEN    ENDP

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3277 C
3278 C
3279 C ACT_DISP_PAGE SELECT ACTIVE DISPLAY PAGE
3280 C THIS ROUTINE SETS THE ACTIVE DISPLAY PAGE, ALLOWING
3281 C FOR MULTIPLE PAGES OF DISPLAYED VIDEO.
3282 C
3283 C INPUT AL HAS THE NEW ACTIVE DISPLAY PAGE
3284 C
3285 C OUTPUT THE CRTC IS RESET TO DISPLAY THAT PAGE
3286 C
3287 C
3288 C AH5:
3289 C MOV ACTIVE_PAGE,AL ; SAVE ACTIVE PAGE VALUE
3290 C MOV CX,CRT_LEN ; GET SAVED LENGTH OF
3291 C ; REGEN BUFFER
3292 C CBN ; CONVERT AL TO WORD
3293 C PUSH AX ; SAVE PAGE VALUE
3294 C MUL CX ; DISPLAY PAGE TIMES
3295 C ; REGEN LENGTH
3296 C MOV CRT_START,AX ; SAVE START ADDRESS FOR
3297 C ; LATER REQUIREMENTS
3298 C MOV CX,AX ; START ADDRESS TO CX
3299 C MOV BL,CRT_MODE ;
3300 C CMP BL,7 ; DO NOT DIVIDE BY TWO
3301 C JAE ADP_1
3302 C ADP_2: SAR CX,1 ; / 2 FOR CRTC HANDLING
3303 C
3304 C ADP_1: MOV AH,C_STRT_HGH ; REG FOR START ADDRESS
3305 C CALL M16 ;
3306 C POP BX ; RECOVER PAGE VALUE
3307 C SAL BX,1 ; *2 FOR WORD OFFSET
3308 C MOV AX,[BX + OFFSET CURSOR_POSN] ; GET CURSOR FOR THIS PAGE
3309 C M16 ; SET THE CURSOR POSITION
3310 C JMP V_RET
3311 C
3312 C SUBTTL
3313 C
3314 C INCLUDE VSCROLL.INC
3315 C SUBTTL VSCROLL.INC
3316 C PAGE
3317 C
3318 C FLTA PROC NEAR ; CHECK FOR SCROLL COUNT
3319 C PUSH AX
3320 C MOV AH,DH ; LOWER ROW
3321 C SUB AH,CH ; UPPER ROW
3322 C INC AH ; NUMBER TO SCROLL
3323 C CMP AH,AL ; SAME AS REQUESTED
3324 C JAE LTA
3325 C JNE LTA
3326 C SUB AL,AL ; YES, SET TO 0 FOR BLANK
3327 C
3328 C LTA: RET
3329 C FLTA ENDP
3330 C
3331 C CRANK PROC NEAR ; MOVE ROWS OF PELS UP
3332 C PUSH BX
3333 C ASSUME DS:ABSO
3334 C DS DS
3335 C CALL DDS ; SAVE DATA SEGMENT
3336 C MOV BX,CRT_COLS ; SET DATA SEGMENT
3337 C POP DS
3338 C CRANK_A:
3339 C PUSH CX ; SAVE MOVE COUNT
3340 C MOV CL,DL ; COLUMN COUNT
3341 C SUB CH,CH ; CLEAR HIGH BYTE
3342 C PUSH SI ; SAVE POINTERS
3343 C PUSH DI
3344 C REP MOVSB ; MOVE THAT ROW
3345 C POP DI ; RECOVER POINTERS
3346 C POP SI
3347 C ADD SI,BX ; NEXT ROW
3348 C ADD DI,BX ; NEXT ROW
3349 C POP CX ; RECOVER ROW COUNT
3350 C LOOP CRANK_A ; DO MORE
3351 C POP BX
3352 C RET ; RETURN TO CALLER
3353 C CRANK ENDP
3354 C
3355 C CRANK_4 PROC NEAR ; MOVE ROWS OF PELS DOWN
3356 C PUSH BX
3357 C ASSUME DS:ABSO
3358 C DS DS
3359 C CALL DDS ; SAVE DATA SEGMENT
3360 C MOV BX,CRT_COLS ; SET DATA SEGMENT
3361 C POP DS
3362 C CRANK_B:
3363 C PUSH CX ; SAVE MOVE COUNT
3364 C MOV CL,DL ; COLUMN COUNT
3365 C SUB CH,CH ; CLEAR HIGH BYTE
3366 C PUSH SI ; SAVE POINTERS
3367 C PUSH DI
3368 C REP MOVSB ; MOVE THAT ROW
3369 C POP DI ; RECOVER POINTERS
3370 C POP SI
3371 C SUB SI,BX ; NEXT ROW
3372 C SUB DI,BX ; NEXT ROW
3373 C POP CX ; RECOVER ROW COUNT
3374 C LOOP CRANK_B ; DO MORE
3375 C POP BX
3376 C RET ; RETURN TO CALLER
3377 C CRANK_4 ENDP
3378 C
3379 C PART_1 PROC NEAR ; FILL ROW AFTER SCROLL
3380 C PUSH DH
3381 C MOV DH,3
3382 C MOV DL,SEQ_ADDR
3383 C MOV AX,020FH ; SEQUENCER
3384 C CALL OUT_DX ; MAP MASK
3385 C DX ; ALL MAPS ON
3386 C SUB AX,AX ; ZERO
3387 C MOV CL,DL ; COLUMN COUNT
3388 C SUB CH,CH
3389 C PUSH DI ; SAVE POINTER
3390 C REP STOSB ; CLEAR ONE ROW OF PELS
3391 C POP DI ; RECOVER POINTER
3392 C MOV AL,DH ; GET COLOR VALUE
3393 C PUSH DX
3394 C MOV DH,3
3395 C MOV DL,SEQ_ADDR ; SEQUENCER
3396 C MOV AH,02H ; MAP MASK
3397 C CALL OUT_DX ; SET THE COLOR
3398 C POP DX
3399 C MOV AL,OFFH ; ALL BITS ON
3400 C MOV CL,DL ; COLUMN COUNT
3401 C PUSH DI ; SAVE POINTER
3402 C REP STOSB ; TURN ON THOSE BITS IN

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1346 5F          3403 C      POP      DI          ; ENABLED PLANES
1347 C3          3404 C      RET      RET          ; RECOVER POINTER
1348             3405 C      PART_1 ENDP ; RETURN TO CALLER
1348             3406 C
1348             3407 C
1348             3408 C
1348 B6 03        3409 C      PART_2 PROC   NEAR
1348 B2 C4        3410 C      MOV      DH,3
1348 B8 020F      3411 C      MOV      DL,SEQ_ADDR ; SEQUENCER
1348 E8 0015 R    3412 C      CALL     OUT_DX      ; MAP MASK, ALL MAPS
1352 C3          3413 C      RET      RET          ; ENABLE THE MAPS
1353             3414 C      PART_2 ENDP ; RETURN TO CALLER
1353             3415 C
1353             3416 C
1353             3417 C
1353 1E           3418 C      BLNK_3 PROC   NEAR ; BLANK FOR SCROLL UP
1353             3419 C      PUSH     DS          ; SAVE DATA SEGMENT
1353             3420 C      ASSUME   DS:ABSO
1353             3421 C      CALL     DDS          ; GET LOW MEMORY SEGMENT
1353             3422 C      MOV      DH,BH ; ATTRIBUTE FOR BLANK LINE
1353             3423 C      SUB      BH,BH ; CLEAR HIGH BYTE
1353             3424 C      PUSH     AX          ; SAVE
1353             3425 C      PUSH     DX          ; SAVE BECAUSE OF MULTIPLY
1353             3426 C      MOV      AX,BX ; ROW COUNT
1353             3427 C      MUL      POINTS ; CHARACTER HEIGHT
1353             3428 C      MOV      BX,AX ; NET VALUE TO BX
1353             3429 C      POP      DX          ; RECOVER
1353             3430 C      POP      AX
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13EE      8A DE      3529 C N7:                                ; BLANK FIELD
13F0      EB DC      3530 C      MOV     BL,DH                                ; GET ROW COUNT
13F2      F6 06 0487 R 04 3531 C      JMP     N3                                ; GO CLEAR THAT AREA
13F7      74 12      3532 C      SCROLL_UP      ENDP
13F9      52      3533 C      ;----- HANDLE COMMON SCROLL SET UP HERE
13FA      86 03      3534 C
13FB      B2 DA      3535 C
13FE      50      3536 C
13FF      EC      3537 C
1400      A8 08      3538 C SCROLL_POSITION PROC NEAR
1402      74 FB      3539 C      TEST    INFO,4
1404      80 25      3540 C      JZ       N9
1406      D8 08      3541 C      ;----- 80X25 COLOR CARD SCROLL
1408      EE      3542 C
1409      58      3543 C      PUSH    DX
140A      5A      3544 C      MOV     DH,3
140B      1408      3545 C      MOV     DL,0DAH
140C      E8 1146 R      3546 C      PUSH    AX
140D      03 06 044E R      3547 C N8:                                ; COLOR CARD HERE
140E      8B FB      3548 C      IN       AL,DX
140F      80 25      3549 C      TEST    AL,8
1410      88 F0      3550 C      JZ       N8
1411      2B 01      3551 C      MOV     DL,25H
1412      8B FB      3552 C      MOV     DL,00BH
1413      80 25      3553 C      OUT     DX,AL
1414      FE C6      3554 C      POP     AX
1415      FE C2      3555 C      POP     DX
1416      2B 01      3556 C      N9:                                ; CONVERT TO REGEN POINTER
1417      8B FB      3557 C      CALL    POSITION
1418      80 25      3558 C      ADD     AX,CRT_START
1419      88 F0      3559 C      MOV     DI,AX
141A      FE C6      3560 C      MOV     SI,AX
141B      FE C2      3561 C      SUB     DX,CX
141C      32 ED      3562 C      INC     DH
141D      8B FB      3563 C      INC     DL
141E      80 2E 044A R      3564 C      XOR     CH,CH
141F      03 ED      3565 C      MOV     BP,CRT_COLS
1420      06      3566 C      ADD     BP,BP
1421      1F      3567 C      MOV     AL,BL
1422      8A C3      3568 C      MUL     BYTE PTR CRT_COLS
1423      F6 26 044A R      3569 C      ADD     AX,AX
1424      03 C0      3570 C      PUSH    ES
1425      06      3571 C      POP     DS
1426      80 FB 00      3572 C      CMP     BL,0
1427      C3      3573 C      RET
1428      80 FB 00      3574 C      SCROLL_POSITION ENDP
1429      C3      3575 C      ;----- MOVE_ROW
1430      C3      3576 C
1431      C3      3577 C      N10 PROC NEAR
1432      8A CA      3578 C      MOV     CL,DL
1433      56      3579 C      SI      PUSH
1434      57      3580 C      PUSH    DI
1435      F3 A5      3581 C      REP     MOVSW
1436      5F      3582 C      POP     DI
1437      5E      3583 C      POP     SI
1438      C3      3584 C      RET
1439      C3      3585 C      N10 ENDP
1440      C3      3586 C      ;----- CLEAR_ROW
1441      C3      3587 C
1442      C3      3588 C      N11 PROC NEAR
1443      8A CA      3589 C      MOV     CL,DL
1444      56      3590 C      SI      PUSH
1445      F3 AB      3591 C      PUSH    DI
1446      5F      3592 C      REP     STOSW
1447      C3      3593 C      POP     DI
1448      C3      3594 C      POP     SI
1449      C3      3595 C      N11 ENDP
1450      C3      3596 C
1451      C3      3597 C      ;----- SCROLL_DOWN
1452      FD      3598 C      ; THIS ROUTINE MOVES THE CHARACTERS WITHIN A
1453      8A D8      3599 C      ; DEFINED BLOCK DOWN ON THE SCREEN, FILLING THE
1454      E8 16EB R      3600 C      ; TOP LINES WITH A DEFINED CHARACTER
1455      53      3601 C      INPUT
1456      8B C2      3602 C      (AH) = CURRENT CRT MODE
1457      E8 13F2 R      3603 C      (AL) = NUMBER OF LINES TO SCROLL
1458      74 20      3604 C      (CX) = UPPER LEFT CORNER OF REGION
1459      2B F0      3605 C      (DX) = LOWER RIGHT CORNER OF REGION
1460      8A E6      3606 C      (BH) = FILL CHARACTER
1461      2A E3      3607 C      (DS) = DATA SEGMENT
1462      58      3608 C      (ES) = REGEN SEGMENT
1463      58      3609 C      OUTPUT
1464      80 20      3610 C      NONE -- SCREEN IS SCROLLED
1465      58      3611 C      ;----- SCROLL_DOWN PROC NEAR
1466      8A D8      3612 C      ; SCROLL_DOWN
1467      E8 143B R      3613 C      ; LINE COUNT TO BL
1468      53      3614 C      MOV     BL,AL
1469      8B C2      3615 C      CALL    MK_ES
1470      75 F5      3616 C      PUSH    BX
1471      58      3617 C      MOV     AX,DX
1472      E8 143B R      3618 C      CALL    SCROLL_POSITION
1473      58      3619 C      JZ       N16
1474      8A E6      3620 C      SUB     SI,AX
1475      2A E3      3621 C      MOV     AH,DH
1476      58      3622 C      SUB     AH,BL
1477      E8 1432 R      3623 C      N13:
1478      58      3624 C      CALL    N10
1479      2B F5      3625 C      SUB     SI,BP
1480      75 F5      3626 C      SUB     DI,BP
1481      58      3627 C      DEC     AH
1482      75 F5      3628 C      JNZ     N13
1483      58      3629 C      POP     AX
1484      75 F5      3630 C      MOV     AL,
1485      58      3631 C      ; RECOVER ATTRIBUTE IN AH
1486      80 20      3632 C      N15:
1487      58      3633 C      CALL    N11
1488      75 F5      3634 C      SUB     DI,BP
1489      58      3635 C      DEC     BL
1490      75 F5      3636 C      JNZ     N15
1491      58      3637 C      ; SCROLL_END
1492      75 F5      3638 C      JMP     N5
1493      58      3639 C      N16:
1494      8A DE      3640 C      MOV     BL,DH
1495      EB ED      3641 C      JMP     N14
1496      74 12      3642 C      SCROLL_DOWN ENDP
1497      74 12      3643 C
1498      74 12      3644 C      ;----- SCROLL_UP
1499      74 12      3645 C      ; THIS ROUTINE SCROLLS UP THE INFORMATION ON THE CRT
1500      74 12      3646 C      ENTRY
1501      74 12      3647 C      CH,CL = UPPER LEFT CORNER OF REGION TO SCROLL
1502      74 12      3648 C      DH,DL = LOWER RIGHT CORNER OF REGION TO SCROLL
1503      74 12      3649 C      BOTH OF THE ABOVE ARE IN CHARACTER POSITIONS
1504      74 12      3650 C      BH = FILL VALUE FOR BLANKED LINES
1505      74 12      3651 C      AL = # LINES TO SCROLL (AL=0 MEANS BLANK THE ENTIRE
1506      74 12      3652 C      ; FIELD)
1507      74 12      3653 C      DS = DATA SEGMENT
1508      74 12      3654 C

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3655 C ; ES = REGEN SEGMENT
3656 C ; EXIT
3657 C ; NOTHING, THE SCREEN IS SCROLLED
3658 C ;-----
3659 C GRAPHICS_UP PROC NEAR
3660 C MOV BL,AL ; SAVE LINE COUNT IN BL
3661 C MOV AX,CX ; GET UPPER LEFT POSITION
3662 C ; INTO AX REG
3663 C
3664 C ;----- USE CHARACTER SUBROUTINE FOR POSITIONING
3665 C ;----- ADDRESS RETURNED IS MULTIPLIED BY 2 FROM CORRECT VALUE
3666 C
3667 C CALL GRAPH_POSN
3668 C MOV DI,AX ; SAVE RESULT AS
3669 C ; DESTINATION ADDRESS
3670 C
3671 C ;----- DETERMINE SIZE OF WINDOW
3672 C
3673 C SUB DX,CX
3674 C ADD DX,101H ; ADJUST VALUES
3675 C SAL DH,1 ; 0 TO HIGH OF COUNT REG
3676 C ; SINCE 8 VERT DOTS/CHAR
3677 C SAL DH,1 ; AND EVEN/ODD ROWS
3678 C
3679 C ;----- DETERMINE CRT MODE
3680 C
3681 C CMP CRT_MODE,6 ; TEST FOR MEDIUM RES
3682 C JNC R7 ; FIND_SOURCE
3683 C
3684 C ;----- MEDIUM RES UP
3685 C
3686 C SAL DL,1 ; * 2,
3687 C SAL DI,1 ; SINCE 2 BYTES/CHAR
3688 C
3689 C ;----- DETERMINE THE SOURCE ADDRESS IN THE BUFFER
3690 C
3691 C R7:
3692 C PUSH ES ; FIND_SOURCE
3693 C POP DS ; GET SEGMENTS BOTH
3694 C SUB CH,CH ; POINTING TO REGEN
3695 C SAL BL,1 ; 0 TO HIGH OF COUNT REG
3696 C SAL BL,1 ; NUMBER OF LINES *4
3697 C JZ R11 ; IF 0, BLANK ENTIRE FIELD
3698 C MOV AL,BL ; NUMBER OF LINES IN AL
3699 C MOV AH,80 ; 80 BYTES/ROW
3700 C MUL AH ; OFFSET TO SOURCE
3701 C MOV SI,DI ; SET UP SOURCE
3702 C ADD SI,AX ; ADD IN OFFSET TO IT
3703 C MOV AH,DH ; NUMBER OF ROWS IN FIELD
3704 C SUB AH,BL ; DETERMINE NUMBER TO MOVE
3705 C
3706 C ;----- LOOP THROUGH, MOVING ONE ROW AT A TIME, BOTH EVEN AND ODD FIELDS
3707 C
3708 C R8:
3709 C CALL R17 ; ROW_LOOP
3710 C SUB SI,2000H-80 ; MOVE ONE ROW
3711 C SUB DI,2000H-80 ; MOVE TO NEXT ROW
3712 C DEC AH ; NUMBER OF ROWS TO MOVE
3713 C JNZ R8 ; CONTINUE TILL ALL MOVED
3714 C
3715 C ;----- FILL IN THE VACATED LINE(S)
3716 C
3717 C R9:
3718 C MOV AL,BH ; CLEAR_ENTRY
3719 C ; ATTRIBUTE TO FILL WITH
3720 C
3721 C R10:
3722 C CALL R18 ; CLEAR THAT ROW
3723 C SUB DI,2000H-80 ; POINT TO NEXT LINE
3724 C DEC BL ; NUMBER OF LINES TO FILL
3725 C JNZ R10 ; CLEAR_LOOP
3726 C
3727 C R11:
3728 C MOV BL,DH ; BLANK_FIELD
3729 C ; SET BLANK COUNT TO
3730 C ; EVERYTHING IN FIELD
3731 C ; CLEAR THE FIELD
3732 C
3733 C GRAPHICS_UP ENDP
3734 C
3735 C ;----- ROUTINE TO MOVE ONE ROW OF INFORMATION
3736 C
3737 C R17 PROC NEAR
3738 C MOV CL,DL ; NUM OF BYTES IN THE ROW
3739 C PUSH SI ; SAVE POINTERS
3740 C REP MOVSB ; MOVE THE EVEN FIELD
3741 C POP DI
3742 C ADD SI,2000H
3743 C ADD DI,2000H ; POINT TO THE ODD FIELD
3744 C PUSH SI
3745 C PUSH DI
3746 C MOV CL,DL ; SAVE THE POINTERS
3747 C REP MOVSB ; COUNT BACK
3748 C POP DI ; MOVE THE ODD FIELD
3749 C POP SI ; POINTERS BACK
3750 C ; RETURN TO CALLER
3751 C
3752 C R17 ENDP
3753 C
3754 C ;----- CLEAR A SINGLE ROW
3755 C
3756 C R18 PROC NEAR
3757 C MOV CL,DL ; NUMBER OF BYTES IN FIELD
3758 C PUSH DI ; SAVE POINTER
3759 C REP STOSB ; STORE THE NEW VALUE
3760 C POP DI ; POINTER BACK
3761 C ADD DI,2000H ; POINT TO ODD FIELD
3762 C PUSH DI
3763 C MOV CL,DL ; FILL THE ODD FIELD
3764 C REP STOSB
3765 C POP DI ; RETURN TO CALLER
3766 C
3767 C R18 ENDP
3768 C
3769 C MEM_DET PROC NEAR
3770 C ASSUME DS:ABS0
3771 C PUSH AX
3772 C PUSH DS
3773 C CALL DDS
3774 C MOV AH,INFO
3775 C AND AH,060H
3776 C DS
3777 C POP AX
3778 C JZ MIN
3779 C STC
3780 C RET
3781 C
3782 C MIN:
3783 C CLC
3784 C RET

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150B      3781 C MEM_DET ENDP
          3782 C
          3783 C ;----- SCROLL ACTIVE PAGE UP
          3784 C
150B      3785 C SC_2:
150B E9 13A3 R      3786 C JMP SCROLL_UP
          3787 C
150E      3788 C AH6:
          3789 C
          3790 C CALL FLTA
          3791 C MOV AH,CRT_MODE ; GET CURRENT MODE
          3792 C CMP AH,07H
          3793 C JBE SC_2 ; ANY OF THE OLD MODES
          3794 C CMP AH,ODH
          3795 C JAE GRAPHICS_UP_2 ; NEW GRAPHICS MODES
          3796 C JMP V_RET ; NOT A RECOGNIZED MODE
          3797 C
          3798 C GR_ST_1 PROC NEAR
          3799 C MOV DX,0A000H ; REGEN BUFFER
          3800 C MOV BP,0511H ; GRAPHICS WRITE MODE
          3801 C CMP AH,0FH
          3802 C JBE VV1
          3803 C CALL MEM_DET
          3804 C JNC VV1
          3805 C MOV BP,0501H ; GRAPHICS WRITE MODE
          3806 C
          3807 C RET
          3808 C
          3809 C GR_ST_1 ENDP
          3810 C
          3811 C GRAPHICS_UP_2 PROC NEAR
          3812 C ASSUME DS:ABS0
          3813 C PUSH DX
          3814 C CALL GR_ST_1 ; SET SEGMENT, WRITE MODE
          3815 C SRLoad ES ; SET REGEN
          3816 C MOV ES,DX
          3817 C POP DX
          3818 C MOV BL,AL ; NUMBER OF LINES
          3819 C MOV AX,CX ; UPPER LEFT CORNER
          3820 C PUSH BX
          3821 C MOV BH,ACTIVE_PAGE ; ACTIVE PAGE FOR SCROLL
          3822 C CALL GR_PSN ; ADDRESS IN REGEN
          3823 C POP BX
          3824 C MOV DI,AX ; SET POINTER
          3825 C DX,CX ; DETERMINE WINDOW
          3826 C ADD DX,0101H ; ADJUST
          3827 C SUB AH,AH ; ZERO HIGH BYTE
          3828 C AL,BL ; LINE COUNT
          3829 C PUSH DX
          3830 C MUL POINTS ; BYTES PER CHARACTER
          3831 C MUL CRT_COLS ; COLUMNS
          3832 C MOV SI,DI ; SET UP SOURCE INDEX
          3833 C ADD SI,AX ; ADJUST
          3834 C ASSUME DS:NOTHING
          3835 C PUSH ES
          3836 C POP DS
          3837 C POP DX
          3838 C OR BL,BL ; LINE COUNT
          3839 C JZ AR9
          3840 C MOV CL,DH
          3841 C SUB CL,BL
          3842 C SUB CH,CH
          3843 C
          3844 C ASSUME DS:ABS0
          3845 C PUSH DS
          3846 C CALL DDS ; LOW MEMORY SEGMENT
          3847 C PUSH AX
          3848 C PUSH DX
          3849 C MOV AX,CX
          3850 C MUL POINTS ; BYTES PER CHAR
          3851 C MOV CX,AX ; SET THE COUNT
          3852 C POP DX
          3853 C POP AX
          3854 C ASSUME DS:NOTHING
          3855 C POP DS
          3856 C
          3857 C PUSH DX
          3858 C MOV AX,BP
          3859 C MOV DH,3
          3860 C MOV DL,GRAPH_ADDR ; GRAPHICS
          3861 C CALL OUT_DX ; SEQUENCER
          3862 C MOV AX,020FH ; ENABLE ALL MAPS
          3863 C CALL OUT_DX
          3864 C POP DX
          3865 C CALL CRANK ; SCROLL THE SCREEN
          3866 C
          3867 C PUSH DX
          3868 C DEC BP
          3869 C MOV AX,BP
          3870 C MOV DH,3
          3871 C MOV DL,GRAPH_ADDR
          3872 C CALL OUT_DX
          3873 C POP DX
          3874 C
          3875 C AR10: CALL BLNK_3
          3876 C JMP V_RET
          3877 C
          3878 C AR9: MOV BL,DH ; BLANK ENTIRE WINDOW
          3879 C JMP AR10
          3880 C GRAPHICS_UP_2 ENDP
          3881 C
          3882 C ;----- SCROLL ACTIVE DISPLAY PAGE DOWN
          3883 C
          3884 C SC_3:
          3885 C
          3886 C
          3887 C AH7:
          3888 C
          3889 C ASSUME DS:ABS0
          3890 C CALL FLTA
          3891 C MOV AH,CRT_MODE ; OLD COLOR ALPHA
          3892 C CMP AH,03H
          3893 C JBE SC_3 ; MONOCHROME ALPHA
          3894 C CMP AH,07H
          3895 C JAE SC_3
          3896 C
          3897 C CMP AH,ODH ; NEW GRAPHICS MODES
          3898 C JAE GRAPHICS_DN_2 ; OLD GRAPHICS MODES
          3899 C CMP AH,06H
          3900 C JA M_0
          3901 C MOV AH,07H
          3902 C INT 42H
          3903 C M_0: JMP V_RET
          3904 C
          3905 C GRAPHICS_DN_2 PROC NEAR
          3906 C

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1503 8A D8      3907 C      MOV BL,AL      ; LINE COUNT
1505 52         3908 C      PUSH DX        ; SAVE LOWER RIGHT
1506 E8 1522 R  3909 C      CALL GR_ST_1
1509 8E C2      3910 C      SRLD ES,DX    ; SET REGEN SEGMENT
150B 5A         3911 C+     MOV DX        ;
150C 8B C2      3912 C      POP DX        ;
150E FE C4      3913 C      MOV AX,DX    ;
150F 53         3914 C      INC BX        ; MOV CHAR ROW UP BY ONE
1511 8A 3E 0462 R 3915 C      PUSH BX        ;
1515 E8 16C6 R  3916 C      MOV BH,ACTIVE_PAGE ; ADDRESS IN REGEN
1518 5B         3917 C      CALL GRK_PSN    ;
1519 2B 06 044A R 3918 C      POP BX        ; ONE SCAN OVERSHOOT
151D 8B F8      3919 C      SUB D1,AX      ;
151F 2B D1      3920 C      SUB DX,CX      ; CALCULATE WINDOW
1521 81 C2 0101 3921 C      ADD DX,0101H  ; ADJUST COUNT
1523 2A E4      3922 C      AH,AX        ;
1525 8A C3      3923 C      MOV AL,BL     ;
1527 52         3924 C      PUSH DX        ;
1529 F7 26 0485 R 3925 C      POINTS     ; BYTES PER CHAR
152B 8B F8      3926 C      MUL CRT_COLS  ; BYTES PER ROW
152D 2B D1      3927 C      SUB DX,CX      ;
152F 81 C2 0101 3928 C      ADD DX,0101H  ;
1531 2A E4      3929 C      AH,AX        ;
1533 8A C3      3930 C      MOV AL,BL     ;
1535 52         3931 C      PUSH DX        ;
1537 F7 26 044A R 3932 C      POINTS     ;
1539 8B F8      3933 C      MUL CRT_COLS  ;
153B 2B D1      3934 C      SUB DX,CX      ;
153D 81 C2 0101 3935 C      ADD DX,0101H  ;
153F 2A E4      3936 C      AH,AX        ;
1541 8A C3      3937 C      MOV AL,BL     ;
1543 52         3938 C      PUSH DX        ;
1545 F7 26 044A R 3939 C      POINTS     ;
1547 8B F8      3940 C      MUL CRT_COLS  ;
1549 2B D1      3941 C      SUB DX,CX      ;
154B 81 C2 0101 3942 C      ADD DX,0101H  ;
154D 2A E4      3943 C      AH,AX        ;
154F 8A C3      3944 C      MOV AL,BL     ;
1551 52         3945 C      PUSH DX        ;
1553 F7 26 0485 R 3946 C      POINTS     ; BYTES PER CHAR
1555 8B F8      3947 C      MUL CRT_COLS  ;
1557 2B D1      3948 C      SUB DX,CX      ;
1559 81 C2 0101 3949 C      ADD DX,0101H  ;
155B 2A E4      3950 C      AH,AX        ;
155D 8A C3      3951 C      MOV AL,BL     ;
155F 52         3952 C      PUSH DX        ;
1561 F7 26 044A R 3953 C      POINTS     ;
1563 8B F8      3954 C      MUL CRT_COLS  ;
1565 2B D1      3955 C      SUB DX,CX      ;
1567 81 C2 0101 3956 C      ADD DX,0101H  ;
1569 2A E4      3957 C      AH,AX        ;
156B 8A C3      3958 C      MOV AL,BL     ;
156D 52         3959 C      PUSH DX        ;
156F F7 26 0485 R 3960 C      POINTS     ;
1571 8B F8      3961 C      MUL CRT_COLS  ;
1573 2B D1      3962 C      SUB DX,CX      ;
1575 81 C2 0101 3963 C      ADD DX,0101H  ;
1577 2A E4      3964 C      AH,AX        ;
1579 8A C3      3965 C      MOV AL,BL     ;
157B 52         3966 C      PUSH DX        ;
157D F7 26 044A R 3967 C      POINTS     ;
157F 8B F8      3968 C      MUL CRT_COLS  ;
1581 2B D1      3969 C      SUB DX,CX      ;
1583 81 C2 0101 3970 C      ADD DX,0101H  ;
1585 2A E4      3971 C      AH,AX        ;
1587 8A C3      3972 C      MOV AL,BL     ;
1589 52         3973 C      PUSH DX        ;
158B F7 26 0485 R 3974 C      POINTS     ;
158D 8B F8      3975 C      MUL CRT_COLS  ;
158F 2B D1      3976 C      SUB DX,CX      ;
1591 81 C2 0101 3977 C      ADD DX,0101H  ;
1593 2A E4      3978 C      AH,AX        ;
1595 8A C3      3979 C      MOV AL,BL     ;
1597 52         3980 C      PUSH DX        ;
1599 F7 26 044A R 3981 C      POINTS     ;
159B 8B F8      3982 C      MUL CRT_COLS  ;
159D 2B D1      3983 C      SUB DX,CX      ;
159F 81 C2 0101 3984 C      ADD DX,0101H  ;
1601 2A E4      3985 C      AH,AX        ;
1603 8A C3      3986 C      MOV AL,BL     ;
1605 52         3987 C      PUSH DX        ;
1607 F7 26 0485 R 3988 C      POINTS     ;
1609 8B F8      3989 C      MUL CRT_COLS  ;
160B 2B D1      3990 C      SUB DX,CX      ;
160D 81 C2 0101 3991 C      ADD DX,0101H  ;
160F 2A E4      3992 C      AH,AX        ;
1611 8A C3      3993 C      MOV AL,BL     ;
1613 52         3994 C      PUSH DX        ;
1615 F7 26 044A R 3995 C      POINTS     ;
1617 8B F8      3996 C      MUL CRT_COLS  ;
1619 2B D1      3997 C      SUB DX,CX      ;
161B 81 C2 0101 3998 C      ADD DX,0101H  ;
161D 2A E4      3999 C      AH,AX        ;
161F 8A C3      4000 C      MOV AL,BL     ;
1621 52         4001 C      PUSH DX        ;
1623 F7 26 0485 R 4002 C      POINTS     ;
1625 8B F8      4003 C      MUL CRT_COLS  ;
1627 2B D1      4004 C      SUB DX,CX      ;
1629 81 C2 0101 4005 C      ADD DX,0101H  ;
162B 2A E4      4006 C      AH,AX        ;
162D 8A C3      4007 C      MOV AL,BL     ;
162F 52         4008 C      PUSH DX        ;
1631 F7 26 044A R 4009 C      POINTS     ;
1633 8B F8      4010 C      MUL CRT_COLS  ;
1635 2B D1      4011 C      SUB DX,CX      ;
1637 81 C2 0101 4012 C      ADD DX,0101H  ;
1639 2A E4      4013 C      AH,AX        ;
163B 8A C3      4014 C      MOV AL,BL     ;
163D 52         4015 C      PUSH DX        ;
163F F7 26 0485 R 4016 C      POINTS     ;
1641 8B F8      4017 C      MUL CRT_COLS  ;
1643 2B D1      4018 C      SUB DX,CX      ;
1645 81 C2 0101 4019 C      ADD DX,0101H  ;
1647 2A E4      4020 C      AH,AX        ;
1649 8A C3      4021 C      MOV AL,BL     ;
164B 52         4022 C      PUSH DX        ;
164D F7 26 044A R 4023 C      POINTS     ;
164F 8B F8      4024 C      MUL CRT_COLS  ;
1651 2B D1      4025 C      SUB DX,CX      ;
1653 81 C2 0101 4026 C      ADD DX,0101H  ;
1655 2A E4      4027 C      AH,AX        ;
1657 8A C3      4028 C      MOV AL,BL     ;
1659 52         4029 C      PUSH DX        ;
165B F7 26 0485 R 4030 C      POINTS     ;
165D 8B F8      4031 C      MUL CRT_COLS  ;
165F 2B D1      4032 C      SUB DX,CX      ;
1661 81 C2 0101 4033 C      ADD DX,0101H  ;
1663 2A E4      4034 C      AH,AX        ;
1665 8A C3      4035 C      MOV AL,BL     ;
1667 52         4036 C      PUSH DX        ;
1669 F7 26 044A R 4037 C      POINTS     ;
166B 8B F8      4038 C      MUL CRT_COLS  ;
166D 2B D1      4039 C      SUB DX,CX      ;
166F 81 C2 0101 4040 C      ADD DX,0101H  ;
1671 2A E4      4041 C      AH,AX        ;
1673 8A C3      4042 C      MOV AL,BL     ;
1675 52         4043 C      PUSH DX        ;
1677 F7 26 0485 R 4044 C      POINTS     ;
1679 8B F8      4045 C      MUL CRT_COLS  ;
167B 2B D1      4046 C      SUB DX,CX      ;
167D 81 C2 0101 4047 C      ADD DX,0101H  ;
167F 2A E4      4048 C      AH,AX        ;
1681 8A C3      4049 C      MOV AL,BL     ;
1683 52         4050 C      PUSH DX        ;
1685 F7 26 044A R 4051 C      POINTS     ;
1687 8B F8      4052 C      MUL CRT_COLS  ;
1689 2B D1      4053 C      SUB DX,CX      ;
168B 81 C2 0101 4054 C      ADD DX,0101H  ;
168D 2A E4      4055 C      AH,AX        ;
168F 8A C3      4056 C      MOV AL,BL     ;
1691 52         4057 C      PUSH DX        ;
1693 F7 26 0485 R 4058 C      POINTS     ;
1695 8B F8      4059 C      MUL CRT_COLS  ;
1697 2B D1      4060 C      SUB DX,CX      ;
1699 81 C2 0101 4061 C      ADD DX,0101H  ;
1701 2A E4      4062 C      AH,AX        ;
1703 8A C3      4063 C      MOV AL,BL     ;
1705 52         4064 C      PUSH DX        ;
1707 F7 26 044A R 4065 C      POINTS     ;
1709 8B F8      4066 C      MUL CRT_COLS  ;
170B 2B D1      4067 C      SUB DX,CX      ;
170D 81 C2 0101 4068 C      ADD DX,0101H  ;
170F 2A E4      4069 C      AH,AX        ;
1711 8A C3      4070 C      MOV AL,BL     ;
1713 52         4071 C      PUSH DX        ;
1715 F7 26 0485 R 4072 C      POINTS     ;
1717 8B F8      4073 C      MUL CRT_COLS  ;
1719 2B D1      4074 C      SUB DX,CX      ;
171B 81 C2 0101 4075 C      ADD DX,0101H  ;
171D 2A E4      4076 C      AH,AX        ;
171F 8A C3      4077 C      MOV AL,BL     ;
1721 52         4078 C      PUSH DX        ;
1723 F7 26 044A R 4079 C      POINTS     ;
1725 8B F8      4080 C      MUL CRT_COLS  ;
1727 2B D1      4081 C      SUB DX,CX      ;
1729 81 C2 0101 4082 C      ADD DX,0101H  ;
172B 2A E4      4083 C      AH,AX        ;
172D 8A C3      4084 C      MOV AL,BL     ;
172F 52         4085 C      PUSH DX        ;
1731 F7 26 0485 R 4086 C      POINTS     ;
1733 8B F8      4087 C      MUL CRT_COLS  ;
1735 2B D1      4088 C      SUB DX,CX      ;
1737 81 C2 0101 4089 C      ADD DX,0101H  ;
1739 2A E4      4090 C      AH,AX        ;
173B 8A C3      4091 C      MOV AL,BL     ;
173D 52         4092 C      PUSH DX        ;
173F F7 26 044A R 4093 C      POINTS     ;
1741 8B F8      4094 C      MUL CRT_COLS  ;
1743 2B D1      4095 C      SUB DX,CX      ;
1745 81 C2 0101 4096 C      ADD DX,0101H  ;
1747 2A E4      4097 C      AH,AX        ;
1749 8A C3      4098 C      MOV AL,BL     ;
174B 52         4099 C      PUSH DX        ;
174D F7 26 0485 R 4100 C      POINTS     ;
174F 8B F8      4101 C      MUL CRT_COLS  ;
1751 2B D1      4102 C      SUB DX,CX      ;
1753 81 C2 0101 4103 C      ADD DX,0101H  ;
1755 2A E4      4104 C      AH,AX        ;
1757 8A C3      4105 C      MOV AL,BL     ;
1759 52         4106 C      PUSH DX        ;
175B F7 26 044A R 4107 C      POINTS     ;
175D 8B F8      4108 C      MUL CRT_COLS  ;
175F 2B D1      4109 C      SUB DX,CX      ;
1761 81 C2 0101 4110 C      ADD DX,0101H  ;
1763 2A E4      4111 C      AH,AX        ;
1765 8A C3      4112 C      MOV AL,BL     ;
1767 52         4113 C      PUSH DX        ;
1769 F7 26 0485 R 4114 C      POINTS     ;
176B 8B F8      4115 C      MUL CRT_COLS  ;
176D 2B D1      4116 C      SUB DX,CX      ;
176F 81 C2 0101 4117 C      ADD DX,0101H  ;
1771 2A E4      4118 C      AH,AX        ;
1773 8A C3      4119 C      MOV AL,BL     ;
1775 52         4120 C      PUSH DX        ;
1777 F7 26 044A R 4121 C      POINTS     ;
1779 8B F8      4122 C      MUL CRT_COLS  ;
177B 2B D1      4123 C      SUB DX,CX      ;
177D 81 C2 0101 4124 C      ADD DX,0101H  ;
177F 2A E4      4125 C      AH,AX        ;
1781 8A C3      4126 C      MOV AL,BL     ;
1783 52         4127 C      PUSH DX        ;
1785 F7 26 0485 R 4128 C      POINTS     ;
1787 8B F8      4129 C      MUL CRT_COLS  ;
1789 2B D1      4130 C      SUB DX,CX      ;
178B 81 C2 0101 4131 C      ADD DX,0101H  ;
178D 2A E4      4132 C      AH,AX        ;
178F 8A C3      4133 C      MOV AL,BL     ;
1791 52         4134 C      PUSH DX        ;
1793 F7 26 044A R 4135 C      POINTS     ;
1795 8B F8      4136 C      MUL CRT_COLS  ;
1797 2B D1      4137 C      SUB DX,CX      ;
1799 81 C2 0101 4138 C      ADD DX,0101H  ;
1801 2A E4      4139 C      AH,AX        ;
1803 8A C3      4140 C      MOV AL,BL     ;
1805 52         4141 C      PUSH DX        ;
1807 F7 26 0485 R 4142 C      POINTS     ;
1809 8B F8      4143 C      MUL CRT_COLS  ;
180B 2B D1      4144 C      SUB DX,CX      ;
180D 81 C2 0101 4145 C      ADD DX,0101H  ;
180F 2A E4      4146 C      AH,AX        ;
1811 8A C3      4147 C      MOV AL,BL     ;
1813 52         4148 C      PUSH DX        ;
1815 F7 26 044A R 4149 C      POINTS     ;
1817 8B F8      4150 C      MUL CRT_COLS  ;
1819 2B D1      4151 C      SUB DX,CX      ;
181B 81 C2 0101 4152 C      ADD DX,0101H  ;
181D 2A E4      4153 C      AH,AX        ;
181F 8A C3      4154 C      MOV AL,BL     ;
1821 52         4155 C      PUSH DX        ;
1823 F7 26 0485 R 4156 C      POINTS     ;
1825 8B F8      4157 C      MUL CRT_COLS  ;
1827 2B D1      4158 C      SUB DX,CX      ;
1829 81 C2 0101 4159 C      ADD DX,0101H  ;
182B 2A E4      4160 C      AH,AX        ;
182D 8A C3      4161 C      MOV AL,BL     ;
182F 52         4162 C      PUSH DX        ;
1831 F7 26 044A R 4163 C      POINTS     ;
1833 8B F8      4164 C      MUL CRT_COLS  ;
1835 2B D1      4165 C      SUB DX,CX      ;
1837 81 C2 0101 4166 C      ADD DX,0101H  ;
1839 2A E4      4167 C      AH,AX        ;
183B 8A C3      4168 C      MOV AL,BL     ;
183D 52         4169 C      PUSH DX        ;
183F F7 26 0485 R 4170 C      POINTS     ;
1841 8B F8      4171 C      MUL CRT_COLS  ;
1843 2B D1      4172 C      SUB DX,CX      ;
1845 81 C2 0101 4173 C      ADD DX,0101H  ;
1847 2A E4      4174 C      AH,AX        ;
1849 8A C3      4175 C      MOV AL,BL     ;
184B 52         4176 C      PUSH DX        ;
184D F7 26 044A R 4177 C      POINTS     ;
184F 8B F8      4178 C      MUL CRT_COLS  ;
1851 2B D1      4179 C      SUB DX,CX      ;
1853 81 C2 0101 4180 C      ADD DX,0101H  ;
1855 2A E4      4181 C      AH,AX        ;
1857 8A C3      4182 C      MOV AL,BL     ;
1859 52         4183 C      PUSH DX        ;
185B F7 26 0485 R 4184 C      POINTS     ;
185D 8B F8      4185 C      MUL CRT_COLS  ;
185F 2B D1      4186 C      SUB DX,CX      ;
1861 81 C2 0101 4187 C      ADD DX,0101H  ;
1863 2A E4      4188 C      AH,AX        ;
1865 8A C3      4189 C      MOV AL,BL     ;
1867 52         4190 C      PUSH DX        ;
1869 F7 26 044A R 4191 C      POINTS     ;
186B 8B F8      4192 C      MUL CRT_COLS  ;
186D 2B D1      4193 C      SUB DX,CX      ;
186F 81 C2 0101 4194 C      ADD DX,0101H  ;
1871 2A E4      4195 C      AH,AX        ;
1873 8A C3      4196 C      MOV AL,BL     ;
1875 52         4197 C      PUSH DX        ;
1877 F7 26 0485 R 4198 C      POINTS     ;
1879 8B F8      4199 C      MUL CRT_COLS  ;
187B 2B D1      4200 C      SUB DX,CX      ;
187D 81 C2 0101 4201 C      ADD DX,0101H  ;
187F 2A E4      4202 C      AH,AX        ;
1881 8A C3      4203 C      MOV AL,BL     ;
1883 52         4204 C      PUSH DX        ;
1885 F7 26 044A R 4205 C      POINTS     ;
1887 8B F8      4206 C      MUL CRT_COLS  ;
1889 2B D1      4207 C      SUB DX,CX      ;
188B 81 C2 0101 4208 C      ADD DX,0101H  ;
188D 2A E4      4209 C      AH,AX        ;
188F 8A C3      4210 C      MOV AL,BL     ;
1891 52         4211 C      PUSH DX        ;
1893 F7 26 0485 R 4212 C      POINTS     ;
1895 8B F8      4213 C      MUL CRT_COLS  ;
1897 2B D1      4214 C      SUB DX,CX      ;
1899 81 C2 0101 4215 C      ADD DX,0101H  ;
1901 2A E4      4216 C      AH,AX        ;
1903 8A C3      4217 C      MOV AL,BL     ;
1905 52         4218 C      PUSH DX        ;
1907 F7 26 044A R 4219 C      POINTS     ;
1909 8B F8      4220 C      MUL CRT_COLS  ;
190B 2B D1      4221 C      SUB DX,CX      ;
190D 81 C2 0101 4222 C      ADD DX,0101H  ;
190F 2A E4      4223 C      AH,AX        ;
1911 8A C3      4224 C      MOV AL,BL     ;
1913 52         4225 C      PUSH DX        ;
1915 F7 26 0485 R 4226 C      POINTS     ;
1917 8B F8      4227 C      MUL CRT_COLS  ;
1919 2B D1      4228 C      SUB DX,CX      ;
191B 81 C2 0101 4229 C      ADD DX,0101H  ;
191D 2A E4      4230 C      AH,AX        ;
191F 8A C3      4231 C      MOV AL,BL     ;
1921 52         4232 C      PUSH DX        ;
1923 F7 26 044A R 4233 C      POINTS     ;
1925 8B F8      4234 C      MUL CRT_COLS  ;
1927 2B D1      4235 C      SUB DX,CX      ;
1929 81 C2 0101 4236 C      ADD DX,0101H  ;
192B 2A E4      4237 C      AH,AX        ;
192D 8A C3      4238 C      MOV AL,BL     ;
192F 52         4239 C      PUSH DX        ;
1931 F7 26 0485 R 4240 C      POINTS     ;
1933 8B F8      4241 C      MUL CRT_COLS  ;
1935 2B D1      4242 C      SUB DX,CX      ;
1937 81 C2 0101 4243 C      ADD DX,0101H  ;
1939 2A E4      4244 C      AH,AX        ;
193B 8A C3      4245 C      MOV AL,BL     ;
193D 52         4246 C      PUSH DX        ;
193F F7 26 044A R 4247 C      POINTS     ;
1941 8B F8      4248 C      MUL CRT_COLS  ;
1943 2B D1      4249 C      SUB DX,CX      ;
1945 81 C2 0101 4250 C      ADD DX,0101H  ;
1947 2A E4      4251 C      AH,AX        ;
1949 8A C3      4252 C      MOV AL,BL     ;
194B 52         4253 C      PUSH DX        ;
194D F7 26 0485 R 4254 C      POINTS     ;
194F 8B F8      4255 C      MUL CRT_COLS  ;
1951 2B D1      4256 C      SUB DX,CX      ;
1953 81 C2 0101 4257 C      ADD DX,0101H  ;
1955 2A E4      4258 C      AH,AX        ;
1957 8A C3      4259 C      MOV AL,BL     ;
1959 52         4260 C      PUSH DX        ;
195B F7 26 044A R 4261 C      POINTS     ;
195D 8B F8      4262 C      MUL CRT_COLS  ;
195F 2B D1      4263 C      SUB DX,CX      ;
1961 81 C2 0101 4264 C      ADD DX,0101H  ;
1963 2A E4      4265 C      AH,AX        ;
1965 8A C3      4266 C      MOV AL,BL     ;
1967 52         4267 C      PUSH DX        ;
1969 F7 26 0485 R 4268 C      POINTS     ;
196B 8B F8      4269 C      MUL CRT_COLS  ;
196D 2B D1      4270 C      SUB DX,CX      ;
196F 81 C2 0101 4271 C      ADD DX,0101H  ;
1971 2A E4      4272 C      AH,AX        ;
1973 8A C3      4273 C      MOV AL,BL     ;
1975 52         4274 C      PUSH DX        ;
1977 F7 26 044A R 4275 C      POINTS     ;
1979 8B F8      4276 C      MUL CRT_COLS  ;
197B 2B D1      4277 C      SUB DX,CX      ;
197D 81 C2 0101 4278 C      ADD DX,0101H  ;
197F 2A E4      4279 C      AH,AX        ;
1981 8A C3      4280 C      MOV AL,BL     ;
1983 52         4281 C      PUSH DX        ;
1985 F7 26 0485 R 4282 C      POINTS     ;
1987 8B F8      4283 C      MUL CRT_COLS  ;
1989 2B D1      4284 C      SUB DX,CX      ;
198B 81 C2 0101 4285 C      ADD DX,0101H  ;
198D 2A E4      4286 C      AH,AX        ;
198F 8A C3      4287 C      MOV AL,BL     ;
1991 52         4288 C      PUSH DX        ;
1993 F7 26 044A R 4289 C      POINTS     ;
1995 8B F8      4289 C      MUL CRT_COLS  ;
1997 2B D1      4290 C      SUB DX,CX      ;
1999 81 C2 0101 4291 C      ADD DX,0101H  ;
199B 2A E4      4292 C      AH,AX        ;
199D 8A C3      4293 C      MOV AL,BL     ;
199F 52         4294 C      PUSH DX        ;
199B 2A E4      4295 C      AH,AX        ;
199D 8A C3      4296 C      MOV AL,BL     ;
199F 52         4297 C      PUSH DX        ;
199B 2A E4      4298 C      AH,AX        ;
199D 8A C3      4299 C      MOV AL,BL     ;
199F 52         4300 C      PUSH DX        ;
199B 2A E4      4301 C      AH,AX        ;
199D 8A C3      4302 C      MOV AL,BL     ;
199F 52         4303 C      PUSH DX        ;
199B 2A E4      4304 C      AH,AX        ;
199D 8A C3      4305 C      MOV AL,BL     ;
199F 52         4306 C      PUSH DX        ;
199B 2A E4      4307 C      AH,AX        ;
199D 8A C3      4308 C      MOV AL,BL     ;
199F 52         4309 C      PUSH DX        ;
199B 2A E4      4310 C      AH,AX        ;
199D 8A C3      4311 C      MOV AL,BL     ;
199F 52         4312 C      PUSH DX        ;
199B 2A E4      4313 C      AH,AX        ;
199D 8A C3      4314 C      MOV AL,BL     ;
199F 52         4315 C      PUSH DX        ;
199B 2A E4      4316 C      AH,AX        ;
199D 8A C3      4317 C      MOV AL,BL     ;
199F 52         4318 C      PUSH DX        ;
199B 2A E4      4319 C      AH,AX        ;
199D 8A C3      4320 C      MOV AL,BL     ;
199F 52         4321 C      PUSH DX        ;
199B 2A E4      4322 C      AH,AX        ;
199D 8A C3      4323 C      MOV AL,BL     ;
199F 52         4324 C      PUSH DX        ;
199B 2A E4      4325 C      AH,AX        ;
199D 8A C3      4326 C      MOV AL,BL     ;
199F 52         4327 C      PUSH DX        ;
199B 2A E4      4328 C      AH,AX        ;
199D 8A C3      4329 C      MOV AL,BL     ;
199F 52         4330 C      PUSH DX        ;
199B 2A E4      4331 C      AH,AX        ;
199D 8A C3      4332 C      MOV AL,BL     ;
199F 52         4333 C      PUSH DX        ;
199B 2A E4      4334 C      AH,AX        ;
199D 8A C3      4335 C      MOV AL,BL     ;
199F 52         4336 C      PUSH DX        ;
199B 2A E4      4337 C      AH,AX        ;
199D 8A C3      4338 C      MOV AL,BL     ;
199F 52         4339 C      PUSH DX        ;
199B 2A E4      4340 C      AH,AX        ;
199D 8A C3      4341 C      MOV AL,BL     ;
199F 52         4342 C      PUSH DX        ;
199B 2A E4      4343 C      AH,AX        ;
199D 8A C3      4344 C      MOV AL,BL     ;
199F 52         4345 C      PUSH DX        ;
199B 2A E4      4346 C      AH,AX        ;
199D 8A C3      4347 C      MOV AL,BL     ;
199F 52         4348 C      PUSH DX        ;
199B 2A E4      4349 C      AH,AX        ;
199D 8A C3      4350 C      MOV AL,BL     ;
199F 52         4351 C      PUSH DX        ;
199B 2A E4      4352 C      AH,AX        ;
199D 8A C3      4353 C      MOV AL,BL     ;
199F 52         4354 C      PUSH DX        ;
199B 2A E4      4355 C      AH,AX        ;
199D 8A C3      4356 C      MOV AL,BL     ;
199F 52         4357 C      PUSH DX        ;
199B 2A E4      4358 C      AH,AX        ;
199D 8A C3      4359 C      MOV AL,BL     ;
199F 52         4360 C      PUSH DX        ;
199B 2A E4      4361 C      AH,AX        ;
199D 8A C3      4362 C      MOV AL,BL     ;
199F 52         4363 C      PUSH DX        ;
199B 2A E4      4364 C      AH,AX        ;
199D 8A C3      4365 C      MOV AL,BL     ;
199F 52         4366 C      PUSH DX        ;
199B 2A E4      4367 C      AH,AX        ;
199D 8A C3      4368 C      MOV AL,BL     ;
199F 52         4369 C      PUSH DX        ;
199B 2A E4      4370 C      AH,AX        ;
199D 8A C3      4371 C      MOV AL,BL     ;
199F 52         4372 C      PUSH DX        ;
199B 2A E4      4373 C      AH,AX        ;
199D 8A C3      4374 C      MOV AL,BL     ;
199F 52         4375 C      PUSH DX        ;
199B 2A E4      4376 C      AH,AX        ;
199D 8A C3      4377 C      MOV AL,BL     ;
199F 52         4378 C      PUSH DX        ;
199B 2A E4      4379 C      AH,AX        ;
199D 8A C3      4380 C      MOV AL,BL     ;
199F 52         4381 C      PUSH DX        ;
199B 2A E4      4382 C      AH,AX        ;
199D 8A C3      4383 C      MOV AL,BL     ;
199F 52         4384 C      PUSH DX        ;
199B 2A E4      4385 C      AH,AX        ;
199D 8A C3      4386 C      MOV AL,BL     ;
199F 52         4387 C      PUSH DX        ;
199B 2A E4      4388 C      AH,AX        ;
199D 8A C3      4389 C      MOV AL,BL     ;
199F 52         4390 C      PUSH DX        ;
199B 2A E4      4391 C      AH,AX        ;
199D 8A C3      4392 C      MOV AL,BL     ;
199F 52         4393 C      PUSH DX        ;
199B 2A E4      4394 C      AH,AX        ;
199D 8A C3      4395 C      MOV AL,BL     ;
199F 52         4396 C      PUSH DX        ;
199B 2A E4      4397 C      AH,AX        ;
199D 8A C3      4398 C      MOV AL,BL     ;
199F 52         4399 C      PUSH DX        ;
199B 2A E4      4400 C      AH,AX        ;
199D 8A C3      4401 C      MOV AL,BL     ;
199F 52         4402 C      PUSH DX        ;
199B 2A E4      4403 C      AH,AX        ;
199D 8A C3      4404 C      MOV AL,BL     ;
199F 52         4405 C      PUSH DX        ;
199B 2A E4      4406 C      AH,AX        ;
199D 8A C3      4407 C      MOV AL,BL     ;
199F 52         4408 C      PUSH DX        ;
199B 2A E4      4409 C      AH,AX        ;
199D 8A C3      4410 C      MOV AL,BL     ;
199F 52         4411 C      PUSH DX        ;
199B 2A E4      4412 C      AH,AX        ;
199D 8A C3      4413 C      MOV AL,BL     ;
199F 52         4414 C      PUSH DX        ;
199B 2A E4      4415 C      AH,AX        ;
199D 8
```

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4033 C ; 16 BITS. THE RESULT IS LEFT IN AX
4034 C
4035 C
1682 52 S21 PROC NEAR
1682 52 C PUSH DX ; SAVE REGISTERS
1683 51 C PUSH CX
1684 53 C PUSH BX
1685 2B D2 C SUB DX,DX ; RESULT REGISTER
1687 B9 0001 C MOV CX,1 ; MASK REGISTER
168A C
168A 8B D8 C MOV BX,AX ; BASE INTO TEMP
168C 23 D9 C AND BX,CX ; USE MASK TO EXTRACT BIT
168E 0B D3 C OR DX,BX ; PUT INTO RESULT REGISTER
1690 D1 E0 C SHL AX,1
1692 D1 E1 C SHL CX,1 ; SHIFT BASE AND MASK BY 1
1694 8B D8 C MOV BX,AX ; BASE TO TEMP
1696 23 D9 C AND BX,CX ; EXTRACT THE SAME BIT
1698 0B D3 C OR DX,BX ; PUT INTO RESULT
169A D1 E1 C SHL CX,1 ; SHIFT ONLY MASK NOW,
; MOVING TO NEXT BASE
169C 73 EC C JNC S22 ; USE MASK BIT COMING OUT
; TO TERMINATE
169E 8B C2 C MOV AX,DX ; RESULT TO PARAM REGISTER
16A0 5B C POP BX
16A1 59 C POP CX
16A2 5A C POP DX
16A3 C3 C RET ; RECOVER REGISTERS
16A4 C ; ALL DONE
;
4050 C
4051 C
4052 C
4053 C
4054 C
4055 C
4056 C
4057 C
4058 C
4059 C
4060 C
4061 C
4062 C
4063 C
4064 C
4065 C
4066 C
4067 C
4068 C
4069 C
4070 C
4071 C
4072 C
4073 C
4074 C
4075 C
4076 C
4077 C
4078 C
4079 C
4080 C
4081 C
4082 C
4083 C
16BA C
16BA A1 0450 R C
16A4 C
16A7 53 C
16A8 8B D8 C
16AA 8A C4 C
16AC F6 26 044A R C
16B0 D1 E0 C
16B2 D1 E0 C
16B4 2A FF C
16B6 03 C3 C
16B8 5B C
16B9 C3 C
16BA C
;
4084 C
4085 C
4086 C
4087 C
4088 C
4089 C
4090 C
4091 C
4092 C
4093 C
4094 C
4095 C
4096 C
4097 C
4098 C
4099 C
4100 C
4101 C
4102 C
4103 C
4104 C
4105 C
4106 C
4107 C
4108 C
4109 C
4110 C
4111 C
4112 C
4113 C
4114 C
4115 C
4116 C
4117 C
4118 C
4119 C
4120 C
4121 C
4122 C
4123 C
4124 C
4125 C
4126 C
4127 C
4128 C
4129 C
4130 C
4131 C
4132 C
4133 C
4134 C
4135 C
4136 C
4137 C
4138 C
4139 C
4140 C
4141 C
4142 C
4143 C
4144 C
4145 C
4146 C
4147 C
4148 C
4149 C
4150 C
4151 C
4152 C
4153 C
4154 C
4155 C
4156 C
4157 C
4158 C
;
; GR_CUR
; ENTRY
; BH = DISPLAY PAGE
; EXIT
; AX = CURSOR POSITION FOR REQUESTED PAGE
;
4084 C
4085 C
4086 C
4087 C
4088 C
4089 C
4090 C
4091 C
4092 C
4093 C
4094 C
4095 C
4096 C
4097 C
4098 C
4099 C
4100 C
4101 C
4102 C
4103 C
4104 C
4105 C
4106 C
4107 C
4108 C
4109 C
4110 C
4111 C
4112 C
4113 C
4114 C
4115 C
4116 C
4117 C
4118 C
4119 C
4120 C
4121 C
4122 C
4123 C
4124 C
4125 C
4126 C
4127 C
4128 C
4129 C
4130 C
4131 C
4132 C
4133 C
4134 C
4135 C
4136 C
4137 C
4138 C
4139 C
4140 C
4141 C
4142 C
4143 C
4144 C
4145 C
4146 C
4147 C
4148 C
4149 C
4150 C
4151 C
4152 C
4153 C
4154 C
4155 C
4156 C
4157 C
4158 C
;
; GRX_PSN
; PROC NEAR
; PUSH BX ; SAVE
; PUSH CX ; SAVE
; PUSH DX ; SAVE
; SUB CH,CH ; ZERO
; MOV CL,BH ; PAGE NUMBER
; MOV BX,AX ; ROW, COLUMN
; MOV AL,AH ; ROW
; MUL BYTE PTR CRT_COLS ; ROW * COLUMNS/ROW
; MUL POINTS ; BYTES PER ROW
; SUB BH,BH ; ZERO TO LEAVE COL VALUE
; ADD AX,BX ; ADD IN COLUMN
; MOV BX,CRT_LEN ; PAGE LENGTH
; JCNZ GP_2 ; NO PAGE OFFSET
;
4099 C
4100 C
4101 C
4102 C
4103 C
4104 C
4105 C
4106 C
4107 C
4108 C
4109 C
4110 C
4111 C
4112 C
4113 C
4114 C
4115 C
4116 C
4117 C
4118 C
4119 C
4120 C
4121 C
4122 C
4123 C
4124 C
4125 C
4126 C
4127 C
4128 C
4129 C
4130 C
4131 C
4132 C
4133 C
4134 C
4135 C
4136 C
4137 C
4138 C
4139 C
4140 C
4141 C
4142 C
4143 C
4144 C
4145 C
4146 C
4147 C
4148 C
4149 C
4150 C
4151 C
4152 C
4153 C
4154 C
4155 C
4156 C
4157 C
4158 C
;
; GP_3
; ADD AX,BX ; ADD IN THE PAGE LENGTH
; LOOP GP_3 ; DO FOR NUMBER OF PAGES
;
4116 C
4117 C
4118 C
4119 C
4120 C
4121 C
4122 C
4123 C
4124 C
4125 C
4126 C
4127 C
4128 C
4129 C
4130 C
4131 C
4132 C
4133 C
4134 C
4135 C
4136 C
4137 C
4138 C
4139 C
4140 C
4141 C
4142 C
4143 C
4144 C
4145 C
4146 C
4147 C
4148 C
4149 C
4150 C
4151 C
4152 C
4153 C
4154 C
4155 C
4156 C
4157 C
4158 C
;
; GP_2
; POP DX ; RECOVER
; POP CX ; RECOVER
; POP BX ; RECOVER
; RET
;
4116 C
4117 C
4118 C
4119 C
4120 C
4121 C
4122 C
4123 C
4124 C
4125 C
4126 C
4127 C
4128 C
4129 C
4130 C
4131 C
4132 C
4133 C
4134 C
4135 C
4136 C
4137 C
4138 C
4139 C
4140 C
4141 C
4142 C
4143 C
4144 C
4145 C
4146 C
4147 C
4148 C
4149 C
4150 C
4151 C
4152 C
4153 C
4154 C
4155 C
4156 C
4157 C
4158 C
;
; GRX_PSN ENDP
;
4122 C
4123 C
4124 C
4125 C
4126 C
4127 C
4128 C
4129 C
4130 C
4131 C
4132 C
4133 C
4134 C
4135 C
4136 C
4137 C
4138 C
4139 C
4140 C
4141 C
4142 C
4143 C
4144 C
4145 C
4146 C
4147 C
4148 C
4149 C
4150 C
4151 C
4152 C
4153 C
4154 C
4155 C
4156 C
4157 C
4158 C
;
; MK_ES
; MOV SI,0B800H
; MOV DI,EQUIP_FLAG
; AND D1,030H
; CMP D1,030H
; JNE P6_A
; MOV SI,0B000H
;
4124 C
4125 C
4126 C
4127 C
4128 C
4129 C
4130 C
4131 C
4132 C
4133 C
4134 C
4135 C
4136 C
4137 C
4138 C
4139 C
4140 C
4141 C
4142 C
4143 C
4144 C
4145 C
4146 C
4147 C
4148 C
4149 C
4150 C
4151 C
4152 C
4153 C
4154 C
4155 C
4156 C
4157 C
4158 C
;
; P6_A
; MOV ES,S1
; RET
;
4132 C
4133 C
4134 C
4135 C
4136 C
4137 C
4138 C
4139 C
4140 C
4141 C
4142 C
4143 C
4144 C
4145 C
4146 C
4147 C
4148 C
4149 C
4150 C
4151 C
4152 C
4153 C
4154 C
4155 C
4156 C
4157 C
4158 C
;
; READ_AC_CURRENT
; THIS ROUTINE READS THE ATTRIBUTE AND CHARACTER
; AT THE CURRENT CURSOR POSITION AND RETURNS THEM
; TO THE CALLER
; INPUT
; (AH) = CURRENT CRT MODE
; (BH) = DISPLAY PAGE ( ALPHA MODES ONLY )
; (DS) = DATA SEGMENT
; (ES) = REGEN SEGMENT
; OUTPUT
; (AL) = CHAR READ
; (AH) = ATTRIBUTE READ
;
4134 C
4135 C
4136 C
4137 C
4138 C
4139 C
4140 C
4141 C
4142 C
4143 C
4144 C
4145 C
4146 C
4147 C
4148 C
4149 C
4150 C
4151 C
4152 C
4153 C
4154 C
4155 C
4156 C
4157 C
4158 C
;
; ASSUME CS:CODE,DS:ABS0,ES:NOTHING
; READ_AC_CURRENT PROC NEAR
; CALL MK_ES
; CALL FIND_POSITION
; MOV SI,BX ; ADDRESSING IN S1
;
4149 C
4150 C
4151 C
4152 C
4153 C
4154 C
4155 C
4156 C
4157 C
4158 C
;
; MOV DX,ADDR_6845 ; GET BASE ADDRESS
; ADD DX,6 ; POINT AT STATUS PORT
;
4153 C
4154 C
4155 C
4156 C
4157 C
4158 C
;
; TEST INFO,4

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1715 06          4159 C      PUSH    ES
1716 1F          4160 C      POP     DS                ; SEGMENT FOR QUICK ACCESS
1717 74 0B       4161 C      JZ      P3A
1718          4162 C      ;-----WAIT FOR HORIZONTAL RETRACE
1719          4163 C
1719          4164 C
1719          4165 C
1719          4166 C
1719 EC          4167 C      P2:          IN      AL,DX                ; WAIT FOR RETRACE LOW
171A A8 01       4168 C      TEST     AL,1                ; GET STATUS
171C 75 F8       4169 C      JNZ     P2                ; IS HORIZ RETRACE LOW
171E FA          4170 C      CLI      P2                ; WAIT UNTIL IT IS
171F          4171 C      ; NO MORE INTERRUPTS
171F EC          4172 C      P3:          IN      AL,DX                ; WAIT FOR RETRACE HIGH
1720 A8 01       4173 C      TEST     AL,1                ; GET STATUS
1722 74 F8       4174 C      JZ      P3                ; IS IT HIGH
1724          4175 C      ; WAIT UNTIL IT IS
1724 AD          4176 C      P3A:         LODSW    V_RET                ; GET THE CHAR/ATTR
1725 E9 219E R    4177 C      JMP      V_RET
1728          4178 C      READ_AC_CURRENT ENDP
1729          4179 C
1729          4180 C
1729          4181 C      ;-----MED_READ_BYTE-----
1729          4182 C      THIS ROUTINE WILL TAKE 2 BYTES FROM THE REGEN
1729          4183 C      BUFFER, COMPARE AGAINST THE CURRENT FOREGROUND
1729          4184 C      COLOR, AND PLACE THE CORRESPONDING ON/OFF BIT
1729          4185 C      PATTERN INTO THE CURRENT POSITION IN THE SAVE
1729          4186 C      AREA
1729          4187 C
1729          4188 C      ENTRY      SI,DS = POINTER TO REGEN AREA OF INTEREST
1729          4189 C      BX = EXPANDED FOREGROUND COLOR
1729          4190 C      BP = POINTER TO SAVE AREA
1729          4191 C
1729          4192 C      ; EXIT
1729          4193 C      BP IS INCREMENT AFTER SAVE
1729          4194 C      ;-----
1728          4195 C      S23:        PROC    NEAR
1728 8A 24         4196 C      MOV     AH,[SI]                ; GET FIRST BYTE
1728 8A 44 01     4197 C      MOV     AL,[SI+1]            ; GET SECOND BYTE
1728 B9 C000     4198 C      MOV     CX,0C000H            ; 2 BIT MASK TO TEST
1729          4199 C      ; THE ENTRIES
1729          4200 C      ; RESULT REGISTER
1729          4201 C
1729          4202 C      S24:        TEST    AX,CX                ; IS THIS BACKGROUND?
1729          4203 C      CLC                      ; CLEAR CARRY IN HOPES
1729          4204 C      ; THAT IT IS
1729          4205 C      JZ      S25                ; IF 0, IT IS BACKGROUND
1729          4206 C      STC                      ; WASN'T, SO SET CARRY
1729          4207 C
1729          4208 C      S25:        RCL     DL,1                ; MOVE THAT BIT INTO THE
1729          4209 C      SHR     CX,1                ; RESULT
1729          4210 C      SHR     CX,1                ; MOVE THE MASK TO THE
1729          4211 C      JNC     S24                ; RIGHT BY 2 BITS
1729          4212 C      ; DO IT AGAIN IF MASK
1729          4213 C      MOV     [BP],DL            ; DIDN'T FALL OUT
1729          4214 C      INC     BP                ; STORE RESULT IN SAVE
1729          4215 C      RET                      ; ADJUST POINTER
1729          4216 C      S23:        ENDP                ; ALL DONE
1729          4217 C
1729          4218 C
1729          4219 C      GRAPHICS_READ PROC    NEAR
1729          4220 C      CALL     MK_ES
1729          4221 C      CALL     S26
1729          4222 C      MOV     SI,AX
1729          4223 C      SUB     SP,8
1729          4224 C      MOV     BP,SP
1729          4225 C
1729          4226 C      ;----- DETERMINE GRAPHICS MODES
1729          4227 C
1729          4228 C
1729          4229 C      CMP     CRT_MODE,6
1729          4230 C      PUSH    ES
1729          4231 C      POP     DS                ; POINT TO REGEN SEGMENT
1729          4232 C      JC      S13P              ; MEDIUM RESOLUTION
1729          4233 C
1729          4234 C      ;----- HIGH RESOLUTION READ
1729          4235 C
1729          4236 C      ;----- GET VALUES FROM REGEN BUFFER AND CONVERT TO CODE POINT
1729          4237 C
1729          4238 C      MOV     DH,4                ; NUMBER OF PASSES
1729          4239 C
1729          4240 C      S12P:        MOV     AL,[SI]                ; GET FIRST BYTE
1729          4241 C      MOV     [BP],AL            ; SAVE IN STORAGE AREA
1729          4242 C      INC     BP                ; NEXT LOCATION BYTE
1729          4243 C      MOV     AL,[SI+2000H]        ; GET LOWER REGION BYTE
1729          4244 C      MOV     [BP],AL            ; ADJUST AND STORE
1729          4245 C      INC     BP
1729          4246 C      ADD     SI,80
1729          4247 C      DEC     DH
1729          4248 C      JNZ     S12P
1729          4249 C      JMP     S15P
1729          4250 C      ; DO IT SOME MORE
1729          4251 C      ; GO MATCH THE SAVED CODE
1729          4252 C      ; POINTS
1729          4253 C
1729          4254 C      ;----- MEDIUM RESOLUTION READ
1729          4255 C
1729          4256 C      S13P:        SAL     SI,1                ; MED.RES.READ
1729          4257 C      MOV     DH,4                ; OFFSET*2, 2 BYTES/CHAR
1729          4258 C      ; NUMBER OF PASSES
1729          4259 C
1729          4260 C      S14P:        CALL    S23                ; GET PAIR BYTES
1729          4261 C      ADD     SI,2000H            ; INTO SINGLE SAVE
1729          4262 C      CALL    S23                ; GO TO LOWER REGION
1729          4263 C      SUB     SI,2000H-80          ; GET THIS PAIR INTO SAVE
1729          4264 C      DEC     DH                ; ADJUST POINTER BACK INTO
1729          4265 C      JNZ     S14P              ; UPPER
1729          4266 C      ; KEEP GOING UNTIL 8 DONE
1729          4267 C
1729          4268 C      ;----- SAVE AREA HAS CHARACTER IN IT, MATCH IT
1729          4269 C
1729          4270 C      S15P:        PUSH    DS                ; FIND_CHAR
1729          4271 C      CALL     DDS
1729          4272 C      LES     DI,GRX_SET
1729          4273 C      POP     DS
1729          4274 C      SUB     BP,8
1729          4275 C      ; ESTABLISH ADDRESSING
1729          4276 C
1729          4277 C      MOV     SI,BP
1729          4278 C      CLD
1729          4279 C      MOV     AL,0
1729          4280 C      ; ADJUST POINTER TO
1729          4281 C      ; BEGINNING OF SAVE AREA
1729          4282 C
1729          4283 C      S16P:        PUSH    SS
1729          4284 C      POP     DS
1729          4285 C      MOV     DX,128
1729          4286 C      ; ENSURE DIRECTION
1729          4287 C      ; CURRENT CODE POINT BEING
1729          4288 C      ; MATCHED
1729          4289 C      ; ADDRESSING TO STACK
1729          4290 C      ; FOR THE STRING COMPARE
1729          4291 C      ; NUMBER TO TEST AGAINST
1729          4292 C
1729          4293 C      S17P:        PUSH    SI
1729          4294 C      PUSH    DI
1729          4295 C      ; SAVE SAVE AREA POINTER
1729          4296 C      ; SAVE CODE POINTER

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17A3 B9 0008      4285 C      MOV      CX,8          ; NUMBER OF BYTES TO MATCH
17A6 F3/ A6      4286 C      REPE     CMPSB         ; COMPARE THE 8 BYTES
17A8 5F          4287 C      D        DI          ; RECOVER THE POINTERS
17A9 5E          4288 C      POP      SI
17AA 74 1D       4289 C      JZ       S18P         ; IF ZERO FLAG SET,
                                ; THEN MATCH OCCURRED
17AC FE C0       4290 C      INC      AL          ; NO MATCH, MOVE TO NEXT
17AE 83 C7 08    4291 C      ADD      DI,8        ; NEXT CODE POINT
17B1 4A          4292 C      DEC      DX          ; LOOP CONTROL
17B2 75 ED       4293 C      JNZ      S17P        ; DO ALL OF THEM
                                ;
                                ;----- CHAR NOT MATCHED, MIGHT BE IN USER SUPPLIED SECOND HALF
                                ;
17B4 3C 00       4296 C      CMP      AL,0        ; AL <> 0 IF ONLY 1ST
                                ; HALF SCANNED
17B6 74 11       4299 C      JE       S18P        ; IF = 0, THEN ALL HAS
                                ; BEEN SCANNED
17B8 E8 0CFE R   4302 C      ASSUME    DS:ABS0
17BB C4 3E 007C R 4303 C      CALL     DDB         ; GET POINTER
17BF 8C C0       4304 C      LES      DI,EXT_PTR
17C1 0B C7       4305 C      MOV      AX,ES     ; SEE IF THE PNTR EXISTS
17C3 74 04       4306 C      OR       AX,DI     ; IF ALL 0, DOESN'T EXIST
17C5 B0 80       4307 C      JZ       S18P        ; NO SENSE LOOKING
17C7 EB D3       4308 C      MOV      AL,128    ; ORIGIN FOR SECOND HALF
                                ; GO BACK AND TRY FOR IT
                                ;
                                ;----- CHARACTER IS FOUND ( AL=0 IF NOT FOUND )
                                ;
17C9            4310 C      S18P:
17C9 83 C4 08    4311 C      ADD      SP,8        ; READJUST THE STACK,
17CC E9 219E R   4312 C      JMP      V_RET       ; THROW AWAY SAVE
17CF            4313 C      GRAPHICS_READ      ; ALL DONE
                                ;
                                ;----- READ CHARACTER/ATTRIBUTE AT CURRENT CURSOR POSITION
                                ;
17CF E9 1701 R   4320 C      AH8:
17CF            4321 C      JMP      READ_AC_CURRENT
17D2            4322 C      AH8:
17D2            4323 C      JMP      READ_AC_CURRENT
17D2            4324 C      AH8:
17D2 8A 26 0449 R 4325 C      ASSUME    DS:ABS0
17D6 80 FC 07    4326 C      MOV      AH,CRT_MODE ; GET THE CURRENT MODE
17D9 74 F4       4327 C      CMP      AH,07H
17DB 80 FC 03    4328 C      JE       AH8
17DE 76 EF       4329 C      CMP      AH,03H
17E0 80 FC 06    4330 C      JBE      AH8
17E3 77 03       4331 C      CMP      AH,06H
17E5 E9 1745 R   4332 C      JA       Z_1
17E8            4333 C      Z_1:
17E8 80 FC 0F    4334 C      JMP      GRAPHICS_READ
17EB 72 52       4335 C      CMP      AH,0FH
17ED E8 14F7 R   4336 C      JB       GRX_RD2
17F0 72 4D       4337 C      CALL     MEM_DET
17F2 EB 0A       4338 C      JCC      GRX_RD2
17F4 80 FC 0D    4339 C      JMP      SHORT GRX_RD1
17F7 73 46       4340 C      CMP      AH,0DH
17F9 B0 00       4341 C      JAE      GRX_RD2
17FB E9 219E R   4342 C      JAE      GRX_RD2
                                ; RANGE TEST
                                ; FOUR MAP READ
17FE            4343 C      MOV      AL,0
17FE            4344 C      JMP      V_RET
17FE            4345 C      GRX_RD1 PROC NEAR
17FE            4346 C      ASSUME    DS:ABS0
17FE            4347 C      SRLOAD    ES,0A000H
17FE            4348 C      MOV      ES,0A000H ; REGEN SEGMENT
17FE            4349 C      MOV      ES,DX
17FE            4350 C      GR_CUR
17FE            4351 C      MOV      SI,AX
17FE            4352 C      MOV      BX,POINTS
17FE            4353 C      SUB      SP,BX
17FE            4354 C      MOV      BP,SP
17FE            4355 C      ; BYTE OFFSET INTO REGEN
17FE            4356 C      ; SAVE IN SI
17FE            4357 C      ; BYTES PER CHARACTER
17FE            4358 C      ; ALLOCATE SPACE TO SAVE
17FE            4359 C      ; THE READ CODE POINT
17FE            4360 C      ; POINTER TO SAVE AREA
17FE            4361 C      ;----- GET VALUES FROM REGEN BUFFER AND CONVERT TO CODE POINT
17FE            4362 C      PUSH     BX
17FE            4363 C      AND      AL,1
17FE            4364 C      MOV      CL,AL
17FE            4365 C      MOV      AL,5
17FE            4366 C      SHL      AL,CL
17FE            4367 C      MOV      AH,COLOR
17FE            4368 C      MOV      DH,3
17FE            4369 C      MOV      DI,GRAPH_ADDR
17FE            4370 C      CALL     OUT_DX
17FE            4371 C      ; SET GRAPHICS CHIP
17FE            4372 C      MOV      AX,518H
17FE            4373 C      ; READ MODE
17FE            4374 C      CALL     OUT_DX
17FE            4375 C      ; SET GRAPHICS CHIP
17FE            4376 C      MOV      AL,ES:[SI]
17FE            4377 C      ; GET FIRST BYTE
17FE            4378 C      NOT      AL
17FE            4379 C      MOV      SS:[BP],AL
17FE            4380 C      INC      BP
17FE            4381 C      ADD      SI,CRT_COLS
17FE            4382 C      DEC      BX
17FE            4383 C      JNZ      S12_1
17FE            4384 C      POP      BP
17FE            4385 C      MOV      AX,510H
17FE            4386 C      MOV      GRX_RECQ
17FE            4387 C      GRX_RD1 ENDP
17FE            4388 C      GRX_RD2 PROC NEAR
17FE            4389 C      ASSUME    DS:ABS0
17FE            4390 C      SRLOAD    ES,0A000H
17FE            4391 C      MOV      ES,0A000H ; REGEN SEGMENT
17FE            4392 C      MOV      ES,DX
17FE            4393 C      GR_CUR
17FE            4394 C      MOV      SI,AX
17FE            4395 C      MOV      BX,POINTS
17FE            4396 C      SUB      SP,BX
17FE            4397 C      MOV      BP,SP
17FE            4398 C      ; BYTE OFFSET INTO REGEN
17FE            4399 C      ; SAVE IN SI
17FE            4400 C      ; BYTES PER CHARACTER
17FE            4401 C      ; ALLOCATE SPACE TO SAVE
17FE            4402 C      ; THE READ CODE POINT
17FE            4403 C      ; POINTER TO SAVE AREA
17FE            4404 C      ;----- GET VALUES FROM REGEN BUFFER AND CONVERT TO CODE POINT
17FE            4405 C      MOV      DH,3
17FE            4406 C      MOV      DI,GRAPH_ADDR
17FE            4407 C      MOV      AX,508H
17FE            4408 C      CALL     OUT_DX
17FE            4409 C      PUSH     BX
17FE            4410 C      S12:
17FE            4411 C      MOV      AL,ES:[SI]
17FE            4412 C      ; GET COLOR COMPARED BYTE
17FE            4413 C      NOT      AL
17FE            4414 C      MOV      SS:[BP],AL
17FE            4415 C      ; ADJUST
17FE            4416 C      INC      BP
17FE            4417 C      MOV      SI,CRT_COLS
17FE            4418 C      DEC      BX
17FE            4419 C      JNZ      S12
17FE            4420 C      ; NEXT LOCATION
17FE            4421 C      ; POINTER INTO REGEN
17FE            4422 C      ; LOOP CONTROL
17FE            4423 C      ; DO IT SOME MORE

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186C 5B          4411 C      POP      BX          ; RECOVER BYTES PER CHAR
186D B8 0500    4412 C      MOV     AX,500H      ; UNDO READ MODE
1870          4413 C      GRX_RD2  ENDP
1870          4415 C      GRX_REGC:
1870          4416 C      ;----- SAVE AREA HAS CHARACTER IN IT, MATCH IT
1870          4417 C      ;-----
1870          4418 C      CALL    OUT_DX      ; SET READ MODE BACK
1873 C4 3E 010C R 4419 C      LES     DI,GRX_SET  ; GET FONT DEFINITIONS
1877 2B EB       4420 C      SUB     BP,BX      ; ADJUST POINTER TO
1877          4421 C      ; BEGINNING OF SAVE AREA
1879 8B F5       4422 C      MOV     SI,BP      ; ENSURE DIRECTION
187B FC         4423 C      CLD             ; CODE POINT BEING MATCHED
187C 80 00       4424 C      MOV     AL,0      ; ADDRESSING TO STACK
187E 16         4425 C      PUSH    SS        ; FOR THE STRING COMPARE
187F 1F         4426 C      POP     DS        ; NUMBER TO TEST AGAINST
1880 BA 0100     4427 C      MOV     DX,256D
1883          4428 C      ;-----
1883          4429 C      S17_5:
1884 57          4430 C      PUSH    SI        ; SAVE SAVE AREA POINTER
1885 8B CB       4431 C      PUSH    DI        ; SAVE CODE POINTER
1887 F3/ A6     4432 C      MOV     CX,BX      ; NUMBER OF BYTES TO MATCH
1889 5F         4433 C      REPE    CMPSB      ; COMPARE THE 8 BYTES
188A 5E         4434 C      POP     DI        ; RECOVER THE POINTERS
188B 74 07       4435 C      POP     SI        ; IF ZFL SET, THEN MATCH
188D FE 00       4436 C      JZ      S18_5     ; OCCURRED
188F 03 FB       4437 C      INC     AL        ; NO MATCH, ON TO NEXT
1891 4A         4438 C      ADD     DI,BX      ; NEXT CODE POINT
1892 75 EF       4439 C      DEC     DX        ; LOOP CONTROL
1894          4440 C      JNZ     S17_5     ; DO ALL OF THEM
1894 03 E3       4441 C      S18_5:
1896 E9 219E R   4442 C      ADD     SP,BX      ; AL=CHAR, 0 IF NOT FOUND
1896          4443 C      JMP     V_RET    ; READJUST THE STACK
1896          4444 C      ;-----
1896          4445 C      ;----- WRITE CHARACTER/ATTRIBUTE AT CURRENT CURSOR POSITION
1896          4446 C      ;-----
1896          4447 C      ;-----
1896          4448 C      ;-----
1896          4449 C      ;-----
1896          4450 C      ;-----
1896          4451 C      ;-----
1896          4452 C      ;-----
1896          4453 C      ;-----
1896          4454 C      ;-----
1896          4455 C      ;-----
1896          4456 C      ;-----
1896          4457 C      ;-----
1896          4458 C      ;-----
1896          4459 C      ;-----
1896          4460 C      ;-----
1896          4461 C      ;-----
1896          4462 C      ;-----
1896          4463 C      ;-----
1896          4464 C      ;-----
1896          4465 C      ;-----
1896          4466 C      ;-----
1896          4467 C      ;-----
1896          4468 C      ;-----
1896          4469 C      ;-----
1896          4470 C      ;-----
1896          4471 C      ;-----
1896          4472 C      ;-----
1896          4473 C      ;-----
1896          4474 C      ;-----
1896          4475 C      ;-----
1896          4476 C      ;-----
1896          4477 C      ;-----
1896          4478 C      ;-----
1896          4479 C      ;-----
1896          4480 C      ;-----
1896          4481 C      ;-----
1896          4482 C      ;-----
1896          4483 C      ;-----
1896          4484 C      ;-----
1896          4485 C      ;-----
1896          4486 C      ;-----
1896          4487 C      ;-----
1896          4488 C      ;-----
1896          4489 C      ;-----
1896          4490 C      ;-----
1896          4491 C      ;-----
1896          4492 C      ;-----
1896          4493 C      ;-----
1896          4494 C      ;-----
1896          4495 C      ;-----
1896          4496 C      ;-----
1896          4497 C      ;-----
1896          4498 C      ;-----
1896          4499 C      ;-----
1896          4500 C      ;-----
1896          4501 C      ;-----
1896          4502 C      ;-----
1896          4503 C      ;-----
1896          4504 C      ;-----
1896          4505 C      ;-----
1896          4506 C      ;-----
1896          4507 C      ;-----
1896          4508 C      ;-----
1896          4509 C      ;-----
1896          4510 C      ;-----
1896          4511 C      ;-----
1896          4512 C      ;-----
1896          4513 C      ;-----
1896          4514 C      ;-----
1896          4515 C      ;-----
1896          4516 C      ;-----
1896          4517 C      ;-----
1896          4518 C      ;-----
1896          4519 C      ;-----
1896          4520 C      ;-----
1896          4521 C      ;-----
1896          4522 C      ;-----
1896          4523 C      ;-----
1896          4524 C      ;-----
1896          4525 C      ;-----
1896          4526 C      ;-----
1896          4527 C      ;-----
1896          4528 C      ;-----
1896          4529 C      ;-----
1896          4530 C      ;-----
1896          4531 C      ;-----
1896          4532 C      ;-----
1896          4533 C      ;-----
1896          4534 C      ;-----
1896          4535 C      ;-----
1896          4536 C      ;-----

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18F4 50          4537 C      PUSH    AX                ; SAVE ON STACK
18F5 51          4538 C      PUSH    CX                ; SAVE WRITE COUNT
18F6 E8 1651 R   4539 C      CALL    FIND_POSITION          ; ADDRESS TO DI
18F9 8B FB       4540 C      MOV     D1,BX              ; WRITE COUNT
18FB 59         4541 C      POP     CX                ; BL HAS CHAR TO WRITE
18FC 5B         4542 C      POP     BX
;----- WAIT FOR HORIZONTAL RETRACE
18FD 8B 16 0463 R 4544 C      MOV     DX,ADDR_6845        ; GET BASE ADDRESS
1901 83 C2 06     4545 C      ADD     DX,6              ; POINT AT STATUS PORT
1904 F6 06 0487 R 04 4546 C      TEST    INFO,4
1909 74 08        4547 C      JZ      P13A
190B EC          4548 C      IN      AL,DX             ; GET STATUS
190C A8 01        4549 C      TEST    AL,1             ; IS IT LOW
190E 75 FB        4550 C      JNZ     P12              ; WAIT UNTIL IT IS
1910 FA          4551 C      CLI                      ; NO MORE INTERRUPTS
1911 EC          4552 C      IN      AL,DX             ; GET STATUS
1912 A8 01        4553 C      TEST    AL,1             ; IS IT HIGH
1914 74 FB        4554 C      JZ      P13              ; WAIT UNTIL IT IS
1916 BA C3        4555 C      MOV     AL,BL             ; RECOVER CHAR
1918 AA          4556 C      STOSB                    ; PUT THE CHAR/ATTR
1919 FB          4557 C      STI                      ; INTERRUPTS BACK ON
191A 47          4558 C      INC     D1              ; BUMP POINTER/PAST ATTR
191B E2 E7        4559 C      LOOP    P11              ; AS REQUESTED
191D E9 219E R   4560 C      JMP     V_RET
;-----
; GRAPHICS WRITE
; THIS ROUTINE WRITES THE ASCII CHARACTER TO THE
; CURRENT POSITION ON THE SCREEN.
; ENTRY
; AL = CHARACTER TO WRITE
; BL = COLOR ATTRIBUTE TO BE USED FOR FOREGROUND COLOR
; IF BIT 7 IS SET, THE CHAR IS XOR'D INTO THE REGEN
; BUFFER (0 IS USED FOR THE BACKGROUND COLOR)
; CX = NUMBER OF CHARS TO WRITE
; DS = DATA SEGMENT
; ES = REGEN SEGMENT
; EXIT
; NOTHING IS RETURNED
; GRAPHICS READ
; THIS ROUTINE READS THE ASCII CHARACTER AT THE CURRENT
; CURSOR POSITION ON THE SCREEN BY MATCHING THE DOTS ON
; THE SCREEN TO THE CHARACTER GENERATOR CODE POINTS
; ENTRY
; NONE (0 IS ASSUMED AS THE BACKGROUND COLOR)
; EXIT
; AL = CHARACTER READ AT THAT POSITION (0 RETURNED IF
; NONE FOUND)
; FOR COMPATIBILITY ROUTINES, THE IMAGES USED TO FORM CHARS ARE
; CONTAINED IN ROM FOR THE 1ST 128 CHARS. TO ACCESS CHARS
; IN THE SECOND HALF, THE USER MUST INITIALIZE THE VECTOR AT
; INTERRUPT 1FH (LOCATION 0007CH) TO POINT TO THE USER
; SUPPLIED TABLE OF GRAPHIC IMAGES (8X8 BOXES).
; FAILURE TO DO SO WILL CAUSE IN STRANGE RESULTS
;-----
1920 80 FC 07     4600 C      ASSUME  CS:CODE,DS:ABSO,ES:NOTHING
1923 72 03        4601 C      GRAPHICS_WRITE PROC NEAR
1925 E9 19D7 R   4602 C      JMP     SI_A
1928 E8 16EB R   4603 C      JB      SI_A_WRT
1928 B4 00        4604 C      JMP     GRX_WRT
192D 50          4605 C      CALL    MK_ES
192E E8 16A4 R   4606 C      MOV     AH,0              ; 0 TO HIGH OF CODE POINT
1931 8B FB       4607 C      PUSH    AX                ; SAVE CODE POINT VALUE
;----- DETERMINE POSITION IN REGEN BUFFER TO PUT CODE POINTS
192E E8 16A4 R   4611 C      CALL    S26
1931 8B FB       4612 C      MOV     D1,AX              ; LOC IN REGEN BUFFER
;----- DETERMINE REGION TO GET CODE POINTS FROM
1933 5B          4613 C      POP     AX                ; RECOVER CODE POINT
1934 3C 80        4614 C      CMP     AL,80H           ; IS IT IN SECOND HALF
1936 73 06        4615 C      JAE     S1              ; YES
;----- IMAGE IS IN FIRST HALF, CONTAINED IN ROM
1938 C5 36 010C R 4616 C      LDS     SI,GRX_SET
193C EB 06        4617 C      JMP     SHORT S2          ; DETERMINE_MODE
;----- IMAGE IS IN SECOND HALF, IN USER RAM
193E 2C 80        4622 C      SUB     AL,80H           ; EXTEND_CHAR
1940 C5 36 007C R 4623 C      LDS     SI,EXT_PTR      ; 0 ORIGIN FOR SECOND HALF
;----- DETERMINE GRAPHICS MODE IN OPERATION
1944 D1 E0        4624 C      SAL     AX,1              ; DETERMINE_MODE
1946 D1 E0        4625 C      SAL     AX,1              ; MULTIPLY CODE POINT
1948 D1 E0        4626 C      SAL     AX,1              ; VALUE BY 8
194A 03 F0        4627 C      ADD     SI,AX              ; SI HAS OFFSET OF
194C 1E          4628 C      PUSH    DS              ; DESIRES CODES
194D EB 0CFE R   4629 C      CALL    DDS
1950 80 3E 0449 R 06 4630 C      CMP     CRT_MODE,6
1955 1F          4631 C      POP     DS
1956 72 2C        4632 C      JC      S7              ; TEST FOR MEDIUM RES MODE
;----- HIGH RESOLUTION MODE
1958 57          4633 C      PUSH    D1              ; HIGH_CHAR
1959 56          4634 C      PUSH    SI              ; SAVE REGEN POINTER
195A B6 04        4635 C      MOV     DH,4              ; SAVE CODE POINTER
195C          4636 C      MOV     SI,SI              ; NUMBER OF TIMES THROUGH
195D F6 C3 80     4637 C      TEST    BL,80H           ; LOOP
1960 75 16        4638 C      JNZ     S6              ; GET BYTE FROM CODE POINT
1962 AA          4639 C      STOSB                    ; SHOULD WE USE THE
1963 AC          4640 C      LODSB                    ; FUNCTION TO PUT CHAR IN
1964          4641 C      LODSB                    ; STORE IN REGEN BUFFER
1964 26: 8B 55 1FFF 4642 C      MOV     ES:[D1+2000H-1],AL ; STORE IN SECOND HALF
1969 83 C7 4F     4643 C      DEC     DI,79              ; MOVE TO NEXT ROW IN REGEN
196C FE CE       4644 C      JNZ     S4              ; DONE WITH LOOP
196E 75 EC       4645 C      DEC     DH
1970 5E          4646 C      POP     SI

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1971 5F 4663 C POP D1 ; RECOVER REGEN POINTER
1972 47 4664 C INC D1 ; POINT TO NEXT CHAR POS
1973 E2 E3 4665 C LOOP S3 ; MORE CHARS TO WRITE
1975 E9 219E R 4666 C JMP V_RET
1978 4667 C
1978 26: 32 05 4668 C XOR AL,ES:[D1] ; XOR WITH CURRENT
1978 AA 4669 C STOSB ; STORE THE CODE POINT
197C AC 4670 C LODSB ; AGAIN FOR ODD FIELD
197D 26: 32 85 1FFF 4671 C XOR AL,ES:[D1+2000H-1]
1982 E8 00 4672 C JMP S5 ; BACK TO MAINSTREAM
4673 C
4674 C ;----- MEDIUM RESOLUTION WRITE
4675 C
4676 C
1984 8A D3 4677 C MOV DL,BL ; MED_RES_WRITE
1986 D1 E7 4678 C SAL D1,1 ; SAVE HIGH COLOR BIT
1988 E8 166D R 4679 C CALL S19 ; OFFSET*2, 2 BYTES/CHAR
1988 57 4680 C ; EXPAND BL TO FULL WORD
1988 56 4681 C PUSH D1 ; OF COLOR
1988 B6 04 4682 C POP SI ; SAVE REGEN POINTER
198F AC 4683 C MOV DH,4 ; SAVE THE CODE POINT
1990 E8 1682 R 4684 C ; NUMBER OF LOOPS
1993 23 C3 4685 C LODSB ; GET CODE POINT
4686 C ; DOUBLE UP ALL THE BITS
4687 C ; CONVERT THEM TO FORE-
4688 C ; GROUND COLOR (0 BACK)
1995 F6 C2 80 4689 C TEST DL,80H ; IS THIS XOR FUNCTION
1998 74 07 4690 C JZ S10 ; NO, STORE IT IN AS IT IS
199A 26: 32 25 4691 C XOR AH,ES:[D1] ; DO FUNCTION WITH HALF
199D 26: 32 45 01 4692 C XOR AL,ES:[D1+1] ; AND WITH OTHER HALF
19A1 26: 88 25 4693 C ; STORE FIRST BYTE
19A1 26: 88 45 01 4694 C MOV ES:[D1+1],AL ; STORE SECOND BYTE
19A8 AC 4695 C LODSB ; GET CODE POINT
19A8 57 4696 C CALL S21 ; CONVERT TO COLOR
19A8 56 4697 C AND AX,BX ; IS THIS XOR FUNCTION
19AC 23 C3 4698 C TEST DL,80H ; NO, JUST STORE THE VALUE
19AE F6 C2 80 4699 C JZ S11 ; FUNCTION WITH FIRST HALF
19B1 74 0A 4700 C ; AND WITH SECOND HALF
19B3 26: 32 A5 2000 4701 C XOR AH,ES:[D1+2000H]
19B8 26: 32 85 2001 4702 C XOR AL,ES:[D1+2001H]
19B8 4703 C
19B8 26: 88 A5 2000 4704 C MOV ES:[D1+2000H],AH
19C2 26: 88 85 2001 4705 C MOV ES:[D1+2000H+1],AL ; STORE IN SECOND PORTION
19C7 83 C7 50 4706 C ADD DI,80 ; POINT TO NEXT LOCATION
19CA FE CE 4707 C DH ; KEEP GOING
19CC 75 C1 4708 C JNZ S9 ; RECOVER CODE POINTER
19CE 5E 4709 C POP SI ; RECOVER REGEN POINTER
19D1 5F 4710 C POP DI ; POINT TO NEXT CHAR
19D0 47 4711 C INC DI ; MORE TO WRITE
19D1 47 4712 C INC DI
19D2 E2 B7 4713 C LOOP S8
19D4 E9 219E R 4714 C JMP V_RET
19D7 4715 C ENDP
4716 C
4717 C
4718 C ;-----
4719 C ; ENTRY
4720 C ; AL = CHAR TO WRITE
4721 C ; BH = DISPLAY PAGE
4722 C ; BL = ATTRIBUTE/COLOR
4723 C ; CX = COUNT OF CHARS TO WRITE
4724 C
19D7 4725 C GRX_WRT PROC NEAR
19D7 80 FC 0F 4726 C ASSUME DS:ABSO, ES:NOTHING
19D7 72 0E 4727 C CMP AH,0FH ; 640X350 GRAPHICS
19D7 E8 14F7 R 4728 C JB NO_ADJ1 ; BASE CARD
19D7 72 09 4729 C JC NO_ADJ1
19E1 80 E3 85 4730 C AND BL,10000101B ; 85H, XOR C2 CO MASK
19E4 BA E3 4731 C MOV AH,BL
19E6 D0 E4 4732 C SHL AH,1 ; EXPAND CO TO C1, C2 TO C3
19E8 0A DC 4733 C OR BL,AH ; BUILD ?(80H) + {0,3,C,F}
19EA 4734 C
19EA 2A E4 4735 C NO_ADJ1: SUB AH,AH ; ZERO
19EC F7 26 0485 R 4736 C MUL POINTS ; OFFSET FONT TABLE BASE
19F0 50 4737 C PUSH AX ; FONT TABLE DISPLACEMENT
19F1 E8 16BA R 4738 C CALL GR_CUR ; GET OFFSET INTO REGEN
19F4 B8 F8 4739 C MOV DI,AX ; INTO DESTINATION
19F6 8B 2E 0485 R 4740 C BP,POINTS ; BYTES PER CHAR
4741 C ; REGEN SEGMENT
4742 C ; SRLOAD ES,0A000H
4743 C ; DX,0A000H
4744 C ; MOV ES,DX
4745 C ; LDS SI,GRX_SET
4746 C ; POP AX
4747 C ; ADD SI,AX
4748 C ; MOV DH,3
19EA 4749 C S20A: TEST BL,080H ; TEST FOR XOR
19EA 74 08 4750 C JZ NO_XOR ; NO XOR
19EA B2 CE 4751 C MOV DL,GRAPH_ADDR ; GRAPHICS CHIP XOR
19EA B8 0318 4752 C MOV AX,0318H ; SET REGISTER
19EA E8 0D15 R 4753 C CALL OUT_DX ; SKIP BLANK
19EA 1A15 E8 1E 90 4754 C JMP F_2 ; BLANK BOX FOR CHAR
19EA 1A18 4755 C ; SAVE REGEN POINTER
19EA 1A18 57 4756 C PUSH DI
19EA 1A19 B2 C4 4757 C MOV DL,SEQ_ADDR ; ENABLE ALL MAPS
19EA 1A1B B8 020F 4758 C MOV AX,020FH
19EA 1A1E E8 0D15 R 4759 C CALL OUT_DX ; STORE ZERO
19EA 1A21 2B C0 4760 C SUB AX,AX ; SAVE CHARACTER COUNT
19EA 1A23 51 4761 C PUSH CX ; GET BYTE COUNT
19EA 1A24 B8 CD 4762 C MOV CX,BP
19EA 1A26 1E 4763 C PUSH DS
19EA 1A27 E8 0CFE R 4764 C CALL DDS
19EA 1A2A 4765 C
19EA 1A2A AA 4766 C S13A: STOSB ; ZERO REGEN BYTE
19EA 1A2B 03 3E 044A R 4767 C ADD DI,CRT_COLS ; NEXT BYTE OF BOX
19EA 1A2F 4F 4768 C DEC DI ; ADJUST
19EA 1A32 1F 4769 C LOOP S13A ; NEXT BYTE
19EA 1A33 59 4770 C POP CX ; RECOVER CHARACTER COUNT
19EA 1A34 5F 4771 C POP DI ; RECOVER REGEN POINTER
19EA 1A35 4772 C
19EA 1A35 B2 C4 4773 C F_2: MOV DL,SEQ_ADDR
19EA 1A37 B4 02 4774 C MOV AH,02H ; SET MAP MASK
19EA 1A39 BA C3 4775 C MOV AL,BI ; FOR COLOR
19EA 1A3B E8 0D15 R 4776 C CALL OUT_DX ; SET THE CHIP
19EA 1A3E 57 4777 C PUSH DI ; SAVE OFFSET IN REGEN
19EA 1A3F 53 4778 C POP BX ; SAVE COLOR VALUE
19EA 1A40 51 4779 C PUSH CX ; SAVE CHARACTER COUNT
19EA 1A41 B8 DD 4780 C MOV BX,BP ; LOOP CONTROL, BYTES/CHAR
19EA 1A43 1E 4781 C PUSH DS ; SAVE FONT SEGMENT
19EA 1A44 E8 0CFE R 4782 C CALL DDS ; SET LOW RAM SEGMENT
19EA 1A47 B8 0E 044A R 4783 C ASSUME DS:ABSO ; GET COLUMN COUNT
19EA 1A4B 1F 4784 C MOV CX,CRT_COLS ; RESTORE FONT SEGMENT
4785 C ; POP DS
4786 C ; ASSUME DS:NOTHING
4787 C
19A4C 4788 C S1K: ; WRITE OUT THE CHARACTER

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1809      80 3E 0449 R 03      4915
1809      76 4A              4916
180E              4917
1810      A0 0466 R          4918
1813      24 DF              4919
1815      80 E3 01          4920
1818      74 02              4921
181A      0C 20              4922
181C              4923
181C      A2 0466 R          4924
181F      24 10              4925
1821      0C 02              4926
1823      0A D6              4927
1825      B4 01              4928
1827      8A C3              4929
1829      E8 109F R          4930
182C      0B ED              4931
182E      74 04              4932
1830      26: 88 5D 01        4933
1834              4934
1834              4935
1834              4936
1834              4937
1834      FE C3              4938
1836      FE C3              4939
1838      B4 02              4940
183A      8A C3              4941
183C      E8 109F R          4942
183F      0B ED              4943
1841      74 04              4944
1843      26: 88 5D 02        4945
1847              4946
1847              4947
1847      FE C3              4948
1849      FE C3              4949
184B      B4 03              4950
184D      8A C3              4951
184F      E8 109F R          4952
1852      0B ED              4953
1854      74 04              4954
1856      26: 88 5D 03        4955
185A              4956
185A      E8 10B7 R          4957
185D      E9 219E R          4958
185D              4959
185D              4960
185D              4961
185D              4962
185D              4963
185D              4964
185D              4965
185D              4966
185D              4967
185D              4968
185D              4969
185D              4970
185D              4971
185D              4972
185D              4973
185D              4974
185D              4975
185D              4976
185D              4977
185D              4978
185D              4979
185D              4980
185D              4981
185D              4982
185D              4983
185D              4984
185D              4985
185D              4986
185D              4987
185D              4988
185D              4989
185D              4990
185D              4991
185D              4992
185D              4993
185D              4994
185D              4995
185D              4996
185D              4997
185D              4998
185D              4999
185D              5000
185D              5001
185D              5002
185D              5003
185D              5004
185D              5005
185D              5006
185D              5007
185D              5008
185D              5009
185D              5010
185D              5011
185D              5012
185D              5013
185D              5014
185D              5015
185D              5016
185D              5017
185D              5018
185D              5019
185D              5020
185D              5021
185D              5022
185D              5023
185D              5024
185D              5025
185D              5026
185D              5027
185D              5028
185D              5029
185D              5030
185D              5031
185D              5032
185D              5033
185D              5034
185D              5035
185D              5036
185D              5037
185D              5038
185D              5039
185D              5040

M20:      CMP      CRT_MODE,3
          JBE      M80

          MOV      AL,CRT_PALETTE
          AND      AL,0DFH
          AND      BL,1
          JZ       M22
          OR       AL,020H

M22:      MOV      CRT_PALETTE,AL
          AND      AL,010H
          OR       AL,2
          OR       BL,AL
          MOV      AH,1
          MOV      AL,BL
          CALL     PAL_SET

          OR       BP,BP
          JZ       M22Y
          MOV      ES:[DI][1],BL

M22Y:      MOV      ES:[DI][2],BL

M27Y:      INC      BL
          INC      BL
          MOV      AH,3
          MOV      AL,BL
          CALL     PAL_SET

          OR       BP,BP
          JZ       M80
          MOV      ES:[DI][3],BL

M80:      CALL     PAL_ON
          JMP      V_RET

          INCLUDE   VDOT.INC
          SUBTTTL   VDOT.INC
          PAGE

          ENTRY
          DX = ROW
          CX = COLUMN
          BH = PAGE
          EXIT
          BX = OFFSET INTO REGEN
          AL = BIT MASK FOR COLUMN BYTE

          DOT_SUP_1 PROC NEAR
;----- OFFSET = PAGE OFFSET + ROW * BYTES/ROW + COLUMN/8
          MUL      WORD PTR CRT_COLS ; ROW * BYTES/ROW
          PUSH     CX ; SAVE COLUMN VALUE
          SHR      CX,1 ; DIVIDE BY EIGHT TO
          SHR      CX,1 ; DETERMINE THE BYTE THAT
          ; THIS DOT IS IN
          ; (8 BITS/BYTE)
          ADD      AX,CX ; BYTE OFFSET INTO PAGE
          MOV      BL,BH ; GET PAGE INTO BL
          SUB      BX,BH ; ZERO
          MOV      CX,BX ; COUNT VALUE
          MOV      BX,CRT_LEN ; LENGTH OF ONE PAGE
          JCXZ     DS_2 ; PAGE ZERO

          DS_3:    ADD      AX,BX ; BUMP TO NEXT PAGE
          LOOP     DS_3 ; DO FOR THE REST

          DS_2:    POP      CX ; RECOVER COLUMN VALUE
          MOV      BX,AX ; REGEN OFFSET
          AND      CL,07H ; SHIFT COUNT FOR BIT MASK
          MOV      AL,080H ; MASK BIT
          SHR      AL,CL ; POSITION MASK BIT
          RET

          DOT_SUP_1 ENDP

          ; THIS SUBROUTINE DETERMINES THE REGEN BYTE LOCATION
          ; OF THE INDICATED ROW COLUMN VALUE IN GRAPHICS MODE.
          ENTRY --
          DX = ROW VALUE (0-199)
          CX = COLUMN VALUE (0-639)
          EXIT --
          SI = OFFSET INTO REGEN BUFFER FOR BYTE OF INTEREST
          AH = MASK TO STRIP OFF THE BITS OF INTEREST
          CL = BITS TO SHIFT TO RIGHT JUSTIFY THE MASK IN AH
          DH = # BITS IN RESULT

          R3:      PROC NEAR
          PUSH     BX ; SAVE BX DURING OPERATION
          PUSH     AX ; WILL SAVE AL DURING OPERATION

          ;----- DETERMINE 1ST BYTE IN INDICATED ROW BY MULTIPLYING ROW VALUE BY 40
          ;----- ( LOW BIT OF ROW DETERMINES EVEN/ODD, 80 BYTES/ROW

          MOV      AL,40
          PUSH     DX ; SAVE ROW VALUE
          AND      DL,0FEH ; STRIP OFF ODD/EVEN BIT
          MUL      AX,DX ; AX HAS ADDRESS OF 1ST BYTE
          ; OF INDICATED ROW
          POP      DX ; RECOVER IT
          TEST     DL,1 ; TEST FOR EVEN/ODD
          JZ       R4 ; JUMP IF EVEN ROW
          ; OFFSET TO LOCATION OF ODD ROWS
          MOV      SI,AX ; MOVE POINTER TO SI
          POP      AX ; RECOVER AL VALUE
          MOV      DX,CX ; COLUMN VALUE TO DX

          ;----- DETERMINE GRAPHICS MODE CURRENTLY IN EFFECT

          ;----- SET UP THE REGISTERS ACCORDING TO THE MODE
          ; CH = MASK FOR LOW OF COLUMN ADDRESS ( 7/3 FOR HIGH/MED RES )

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5041 C ; CL = # OF ADDRESS BITS IN COLUMN VALUE ( 3/2 FOR H/M ) ;
5042 C ; BL = MASK TO SELECT BITS FROM POINTED BYTE ( 80H/COH FOR H/M ) ;
5043 C ; BH = NUMBER OF VALID BITS IN POINTED BYTE ( 1/2 FOR H/M ) ;
5044 C -----
5045 C
5046 C MOV BX,200H
5047 C MOV CX,302H ; SET PARMS FOR MED RES
5048 C CMP CRT_MODE,6
5049 C JC R5 ; HANDLE IF MED ARES
5050 C MOV BX,180H
5051 C MOV CX,703H ; SET PARMS FOR HIGH RES
5052 C
5053 C ;----- DETERMINE BIT OFFSET IN BYTE FROM COLUMN MASK
5054 C
5055 C R5:
5056 C AND CH,DL ; ADDRESS OF PEL WITHIN BYTE TO CH
5057 C
5058 C ;----- DETERMINE BYTE OFFSET FOR THIS LOCATION IN COLUMN
5059 C
5060 C SHR DX,CL ; SHIFT BY CORRECT AMOUNT
5061 C ADD SI,DX ; INCREMENT THE POINTER
5062 C MOV DH,BH ; GET THE # OF BITS IN RESULT TO DH
5063 C
5064 C ;----- MULTIPLY BH (VALID BITS IN BYTE) BY CH (BIT OFFSET)
5065 C
5066 C SUB CL,CL ; ZERO INTO STORAGE LOCATION
5067 C
5068 C R6:
5069 C ROR AL,1 ; LEFT JUSTIFY THE VALUE
5070 C ; IN AL (FOR WRITE)
5071 C ADD CL,CH ; ADD IN THE BIT OFFSET VALUE
5072 C DEC BH ; LOOP CONTROL
5073 C JNZ R6 ; ON EXIT, CL HAS SHIFT COUNT
5074 C ; TO RESTORE BITS
5075 C MOV AH,BL ; GET MASK TO AH
5076 C SHR AH,CL ; MOVE THE MASK TO CORRECT LOCATION
5077 C POP BX ; RECOVER REG
5078 C RET ; RETURN WITH EVERYTHING SET UP
5079 C
5080 C
5081 C ;----- READ DOT -- WRITE DOT
5082 C ; THESE ROUTINES WILL WRITE A DOT, OR READ THE DOT AT
5083 C ; THE INDICATED LOCATION
5084 C
5085 C ENTRY -
5086 C DX = ROW (0-199) (THE ACTUAL VALUE DEPENDS ON THE MODE)
5087 C CX = COLUMN (0-639) (THE VALUES ARE NOT RANGE CHECKED)
5088 C AL = DOT VALUE TO WRITE (1,2 OR 4 BITS DEPENDING ON MODE,
5089 C RIGHT FOR WRITE DOT ONLY, RIGHT JUSTIFIED)
5090 C BIT 7 OF AL=1 INDICATES XOR THE VALUE INTO THE LOCATION
5091 C DS = DATA SEGMENT
5092 C ES = REGEN SEGMENT
5093 C
5094 C EXIT
5095 C AL = DOT VALUE READ, RIGHT JUSTIFIED, READ ONLY
5096 C
5097 C ;----- WRITE DOT
5098 C
5099 C AHC:
5100 C ASSUME DS:ABSO
5101 C CMP CRT_MODE,7
5102 C JA WRITE_DOT_2
5103 C
5104 C WRITE_DOT PROC NEAR
5105 C ASSUME DS:ABSO,ES:NOTHING
5106 C PUSH DX
5107 C SRLoad ES,0800H
5108 C MOV DX,0800H
5109 C MOV ES,DX
5110 C POP DX
5111 C PUSH AX ; SAVE DOT VALUE
5112 C PUSH AX ; TWICE
5113 C CALL R3
5114 C SHR AL,CL ; DETERMINE BYTE POSITION OF THE DOT
5115 C AND AL,AH ; SHIFT TO SET UP THE BITS FOR OUTPUT
5116 C MOV CL,ES:[SI] ; STRIP OFF THE OTHER BITS
5117 C POP BX ; GET THE CURRENT BYTE
5118 C TEST BL,80H ; RECOVER XOR FLAG
5119 C JNZ R2 ; IS IT ON
5120 C NOT AH ; YES, XOR THE DOT
5121 C AND CL,AH ; SET THE MASK TO REMOVE THE
5122 C OR AL,CL ; INDICATED BITS
5123 C R1: MOV ES:[SI],AL ; OR IN THE NEW VALUE OF THOSE BITS
5124 C POP AX ; FINISH DOT
5125 C V_RET ; RESTORE THE BYTE IN MEMORY
5126 C
5127 C R2: ; XOR DOT
5128 C XOR AL,CL ; EXCLUSIVE OR THE DOTS
5129 C JMP R1 ; FINISH UP THE WRITING
5130 C
5131 C WRITE_DOT_2 PROC NEAR
5132 C CMP CRT_MODE,0FH
5133 C JB NO_ADJ2
5134 C CALL MEX_DET
5135 C JC NO_ADJ2 ; BASE CARD
5136 C AND AL,10000101B ; 85H, XOR C2 CO MASK
5137 C MOV AH,1
5138 C SHL AL,AH ; EXPAND CO TO C1, C2 TO C3
5139 C OR AL,AH ; BUILD ?(80H) * (0,3,C,F)
5140 C
5141 C NO_ADJ2:
5142 C PUSH AX
5143 C MOV AX,DX
5144 C CALL DOT_SUP_1
5145 C MOV DH,3
5146 C MOV DL,GRAPH_ADDR
5147 C MOV AH,G_BIT_MASK
5148 C CALL OUT_DX
5149 C PUSH DX
5150 C SRLoad ES,0A000H
5151 C MOV DX,0A000H ; REGEN SEGMENT
5152 C MOV ES,DX
5153 C POP DX
5154 C POP AX
5155 C MOV CH,AL
5156 C TEST DH,080H ; RECOVER COLOR
5157 C WD_A: MOV AH,NO_XOR ; SAVE COLOR
5158 C JZ NO_XOR ; SEE IF XOR
5159 C MOV AH,G_DATA_ROT ; NO XOR
5160 C CALL OUT_DX ; DO XOR
5161 C JMP WD_B ; XOR FUNCTION
5162 C ; SET THE REGISTER
5163 C MOV DL,SEQ_ADDR ; SKIP THE DOT
5164 C MOV AH,S_MAP ; BLANK THE BLANK
5165 C MOV AL,OFFH ; SEQUENCER
5166 C CALL OUT_DX ; MAP MASK
5167 C ; ENABLE ALL MAPS
5168 C ; SET THE REGISTER

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1C41 26: 8A 07      5167 C      MOV     AL,ES:[BX]      ; LATCH DATA
1C44 2A C0          5168 C      SUB     AL,AL          ; ZERO
1C46 26: 88 07      5169 C      MOV     ES:[BX],AL    ; BLANK THE DOT
1C49              5170 C      ; SET THE COLOR MAP MASK
1C49 B2 C4          5171 C      MOV     DL,SEQ_ADDR    ; SEQUENCER
1C4B B4 02          5172 C      MOV     AH,S_MAP      ; MAP MASK REGISTER
1C4D 8A C5          5173 C      MOV     AL,CR         ; COLOR VALUE
1C4F 24 0F          5174 C      AND     AL,0FFH        ; VALUES 0-15
1C51 E8 0D15 R      5175 C      CALL    OUT_DX          ; SET IT
1C54 26: 8A 07      5176 C      MOV     AL,ES:[BX]      ; LATCH DATA
1C57 B0 FF          5177 C      MOV     AL,0FFH        ; WRITE VALUE
1C59 26: 88 07      5178 C      MOV     ES:[BX],AL     ; SET THE DOT
1C59              5179 C      ;
1C59              5180 C      ;----- NORMALIZE THE ENVIRONMENT
1C59              5181 C      ;
1C5C E8 0D15 R      5182 C      CALL    OUT_DX          ; ALL MAPS ON
1C5F B2 CE          5183 C      MOV     DL,GRAPH_ADDR ; GRAPHICS CHIPS
1C61 B4 03          5184 C      MOV     AH,G_DATA_ROT  ; XOR REGISTER
1C63 2A C0          5185 C      SUB     AL,AL          ; NORMAL WRITES
1C65 E8 0D15 R      5186 C      CALL    OUT_DX          ; SET IT
1C68 B4 08          5187 C      MOV     AH,G_BIT_MASK ; BIT MASK
1C6A B0 FF          5188 C      MOV     AL,0FFH        ; ALL BITS ON
1C6C E8 0D15 R      5189 C      CALL    OUT_DX          ; SET IT
1C6F E9 219E R      5190 C      JMP     V_RET          ; WRITE DOT DONE
1C72              5191 C      WRITE_DOT_2
1C72              5192 C      ENDP
1C72              5193 C      RD_S      PROC      NEAR
1C72              5194 C      ASSUME   DS:ABS0
1C73 52              5195 C      PUSH    AX
1C73 52              5196 C      PUSH    DX
1C74 BA A000         5197 C      SRLoad  ES,0A000H
1C77 8E C2           5198 C+     MOV     DX,0A000H
1C79 5A             5199 C      MOV     ES,DX
1C7A 58             5200 C      POP     DX
1C7B 88 C2           5201 C      POP     AX
1C7D E8 1B60 R      5202 C      MOV     AX,DX
1C80 B5 07           5203 C      CALL    DOT_SUP_1
1C82 2A E9           5204 C      MOV     CH,7
1C84 2B D2           5205 C      SUB     CH,CL
1C86 B0 00           5206 C      SUB     DX,DX
1C88 C3             5207 C      MOV     AL,0
1C89              5208 C      RET
1C89              5209 C      RD_S      ENDP
1C89              5210 C      ;
1C89              5211 C      RD_1S     PROC      NEAR
1C89 8A CD           5212 C      MOV     CL,CH
1C8B B4 04           5213 C      MOV     AH,4
1C8D 52             5214 C      PUSH    DX
1C8E B6 03           5215 C      MOV     DH,3
1C90 B2 CE           5216 C      MOV     DL,GRAPH_ADDR
1C92 E8 0D15 R      5217 C      CALL    OUT_DX
1C95 5A             5218 C      POP     DX
1C96 26: 8A 27       5219 C      MOV     AH,ES:[BX]
1C99 D2 EC           5220 C      SHR     AH,CL
1C9B B0 E4 01        5221 C      AND     AH,1
1C9E C3             5222 C      RET
1C9F              5223 C      RD_1S     ENDP
1C9F              5224 C      ;
1C9F              5225 C      ;----- READ DOT
1C9F              5226 C      ;
1C9F              5227 C      AHD:
1C9F 80 3E 0449 R 07 5228 C      ASSUME   DS:ABS0
1CA4 77 18           5229 C      CMP     CRT_MODE,7
1CA4              5230 C      JA      R_1
1CA4              5231 C      ;
1CA6              5232 C      READ_DOT  PROC      NEAR
1CA6 52             5233 C      ASSUME   DS:ABS0,ES:NOTHING
1CA6 52             5234 C      PUSH    DX
1CA7 BA B800         5235 C      SRLoad  ES,0B800H
1CAA 8E C2           5236 C+     MOV     DX,0B800H
1CAC 5A             5237 C+     MOV     ES,DX
1CAD E8 1B88 R      5238 C      POP     DX
1CB0 26: 8A 04       5239 C      CALL    R3
1CB3 22 C4           5240 C      MOV     AL,ES:[SI]
1CB5 D2 E0           5241 C      AND     AL,AH
1CB7 8A CE           5242 C      SHL     AL,CL
1CB9 D2 C0           5243 C      CLD
1CBB E9 219E R      5244 C      ROL     AL,CL
1CBE              5245 C      JMP     V_RET
1CBE              5246 C      READ_DOT  ENDP
1CBE              5247 C      ;
1CBE              5248 C      R_1:
1CBE 80 3E 0449 R OF 5249 C      CMP     CRT_MODE,0FH
1CC3 72 25           5250 C      JB      READ_DOT_2
1CC5 E8 14F7 R      5251 C      CALL    MEM_DET
1CC8 72 20           5252 C      JC      READ_DOT_2
1CCA              5253 C      ;
1CCA              5254 C      READ_DOT_1 PROC      NEAR
1CCA E8 1C72 R      5255 C      ASSUME   DS:ABS0,ES:NOTHING ; 2 MAPS
1CCD E8 1C89 R      5256 C      CALL    RD_S
1CCD E8 1C89 R      5257 C      CALL    RD_1S
1CCD 0A D4           5258 C      OR      DL,AH
1CCD 0A D4           5259 C      OR      AH,1
1CCD 0A D4           5260 C      OR      DL,AH
1CCD B0 02           5261 C      MOV     AL,2
1CCD E8 1C89 R      5262 C      CALL    RD_1S
1CCD B0 02           5263 C      SHL     AH,1
1CCD D0 E4           5264 C      SHL     AH,1
1CCD D0 E4           5265 C      OR      DL,AH
1CCD D0 E4           5266 C      OR      DL,AH
1CCD D0 E4           5267 C      OR      AL,DL
1CCD D0 E4           5268 C      MOV     AL,DL
1CCF E9 219E R      5269 C      JMP     V_RET
1CEA              5270 C      READ_DOT_1 ENDP
1CEA              5271 C      ;
1CEA              5272 C      READ_DOT_2 PROC      NEAR ; 4 MAPS
1CEA E8 1C72 R      5273 C      ASSUME   DS:ABS0,ES:NOTHING
1CED              5274 C      CALL    RD_S
1CED              5275 C      RD_2A:
1CED E8 1C89 R      5276 C      CALL    RD_1S
1CF0 8A C8           5277 C      MOV     CL,AL
1CF2 D2 E4           5278 C      SHL     AH,CL
1CF4 0A D4           5279 C      OR      DL,AH
1CF6 FE C0           5280 C      INC     AL
1CF8 3C 03           5281 C      CMP     AL,3
1CFA 76 F1           5282 C      JBE     RD_2A
1CFC 8A C2           5283 C      MOV     AL,DL
1CFE E9 219E R      5284 C      JMP     V_RET
1CEA              5285 C      READ_DOT_2 ENDP
1CEA              5286 C      ;
1CEA              5287 C      ;----- WRITE TTY
1CEA              5288 C      WRITE_TTY WRITE TELETYPE TO ACTIVE PAGE
1CEA              5289 C      ;
1CEA              5290 C      ; THIS INTERFACE PROVIDES A TELETYPE LIKE INTERFACE TO THE VIDEO :
1CEA              5291 C      ; CARD. THE INPUT CHARACTER IS WRITTEN TO THE CURRENT CURSOR :
1CEA              5292 C      ; POSITION, AND THE CURSOR IS MOVED TO THE NEXT POSITION. IF THE :
1CEA              5293 C      ; CURSOR LEAVES THE LAST COLUMN OF THE FIELD, THE COLUMN IS SET :

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5293 C ; TO ZERO, AND THE ROW VALUE IS INCREMENTED. IF THE ROW VALUE :
5294 C ; LEAVES THE FIELD, THE CURSOR IS PLACED ON THE LAST ROW, FIRST :
5295 C ; COLUMN, AND THE ENTIRE SCREEN IS SCROLLED UP ONE LINE. WHEN :
5296 C ; THE SCREEN IS SCROLLED UP, THE ATTRIBUTE FOR FILLING THE NEWLY :
5297 C ; BLANKED LINE IS READ FROM THE CURSOR POSITION ON THE PREVIOUS :
5298 C ; LINE BEFORE THE SCROLL, IN CHARACTER MODE. IN GRAPHICS MODE, :
5299 C ; THE 0 COLOR IS USED.
5300 C ; ENTRY
5301 C ; (AH) = CURRENT CRT MODE
5302 C ; (AL) = CHARACTER TO BE WRITTEN
5303 C ; NOTE THAT BACK SPACE, CAR RET, BELL AND LINE FEED ARE HANDLED :
5304 C ; AS COMMANDS RATHER THAN AS DISPLAYABLE GRAPHICS
5305 C ; (BL) = FOREGROUND COLOR FOR CHAR WRITE IF CURRENTLY IN A :
5306 C ; GRAPHICS MODE
5307 C ; EXIT
5308 C ; ALL REGISTERS SAVED
5309 C ; -----
5310 C ;
5311 C ; AH:
5312 C ; ASSUME CS:CODE,DS:ABSO
5313 C ; PUSH AX
5314 C ; MOV BH,ACTIVE_PAGE ; SAVE REGISTERS
5315 C ; PUSH BX ; GET THE ACTIVE PAGE
5316 C ; MOV BL,BX ; SAVE
5317 C ; XOR BH,BH ; GET PAGE TO BL
5318 C ; SAL BX,1 ; CLEAR HIGH BYTE
5319 C ; POP BX ; *2 FOR WORD OFFSET
5320 C ; ; CURSOR, ACTIVE PAGE
5321 C ; ; RECOVER
5322 C ;
5323 C ; ; DX NOW HAS THE CURRENT CURSOR POSITION
5324 C ;
5325 C ; CMP AL,0DH ; IS IT CARRIAGE RETURN
5326 C ; JE UP ; CAR RET
5327 C ; CMP AL,0AH ; IS IT A LINE FEED
5328 C ; JE U10 ; LINE FEED
5329 C ; CMP AL,08H ; IS IT A BACKSPACE
5330 C ; JE UB ; BACK SPACE
5331 C ; CMP AL,07H ; IS IT A BELL
5332 C ; JE U11 ; BELL
5333 C ;
5334 C ; ; WRITE THE CHAR TO THE SCREEN
5335 C ;
5336 C ; MOV AH,10 ; WRITE CHAR ONLY
5337 C ; MOV CX,1 ; ONLY ONE CHAR
5338 C ; INT 10H ; WRITE THE CHAR
5339 C ;
5340 C ; ; POSITION THE CURSOR FOR NEXT CHAR
5341 C ;
5342 C ; INC DL ;
5343 C ; CMP DL,BYTE PTR CRT_COLS ; TEST FOR COLUMN OVERFLOW
5344 C ; JNZ U7 ; SET CURSOR
5345 C ; SUB DL,DL ; COLUMN FOR CURSOR
5346 C ; CMP DH,ROWS ;
5347 C ; JNZ U6 ; SET_CURSOR_INC
5348 C ;
5349 C ; ; SCROLL REQUIRED
5350 C ;
5351 C ; U1: CALL SET_CPOS ; SET THE CURSOR
5352 C ;
5353 C ; ; DETERMINE VALUE TO FILL WITH DURING SCROLL
5354 C ;
5355 C ; MOV AL,CRT_MODE ; GET THE CURRENT MODE
5356 C ; CMP AL,4 ;
5357 C ; JB U2 ; READ-CURSOR
5358 C ; SUB BH,BH ; FILL WITH BACKGROUND
5359 C ; CMP AL,7 ;
5360 C ; JNE U3 ; SCROLL-UP
5361 C ; ; READ-CURSOR
5362 C ; U2: MOV AH,8 ;
5363 C ; INT 10H ; READ CHAR/ATTR
5364 C ; MOV BH,AH ; STORE IN BH
5365 C ; U3: MOV AX,601H ; SCROLL-UP
5366 C ; SUB CX,CX ; SCROLL ONE LINE
5367 C ; MOV DH,ROWS ; UPPER LEFT CORNER
5368 C ; DL,BYTE PTR CRT_COLS ; LOWER RIGHT ROW
5369 C ; DEC DL ; LOWER RIGHT COLUMN
5370 C ;
5371 C ; U4: ; VIDEO-CALL-RETURN
5372 C ; INT 10H ; SCROLL UP THE SCREEN
5373 C ; U5: POP AX ; TTY-RETURN
5374 C ; JMP V_RET ; RESTORE THE CHARACTER
5375 C ; U6: INC DH ; RETURN TO CALLER
5376 C ; INC DH ; SET-CURSOR-INC
5377 C ; U7: MOV AH,2 ; NEXT ROW
5378 C ; JMP U4 ; SET-CURSOR
5379 C ;
5380 C ; ; ESTABLISH THE NEW CURSOR
5381 C ;
5382 C ; ; BACK SPACE FOUND
5383 C ;
5384 C ; U8: OR DL,DL ; ALREADY AT END OF LINE
5385 C ; JZ U7 ; SET CURSOR
5386 C ; DEC DL ; NO -- JUST MOVE IT BACK
5387 C ; JMP U7 ; SET_CURSOR
5388 C ;
5389 C ; ; CARRIAGE RETURN FOUND
5390 C ;
5391 C ; U9: SUB DL,DL ; MOVE TO FIRST COLUMN
5392 C ; JMP U7 ; SET_CURSOR
5393 C ;
5394 C ; ; LINE FEED FOUND
5395 C ;
5396 C ; U10: CMP DH,ROWS ; BOTTOM OF SCREEN
5397 C ; JNE U6 ; YES, SCROLL THE SCREEN
5398 C ; JMP U1 ; NO, JUST SET THE CURSOR
5399 C ;
5400 C ; ; BELL FOUND
5401 C ;
5402 C ; U11: ;
5403 C ; MOV BL,2 ; SET UP COUNT FOR BEEP
5404 C ; BEEP ; SOUND THE POD BELL
5405 C ; JMP U5 ; TTY_RETURN
5406 C ;
5407 C ; ; CURRENT VIDEO STATE
5408 C ;
5409 C ;
5410 C ;
5411 C ;
5412 C ; AH:
5413 C ; ASSUME DS:ABSO
5414 C ; MOV AH,BYTE PTR CRT_COLS ; GET NUMBER OF COLUMNS
5415 C ; MOV BH,ACTIVE_PAGE ;
5416 C ; MOV AL,INFO ;
5417 C ; AND AL,080H ;
5418 C ; OR AL,CRT_MODE ;

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1096 5F          5419 C      POP    DI
1097 5E          5420 C      POP    SI
1098 59          5421 C      POP    CX
1099 59          5422 C      POP    CX
109A 5A          5423 C      POP    DX
109B 1F          5424 C      POP    DS
109C 07          5425 C      POP    ES
109D 5D          5426 C      POP    BP
109E CF          5427 C      IRET
                    5428 C
                    5429 C      SUBTTL
                    5430
                    5431
109F            5432 PAL_SET PROC    NEAR
109F 50          5433         PUSH  AX
10A0 E8 0D05 R  5434         CALL  WHAT_BASE
10A3 FA          5435         CLI
10A4            5436
10A4 EC          5437 VR:      IN      AL,DX
10A5 A8 08      5438         TEST  AL,08H
10A7 74 FB      5439         JZ     VR
10A9 58          5440         POP    AX
10AA 82 C4      5441         MOV    DL,ATTR_WRITE
10AC 86 C4      5442         XCHG   AL,AH
10AE EE          5443         OUT    DX,AL
10AF 86 C4      5444         XCHG   AL,AH
10B1 EE          5445         OUT    DX,AL
10B2 80 20      5446         MOV    AL,020H
10B4 EE          5447         OUT    DX,AL
10B5 FB          5448         STI
10B6 C3          5449         RET
10B7            5450 PAL_SET ENDP
                    5451
10B7            5452 PAL_ON  PROC    NEAR
10B8 E8 1DC0 R  5453         CALL  PAL_INIT
10BA B2 C0      5454         MOV    DL,ATTR_WRITE
10BC 80 20      5455         MOV    AL,020H
10BE EE          5456         OUT    DX,AL
10BF C3          5457         RET
10C0            5458 PAL_ON  ENDP
                    5459
10C0            5460 PAL_INIT PROC    NEAR
10C0 E8 0D05 R  5461         CALL  WHAT_BASE
10C3 EC          5462         IN      AL,DX
10C4 C3          5463         RET
10C5            5464 PAL_INIT ENDP
                    5465
                    5466 ;----- SET PALETTE REGISTERS
                    5467
AH10:            5468
                    5469 ASSUME  DS:ABSO
10C5 F6 06 04B7 R 02 5470 TEST    INFO,2
10C6 75 07          5471 JNZ     BM_OK
                    5472
                    5473 ;----- HERE THE EGA IS IN A COLOR MODE
                    5474
                    5475 CMP     BYTE PTR ADDR_6845,0B4H
10CC 80 3E 0463 R B4 5476 JE      BM_OUT
10D1 74 33          5477
10D3            5478 BM_OUT: MOV    AH,AL
10D5 0A E4          5479         OR     AH,AH
10D7 75 30          5480         JNZ     BM_1
                    5481
                    5482 ;----- SET INDIVIDUAL REGISTER
                    5483
10D9 2B ED          5484         SUB     BP,BP
10DB 04 3E 0A48 R  5485         LES     DI,SAVE_PTR
10DD 83 C7 04      5486         ADD     DI,4
10DE 26: C4 3D      5487         LES     DI,DWORD PTR ES:[DI]
10E5 8C C0          5488         MOV    AX,ES
10E7 0B C7          5489         OR     AX,DI
10E9 74 01          5490         JZ     TLO_1
10EB 45            5491         INC     BP
10EC            5492 TLO_1: INC     BP
                    5493
10EC E8 1DC0 R      5494         CALL  PAL_INIT
10EF 8A E3          5495         MOV    AH,BL
10F1 8A C7          5496         MOV    AL,BH
10F3 E8 1D9F R      5497         CALL  PAL_SET
10F6 E8 1D87 R      5498         CALL  PAL_ON
10F9 0B ED          5499         OR     BP,BP
10FB 74 09          5500         JZ     BM_OUT
10FD 8A C7          5501         MOV    AL,BH
10FF 2A FF          5502         SUB     BH,BH
1E01 03 FB          5503         ADD     DI,BX
1E03 26: 88 05      5504         MOV    ES:[DI],AL
1E06            5505 BM_OUT: JMP     V_RET
                    5506
1E09            5507 BM_1:
1E09 FE CC          5508         DEC     AH
1E0B 75 2D          5509         JNZ     BM_2
                    5510
                    5511
1E0D 2B ED          5512         SUB     BP,BP
1E0F 04 3E 0A48 R  5513         LES     DI,SAVE_PTR
1E13 83 C7 04      5514         ADD     DI,4
1E16 26: C4 3D      5515         LES     DI,DWORD PTR ES:[DI]
1E19 8C C0          5516         MOV    AX,ES
1E1B 0B C7          5517         OR     AX,DI
1E1D 74 01          5518         JZ     TLO_2
1E1F 45            5519         INC     BP
1E20            5520 TLO_2: INC     BP
                    5521
                    5522 ;----- SET OVERSCAN REGISTER
                    5523
1E20 E8 1DC0 R      5524         CALL  PAL_INIT
1E23 B4 11          5525         MOV    AH,011H
1E25 8A C7          5526         MOV    AL,BH
1E27 E8 1D9F R      5527         CALL  PAL_SET
1E2A E8 1D87 R      5528         CALL  PAL_ON
                    5529
1E2D 0B ED          5530         OR     BP,BP
1E2F 74 05          5531         JZ     BM_OUT
1E31 83 C7 11      5532         ADD     DI,011H
1E34 26: 88 3D      5533         MOV    ES:[DI],BH
                    5534
1E37 E9 219E R      5535         JMP     V_RET
                    5536
1E3A            5537 BM_2:
1E3A FE CC          5538         DEC     AH
1E3C 75 4D          5539         JNZ     BM_3
                    5540
                    5541 ;----- SET 16 PALETTE REGISTERS AND OVERSCAN REGISTER
                    5542
1E3E 1E          5543         PUSH   DS
1E3F 06          5544         PUSH   DS

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1E40 C6 3E 04A8 R      5545      LES      D1,SAVE_PTR
1E44 83 C7 04          5546      ADD      D1,4
1E47 26: C4 3D        5548      LES      D1,DWORD PTR ES:[D1]      ; ES:D1 PTR TO PAL SAVE AREA
1E4A 8C C0            5549      MOV      AX,ES
1E4C 0B C7            5550      OR       AX,D1
1E4E 74 09            5551      JZ       TL0_3
                        5552
1E50 1F              5553      POP      DS
                        5554      ; PARAMETER ES
1E51 1E              5555      PUSH     DS
1E52 8B F2            5555      MOV      SI,DX
                        5556      ; PARAMETER OFFSET
1E54 B9 0011          5556      MOV      CX,17D
1E57 F3/ AH          5557      MOVSB
                        5558
1E59              5559      TL0_3:
1E5A 1F              5560      POP      ES
                        5561      DS
                        5562
1E5B 8B DA            5563      MOV      BX,DX
1E5D E8 1DC0 R        5564      CALL    PAL_INIT
1E60 2A E4            5565      SUB      AH,AH
                        5566
1E62              5567      BM_2A:
1E62 26: 8A 07        5567      MOV      AL,ES:[BX]
1E65 E8 1D9F R        5568      CALL    PAL_SET
1E68 FE C4            5569      INC      AH
                        5570      INC      BX
1E6B 80 FC 10         5571      CMP      AH,010H
1E6E 72 F2            5572      JB       BM_2A
1E70 FE C4            5573      INC      AH
1E72 26: 8A 07        5574      MOV      AL,ES:[BX]
1E75 E8 1D9F R        5575      CALL    PAL_SET
1E78 E8 1D87 R        5576      CALL    PAL_ON
1E7B E9 219E R        5577      JMP      V_RET
                        5578
1E7E              5579      BM_3:
1E7E FE CC            5580      DEC      AH
1E80 75 29            5581      JNZ      BM_4
                        5582
                        5583      ;----- TOGGLE INTENSIFY/BLINKING BIT
                        5584
1E82 53              5585      PUSH     BX
1E83 E8 0D5A R        5586      CALL    MAKE_BASE
1E86 83 C3 33         5587      ADD      BX,010H + LN_4
1E89 26: 5A 07        5588      MOV      AL,ES:[BX]
1E8C 5B              5589      POP      BX
                        5590
1E8D 0A DB            5591      OR       BL,BL
1E8F 75 0A            5592      JNZ      BM_6
                        5593
                        5594      ;----- ENABLE INTENSIFY
                        5595
1E91 80 26 0465 R DF  5596      AND      CRT_MODE_SET,11011111B
1E96 24 F7            5597      AND      AL,07FH
1E98 E8 0C 90         5598      JMP      BM_7
1E9B              5599
1E9E FE CB            5600      DEC      BL
1E9D 75 07            5601      JNZ      BM_7
                        5602
                        5603      ;----- ENABLE BLINK
                        5604
1E9F 80 0E 0465 R 20  5605      OR       CRT_MODE_SET,020H
1EA4 0C 08            5606      OR       AL,08H
1EA6              5607      BM_7:
1EA6 B4 10            5608      MOV      AH,P_MODE
1EA8 E8 1D9F R        5609      CALL    PAL_SET
1EAB              5610      BM_4:
1EAB E9 219E R        5611      JMP      V_RET
                        5612
1E13 C                5613      C      INCLUDE      VCHGEN,INC
1E14 C                5614      C      SUBTTL      VCHGEN,INC
1E15 C                5615      C      PAGE
1E16 C                5616      C      ;-----
1E17 C                5617      C      ; ENTRY
1E18 C                5618      C      ; AL = 0 USER SPECIFIED FONT
1E19 C                5619      C      ; 1 8 X 14 FONT
1E20 C                5620      C      ; 2 8 X 8 DOUBLE DOT
1E21 C                5621      C      ; BL = BLOCK TO LOAD
1E22 C                5622      C      ;-----
1E23 C                5623      C      CH_GEN:
1E24 C                5624      C      ;
1E25 C                5625      C      ; PUSH     AX
1E26 C                5626      C      ; PUSH     BP
1E27 C                5627      C      ; PUSH     CX
1E28 C                5628      C      ; PUSH     DX
1E29 C                5629      C      ; PUSH     ES
1E30 C                5630      C      ;
1E31 C                5631      C      ; ASSUME DS:ABS0
1E32 C                5632      C      ; CALL     DDS
1E33 C                5633      C      ; MOV      AL,CRT_MODE
1E34 C                5634      C      ; PUSH     AX
1E35 C                5635      C      ; CMP      AL,7
1E36 C                5636      C      ; JE       H14
1E37 C                5637      C      ; MOV      CRT_MODE,0BH
1E38 C                5638      C      ; JMP      SHORT H15
1E39 C                5639      C      ;
1E40 C                5640      C      ; H14:
1E41 C                5641      C      ; MOV      CRT_MODE,0CH
1E42 C                5642      C      ;
1E43 C                5643      C      ; H15:
1E44 C                5644      C      ; CALL     SET_REGS
1E45 C                5645      C      ; CALL     DDS
1E46 C                5646      C      ; POP      AX
1E47 C                5647      C      ; MOV      CRT_MODE,AL
1E48 C                5648      C      ;
1E49 C                5649      C      ; POP      ES
1E50 C                5650      C      ; POP      DX
1E51 C                5651      C      ; POP      CX
1E52 C                5652      C      ; POP      BX
1E53 C                5653      C      ; POP      BP
1E54 C                5654      C      ; POP      AX
1E55 C                5655      C      ;
1E56 C                5656      C      ; OR       AL,AL
1E57 C                5657      C      ; JZ       DO_MAP2
1E58 C                5658      C      ; PUSH     CS
1E59 C                5659      C      ; POP      ES
1E60 C                5660      C      ; SUB      DX,DX
1E61 C                5661      C      ; MOV      CX,0256D
1E62 C                5662      C      ; DEC      AL
1E63 C                5663      C      ; JNZ      H7
1E64 C                5664      C      ; MOV      BH,014D
1E65 C                5665      C      ; MOV      BP,OFFSET CGDM
1E66 C                5666      C      ; JMP      SHORT DO_MAP2
1E67 C                5667      C      ;
1E68 C                5668      C      ; H7:
1E69 C                5669      C      ; MOV      BH,8
1E70 C                5670      C      ; MOV      BP,OFFSET CGDDOT
1E71 C                5671      C      ;
1E72 C                5672      C      ; ;-----
1E73 C                5673      C      ; ; ALPHA CHARACTER GENERATOR LOAD
1E74 C                5674      C      ;

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5671 C ;
5672 C ; ENTRY
5673 C ES:BP - POINTER TO TABLE
5674 C CX - COUNT OF CHARS
5675 C DX - CHAR COUNT OFFSET INTO MAP 2
5676 C BH - BYTES PER CHARACTER
5677 C BL - MAP 2 BLOCK TO LOAD
5678 C -----
1EF6 DO_MAP2:
1EF6 06 5680 PUSH ES
1EF7 1F 5681 POP DS
1EF8 52 5682 PUSH DX
5683 SRLoad ES,0A000H
5684 MOV DX,0A000H
5685 MOV ES,DX
5686 POP DX
5687 PUSH CX
5688 MOV CL,5
5689 SHL DX,CL
5690 POP CX
5691 OR BL,BL
5692 JZ H3
H4:
5693 ADD DX,04000H
5694 DEC BL
5695 JNZ H4
H3:
5696 MOV AL,BH
5697 SUB AH,AH
5698 MOV DI,DX
5700 MOV SI,BP
5701 JCXZ LD_OVER
LD:
5702 PUSH CX
5703 MOV CX,AX
5704 REP MOVSB
5705 SUB DI,AX
5706 ADD DI,020H
5707 POP CX
5708 LOOP LD
LD_OVER:
5709 RET
5710
5711 BRK_1:
5712 ASSUME DS:ABS0
5713 CALL DDS
5714 MOV POINTS,AX
5715 DX,ADDR 6845
5716 CMP CRT_MODE,7
5717 JNE H11A
5718 MOV AH,C_UNDERLN_LOC
5719 CALL OUT_DX
H11A:
5720 DEC AL
5721 MOV AH,C_MAX_SCAN_LN
5722 CALL OUT_DX
5723 DEC AL
5724 MOV CH,AL
5725 MOV CL,AL
5726 INC CL
5727 MOV AH,1
5728 INT 10H
5729 MOV BL,CRT_MODE
5730 MOV AX,350D
5731 BL CMP
5732 JA H11
5733 CALL BRST_DET
5734 H11:
5735 MOV AX,200D
5736 CWD
5737 DIV POINTS
5738 DEC AX
5739 MOV ROWS,AL
5740 INC AL
5741 SUB AH,AH
5742 MUL POINTS
5743 DEC AX
5744 MOV DX,ADDR_6845
5745 MOV AH,C_VRT_DSP_END
5746 CALL OUT_DX
5747 MUL AL,ROWS
5748 INC AL
5749 BYTE PTR CRT_COLS
5750 SHL AX,1
5751 ADD AX,256D
5752 MOV CRT_LEN,AX
5753 CALL PH_5
5754 JMP V_RET
5755
5756 ;----- LOADABLE CHARACTER GENERATOR ROUTINES
5757
5758 AH11:
5759 CMP AL,010H
5760 JAE AH11_ALPHA1
5761
5762 ;----- ALPHA MODE ACTIVITY HERE
5763
5764 CMP AL,03H
5765 JAE H1
5766 CALL CH_GEN
5767 CALL SET_REGS
5768 CALL PH_5
5769 ASSUME DS:ABS0
5770 CALL DDS
5771 MOV CX,CURSOR_MODE
5772 MOV AH,1
5773 INT 10H
5774 JMP V_RET
5775
5776 ;----- SET THE CHARACTER GENERATOR BLOCK SELECT REGISTER
5777
5778 H1:
5779 JNE H2
5780 MOV DH,3
5781 MOV DL,SEQ_ADDR
5782
5783 MOV AX,1
5784 CALL OUT_DX
5785
5786 MOV AH,S_CGEN
5787 MOV AL,BL
5788 CALL OUT_DX
5789
5790 ; FONT TABLE SEGMENT
5791 ; ADDRESSING TO TABLE
5792 ; SAVE REGISTER
5793 ; ADDRESSING TO MAP 2
5794
5795 ; RECOVER REGISTER
5796 ; MULTIPLY BY 020H SINCE
5797 ; MAXIMUM BYTES PER
5798 ; CHARACTER IS 32D=020H
5799 ; RECOVER
5800 ; WHICH 16K BLOCK TO LOAD
5801 ; BLOCK ZERO
5802
5803 ; INCREMENT TO NEXT BLOCK
5804 ; ANY MORE
5805 ; DO ANOTHER
5806
5807 ; BYTES PER CHARACTER
5808 ; ZERO
5809 ; OFFSET INTO MAP
5810 ; OFFSET INTO TABLE
5811 ; CHARACTER COUNT
5812
5813 ; SAVE CHARACTER COUNT
5814 ; ONE ENTIRE CHARACTER
5815 ; AT A TIME
5816 ; ADJUST OFFSET
5817 ; NEXT CHARACTER POSITION
5818 ; RECOVER CHARACTER COUNT
5819 ; DO THE REST
5820
5821 ; SET LOW MEMORY SEGMENT
5822 ; GET BYTES/CHARACTER
5823 ; CRTC REGISTER
5824
5825 ; R10H
5826 ; SET THE UNDERLINE LOC
5827
5828 ; POINTS - 1
5829 ; R09H
5830 ; SET THE CHARACTER HEIGHT
5831 ; POINTS - 2
5832
5833 ; CURSOR START
5834 ; CURSOR END
5835 ; ADJUST END
5836 ; SET C_TYPE BIOS CALL
5837 ; SET THE CURSOR
5838
5839 ; GET THE CURRENT MODE
5840 ; MAX SCANS ON SCREEN
5841 ; 680X200 ALPHA MODES
5842 ; MUST BE 350
5843
5844 ; SET FOR 200
5845
5846 ; PREPARE TO DIVIDE
5847 ; MAX ROWS ON SCREEN
5848 ; ADJUST
5849 ; SAVE ROWS
5850 ; READJUST
5851 ; CLEAR
5852 ; ROWS*BYTES/CHAR
5853 ; ADJUST
5854 ; CRTC ADDRESS
5855 ; SCANS DISPLAYED
5856 ; SET IT
5857 ; GET CHARACTER ROWS
5858 ; ADJUST
5859 ; ROWS*COLUMNS
5860 ; *2 FOR ALPHA MODE
5861 ; SPACE BETWEEN PAGES
5862 ; BYTES PER PAGE
5863 ; VIDEO ON
5864 ; RETURN TO CALLER
5865
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208A	0A FF	5923	C	OR	BH,BH	
208C	75 07	5924	C	F9	JNZ	BP,EXT_PTR
208E	C4 2E 007C R	5925	C	LES	BP,EXT_PTR	
2092	EB 1A 90	5926	C	JMP	INFORM_OUT	
2095		5927	C			
2099	C4 2E 010C R	5928	C	F9:	LES	BP,GRX_SET
2099	EB 13 90	5929	C	JMP	INFORM_OUT	
		5930	C			
		5931	C			
		5932	C			
209C		5933	C			
		5934	C	F7:	ASSUME	DS:ABSO
209C	80 EF 02	5935	C	SUB	BH,2	
209F	8A DF	5936	C	MOV	BL,BH	
20A1	2A FF	5937	C	SUB	BH,BH	
20A3	D1 E3	5938	C	SAL	BL,1	
20A5	81 C3 20B7 R	5939	C	ADD	BX,OFFSET_TBL_5	
20A9	2E: 8B 2F	5940	C	MOV	BP,CS:[BX]	
20AC	0E	5941	C	PUSH	CS	
20AD	07	5942	C	POP	ES	
		5943	C			
20AE		5944	C	INFORM_OUT:		
20AE	5F	5945	C	POP	DI	
20AF	5E	5946	C	POP	SI	
20B0	5B	5947	C	POP	BX	
20B1	58	5948	C	POP	AX	; DISCARD SAVED CX
20B2	58	5949	C	POP	AX	; DISCARD SAVED DX
20B3	1F	5950	C	POP	DS	
20B4	58	5951	C	POP	AX	; DISCARD SAVED ES
20B5	58	5952	C	POP	AX	; DISCARD SAVED BP
20B6	CF	5953	C	IRET		
		5954	C			
		5955	C			
		5956	C			
		5957	C			
20B7	0000 E	5958	C	TBL_5	LABEL	WORD
20B9	0000 E	5959	C	DW	OFFSET CGMN	
20BB	0000 E	5960	C	DW	OFFSET CGDOT	
20BD	0000 E	5961	C	DW	OFFSET INT_1F_1	
		5962	C	DW	OFFSET CGMN_FDG	
		5963	C			
		5964	C			
		5965	C			
		5966	C			
		5967	C			
20BF		5968	C	AH12:	ASSUME	DS:ABSO
20BF	80 FB 10	5969	C	CMF	BL,010H	; RETURN ACTIVE CALL
20C2	72 51	5970	C	JB	ACT_1	
20C4	74 1B	5971	C	JE	ACT_3	
20C6	80 FB 20	5972	C	CMF	BL,020H	; ALTERNATE PRINT SCREEN
20C9	74 03	5973	C	JE	ACT_2	
20CB	E9 219E R	5974	C	JMP	V_RET	; INVALID CALL
20CE		5975	C			; NEW PRINT SCREEN
20CE	2B 02	5976	C	ACT_2:	SRLOAD	DS,0
20D0	8E DA	5977	C	SUB	DX,DX	
20D2	FA	5978	C	MOV	DX,DX	
20D3	C7 06 0014 R 21A7 R	5979	C	CLI		
20D9	8C 0E 0016 R	5980	C	MOV	WORD PTR INT5_PTR, OFFSET PRINT_SCREEN	
20DD	FB	5981	C	MOV	WORD PTR INT5_PTR+2, CS	
20DE	E9 219E R	5982	C	STI		
20E1		5983	C	JMP	V_RET	
20E1	8A 3E 0487 R	5984	C	ACT_3:		
20E5	80 E7 02	5985	C	MOV	BH,INFO	; LOOKING FOR MONOC BIT
20E8	D0 EF	5986	C	AND	BH,2	; ISOLATE
		5987	C	SHR	BH,1	; ADJUST
		5988	C			
20EA	A0 0487 R	5989	C	MOV	AL,INFO	; LOOKING FOR MEMORY
20ED	24 60	5990	C	AND	AL,01100000B	; MEMORY BITS
20EF	B1 05	5991	C	MOV	CL,5	; SHIFT COUNT
20F1	D2 E8	5992	C	SHR	AL,CL	; ADJUST MEM VALUE
20F3	8A D8	5993	C	MOV	BL,AL	; RETURN REGISTER
		5994	C			
20F5	8A 0E 0488 R	5995	C	MOV	CL,INFO_3	; FEATURE/SWITCH
20F9	8A E9	5996	C	MOV	CH,CL	; DUPLICATE IN CH
20FB	80 E1 0F	5997	C	AND	CL,0FH	; MASK OFF SWITCH VALUE
20FE	D0 ED	5998	C	SHR	CH,1	; MOVE FEATURE VALUE
2100	D0 ED	5999	C	SHR	CH,1	
2102	D0 ED	6000	C	SHR	CH,1	
2104	D0 ED	6001	C	SHR	CH,1	
2106	80 E5 0F	6002	C	AND	CH,0FH	; MASK IT
		6003	C			
2109	5F	6004	C	POP	DI	
210A	5E	6005	C	POP	SI	
210B	5A	6006	C	POP	DX	
210C	5A	6007	C	POP	DX	; DISCARD BX
210D	5A	6008	C	POP	DX	; DISCARD CX
210E	1F	6009	C	POP	DS	
210F	07	6010	C	POP	ES	
2110	5D	6011	C	POP	BP	
2111	CF	6012	C	IRET		
2112		6013	C			
2112	E9 219E R	6014	C	AH12_X:	JMP	V_RET
2115		6015	C			
2115		6016	C	ACT_1:		
2115	E9 219E R	6017	C	STR_OUTZ:	JMP	V_RET
		6018	C			; RETURN TO CALLER
		6019	C			
		6020	C			
2118		6021	C			
2118	3C 04	6022	C	AH13:	CMF	AL,0H
211A	73 F9	6023	C	JAE	STR_OUTZ	; RANGE CHECK
211C	E3 F7	6024	C	JCXZ	STR_OUTZ	; INVALID PARAMETER
211E	53	6025	C	BX	PUSH	
211F	8A DF	6026	C	MOV	BL,BH	
2121	2A FF	6027	C	SUB	BH,BH	
2123	D1 E3	6028	C	SAL	BL,1	
2125	8B B7 0450 R	6029	C	MOV	SI,[BX + OFFSET CURSOR_POSN]	
2129	5B	6030	C	POP	BX	
212A	56	6031	C	PUSH	SI	
		6032	C			
212B	50	6033	C			
212C	8B 0200	6034	C	PUSH	AX,0200H	
212F	CD 10	6035	C	INT	10H	; SET THE CURSOR POSITION
2131	58	6036	C	POP	AX	
2132		6037	C			
2132	51	6038	C	STR_1:	PUSH	CX
2133	53	6039	C	PUSH	BX	
2134	50	6040	C	PUSH	AX	
2135	86 E0	6041	C	XCHG	AH,AL	
2137	26: 8A 46 00	6042	C	MOV	AL,ES:[BP]	; GET THE CHAR TO WRITE
2138	45	6043	C	INC	BP	
213C	3C 0D	6044	C	CMF	AL,0DH	; CARRIAGE RETURN
213E	74 3D	6045	C	JE	STR_CR_LF	
2140	3C 0A	6046	C	CMF	AL,0AH	; LINE FEED
2142	74 39	6047	C	JE	STR_CR_LF	
2144	3C 08	6048	C	CMF	AL,08H	; BACKSPACE

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2146 74 35 6049 JE STR_CR_LF
2148 3C 07 6050 JE STR_CR_LF ; BELL
214A 74 11 6051 MOV MOV CX,1 ; COUNT OF CHARACTERS
214C 89 0001 6052 CMP AH,2 ; CHECK WHERE ATTR IS
214F 80 FC 02 6053 JB DO_STR ; NOT IN THE STRING
2152 72 05 6054 MOV BL,ES:[BP] ; GET THE ATTRIBUTE
2154 26: 8A 5E 00 6055 INC BP ; NEXT ITEM IN STRING
2158 45 6056
2159 6057
2159 B4 09 6058 MOV AH,09H ; WRITE THE CHAR/ATTR
215B CD 10 6059 INT 10H
215D FE C2 6060 INC DL ; NEXT CURSOR POSITION
215F 3A 16 044A R 6061 CMP DL,BYTE PTR CRT_COLS ; COLUMN OVERFLOW
2163 72 11 6062 JNB STR_2 ; NOT YET
2165 3A 36 0484 R 6063 CMP DH,ROWS
2169 75 07 6064 INC STR_3
216B 88 0E0A 6065 MOV AX,0E0AH
216E CD 10 6066 INT 10H
2170 FE CE 6067 DEC DH
2172 6068
2172 FE C6 6069 INC DH ; NEXT ROW
2174 2A D2 6070 SUB DL,DL ; COLUMN ZERO
2176 6071
2176 B8 0200 6072 MOV AX,0200H ; SET THE CURSOR
2179 CD 10 6073 INT 10H
217B EB 0E 6074 JMP SHORT STR_4
217D 6075
217D B4 0E 6076 MOV AH,0EH
217F CD 10 6077 INT 10H
2181 8A DF 6078 MOV BL,BH ; GET PAGE TO LOW BYTE
2183 2A FF 6079 SUB BH,BH ;
2185 D1 E3 6080 SAL BX,1 ; *2 FOR WORD OFFSET
2187 8B 97 0450 R 6081 MOV DX,[BX + OFFSET CURSOR_POSM] ; GET CURSOR POSITION
2188 6082
2188 58 6083 POP AX
218C 58 6084 POP BX
218D 59 6085 POP CX
218E E2 A2 6086 LOOP STR_1
2190 5A 6087
2190 6088 POP DX ; RECOVER CURSOR POSITION
2191 3C 01 6089 ; FROM PUSH SI ABOVE
2193 74 09 6090 CMP AL,1
2195 3C 03 6091 JE STR_OUT
2197 74 05 6092 AL,3
2199 B8 0200 6093 JE STR_OUT ; SET CURSOR POSITION
219C CD 10 6094 MOV AX,0200H
219E 6095 INT 10H
219E 6096 STR_OUT:
219E 6097 ; ALLOW FALL THROUGH
219E 6098
219E 6099
219E 5F 6100 V_RET PROC NEAR ; VIDEO BIOS RETURN
219F 5E 6101 POP DI
21A0 58 6102 POP SI
21A1 59 6103 POP BX
21A2 5A 6104 POP CX
21A3 1F 6105 POP DX
21A4 07 6106 POP DS
21A5 5D 6107 POP ES
21A6 CF 6108 POP BP
21A7 6109
21A7 6110 V_RET ENDP
21A7 6111
21A7 6112 COMBO_VIDEO ENDP
21A7 6113
21A7 6114 C INCLUDE VPRSC.INC
21A7 6115 C SUBTTL VPRSC.INC
21A7 6116 C PAGE
21A7 6117 C
21A7 6118 C -----
21A7 6119 C INTERRUPT 5
21A7 6120 C THIS LOGIC WILL BE INVOKED BY INTERRUPT 05H TO PRINT THE
21A7 6121 C SCREEN. THE CURSOR POSITION AT THE TIME THIS ROUTINE IS INVOKED
21A7 6122 C WILL BE SAVED AND RESTORED UPON COMPLETION. THE ROUTINE IS
21A7 6123 C INTENDED TO RUN WITH INTERRUPTS ENABLED. IF A SUBSEQUENT
21A7 6124 C 'PRINT SCREEN' KEY IS DEPRESSED DURING THE TIME THIS ROUTINE
21A7 6125 C IS PRINTING IT WILL BE IGNORED.
21A7 6126 C ADDRESS 50:0 CONTAINS THE STATUS OF THE PRINT SCREEN:
21A7 6127 C
21A7 6128 C 50:0 =0 EITHER PRINT SCREEN HAS NOT BEEN CALLED
21A7 6129 C OR UPON RETURN FROM A CALL THIS INDICATES
21A7 6130 C A SUCCESSFUL OPERATION.
21A7 6131 C =1 PRINT SCREEN IS IN PROGRESS
21A7 6132 C =255 ERROR ENCOUNTERED DURING PRINTING
21A7 6133 C -----
21A7 6134 C ASSUME CS:CODE,DS:ABS0
21A7 6135 C PRINT_SCREEN PROC FAR
21A7 6136 C STI DS ; MUST RUN WITH INTS ENABLED
21A7 6137 C PUSH DS ; MUST USE 50:0 FOR DATA
21A7 6138 C PUSH AX ; AREA STORAGE
21A7 6139 C PUSH BX
21A7 6140 C PUSH CX
21A7 6141 C PUSH DX
21A7 6142 C CALL DDS
21A7 6143 C CMP STATUS_BYTE,1 ; SEE IF PRINT ALREADY IN PROGRESS
21A7 6144 C JZ EXIT ; JUMP IF PRINT IN PROGRESS
21A7 6145 C MOV STATUS_BYTE,1 ; INDICATE PRINT NOW IN PROGRESS
21A7 6146 C MOV AH,15 ; WILL REQUEST THE CURRENT MODE
21A7 6147 C INT 10H ; [AL]=MODE (NOT USED)
21A7 6148 C ; [AH]=NUMBER COLUMNS/LINE
21A7 6149 C ; [BH]=VISUAL PAGE
21A7 6150 C -----
21A7 6151 C AT THIS POINT WE KNOW THE COLUMNS/LINE ARE IN
21A7 6152 C [AX] AND THE PAGE IF APPLICABLE IS IN [BH]. THE STACK
21A7 6153 C HAS DS:AX,BX,CX,DX PUSHED. [AL] HAS VIDEO MODE
21A7 6154 C -----
21A7 6155 C MOV CL,AH ; WILL MAKE USE OF [CX] REG TO
21A7 6156 C MOV CH,ROWS ; CONTROL ROW & COLUMNS
21A7 6157 C INC CH ; ADJUST
21A7 6158 C CALL CRLF ; CAR RETURN LINE FEED ROUTINE
21A7 6159 C PUSH CX ; SAVE SCREEN BOUNDS
21A7 6160 C MOV AH,3 ; WILL NOW READ THE CURSOR.
21A7 6161 C INT 10H ; AND PRESERVE THE POSITION
21A7 6162 C POP CX ; RECALL SCREEN BOUNDS
21A7 6163 C PUSH DX ; RECALL [BH]=VISUAL PAGE
21A7 6164 C XOR DX,DX ; SET CURSOR POSITION TO [0,0]
21A7 6165 C -----
21A7 6166 C THE LOOP FROM PR110 TO THE INSTRUCTION PRIOR TO PR120
21A7 6167 C IS THE LOOP TO READ EACH CURSOR POSITION FROM THE
21A7 6168 C SCREEN AND PRINT.
21A7 6169 C -----
21A7 6170 C PR110:
21A7 6171 C MOV AH,2 ; TO INDICATE CURSOR SET REQUEST
21A7 6172 C INT 10H ; NEW CURSOR POS ESTABLISHED
21A7 6173 C MOV AH,8 ; TO INDICATE READ CHARACTER
21A7 6174 C INT 10H ; CHARACTER NOW IN [AL]
21A7 6175 C OR AL,AL ; SEE IF VALID CHAR

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21DE 75 02      6175  C C      JNZ      PRI15      ; JUMP IF VALID CHAR
21ED 80 20      6176  C C      MOV      AL, 1      ; MAKE A BLANK
21E2          6177  C C      PR115:
21E2 52          6178  C C      PUSH     DX      ; SAVE CURSOR POSITION
21E3 33 D2      6179  C C      XOR      DX,DX    ; INDICATE PRINTER 1
21E3 32 E6      6180  C C      XOR      AH,AH    ; TO INDICATE PRINT CHAR IN [AL]
21E7 CD 17      6181  C C      INT      17H    ; PRINT THE CHARACTER
21E9 5A         6182  C C      POP      DX      ; RECALL CURSOR POSITION
21EA F6 C4 29   6183  C C      TEST     AH,029H  ; TEST FOR PRINTER ERROR
21ED 75 21      6184  C C      JNZ      ERR10   ; JUMP IF ERROR DETECTED
21EF FE C2      6185  C C      INC      DL      ; ADVANCE TO NEXT COLUMN
21F1 3A CA      6186  C C      CMP      CL,DL    ; SEE IF AT END OF LINE
21F3 75 DF      6187  C C      JNZ      1817   ; IF NOT PROCEED
21F5 32 D2      6188  C C      XOR      DL,DL    ; BACK TO COLUMN 0
21F7 BA E2      6189  C C      MOV      AH,DL    ; [AH]=0
21F9 52         6190  C C      PUSH     DX      ; SAVE NEW CURSOR POSITION
21FA EB 2220 R  6191  C C      CALL     CRLF    ; LINE FEED CARRIAGE RETURN
21FD 5A         6192  C C      POP      DX      ; RECALL CURSOR POSITION
21FE FE C6      6193  C C      INC      DH      ; ADVANCE TO NEXT LINE
2200 3A EE      6194  C C      CMP      CH,DH    ; FINISHED?
2202 75 D0      6195  C C      JNZ      PR110   ; IF NOT CONTINUE
                6196  C C
220A 5A         6197  C C      POP      DX      ; RECALL CURSOR POSITION
2205 B4 02      6198  C C      MOV      AH,2     ; TO INDICATE CURSOR SET REQUEST
2207 CD 10      6199  C C      INT      10H     ; CURSOR POSITION RESTORED
2209 C6 06 0500 R 00 6200  C C      MOV      STATUS,BYTE,0 ; INDICATE FINISHED
220E EB 0A      6201  C C      JMP      SHORT EXIT ; EXIT THE ROUTINE
                6202  C C
2210 5A         6203  C C      POP      DX      ; GET CURSOR POSITION
2211 B4 02      6204  C C      MOV      AH,2     ; TO REQUEST CURSOR SET
2213 CD 10      6205  C C      INT      10H     ; CURSOR POSITION RESTORED
2215 C6 06 0500 R FF 6206  C C      MOV      STATUS,BYTE,0FFH ; INDICATE ERROR
221A          6207  C C
221A 5A         6208  C C      EXIT:
221B 59         6209  C C      POP      DX      ; RESTORE ALL THE REGISTERS USED
221C 5B         6210  C C      POP      CX
221D 58         6211  C C      POP      BX
221E 1F         6212  C C      POP      AX
221F CF         6213  C C      POP      DS
                6214  C C
                6215  C C      PRINT_SCREEN      ENDP
                6216  C C
                6217  C C      ;----- CARRIAGE RETURN, LINE FEED SUBROUTINE
2220          6218  C C
2220 33 D2      6219  C C      CRLF      PROC      NEAR
2222 32 E4      6220  C C      XOR      DX,DX    ; PRINTER 0
                6221  C C      XOR      AH,AH    ; WILL NOW SEND INITIAL CR, LF
                6222  C C
                6223  C C      MOV      AL,0DH   ; CR
2226 80 0D      6224  C C      INT      17H     ; SEND THE LINE FEED
2228 CD 17      6225  C C      XOR      AH,AH    ; NOW FOR THE CR
222A 80 0A      6226  C C      MOV      AL,0AH   ; LF
222C CD 17      6227  C C      INT      17H     ; SEND THE CARRIAGE RETURN
222E C3         6228  C C
222F          6229  C C      CRLF      ENDP
                6230  C C
                6231  C C      SUBTTL
222F          6232  C C      CODE      ENDS
                6233  C C      END

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1
2
3
CODE SUBTTL PAGE,120 MONOCHROME CHARACTER GENERATOR
4 SEGMENT PUBLIC
5 PUBLIC
6 CGMN LABEL BYTE
7
8 DB 000H,000H,000H,000H,000H,000H,000H,000H ; BW 8*14 PATTERN
9
10 DB 000H,000H,000H,000H,000H,000H,000H,000H ; TOP_HALF 00
11
12 DB 000H,000H,07EH,081H,0A5H,081H,081H,0BDH ; TH_01
13
14 DB 099H,081H,07EH,000H,000H,000H,000H,000H ; BT_01
15 DB 000H,000H,07EH,0FFH,0DBH,0FFH,0FFH,0C3H ; TH_02
16
17 DB 0E7H,0FFH,07EH,000H,000H,000H,000H,000H ; BT_02
18 DB 000H,000H,000H,06CH,0FEH,0FEH,0FEH,0FEH ; TH_03
19
20 DB 07CH,038H,010H,000H,000H,000H,000H,000H ; BT_03
21 DB 000H,000H,000H,010H,038H,07CH,0FEH,07CH ; TH_04
22
23 DB 038H,010H,000H,000H,000H,000H,000H,000H ; BT_04
24 DB 000H,000H,018H,03CH,03CH,0E7H,0E7H,0E7H ; TH_05
25
26 DB 018H,018H,03CH,000H,000H,000H,000H,000H ; BT_05
27 DB 000H,000H,018H,03CH,07EH,0FFH,0FFH,07EH ; TH_06
28
29 DB 018H,018H,03CH,000H,000H,000H,000H,000H ; BT_06
30 DB 000H,000H,000H,000H,000H,018H,03CH,03CH ; TH_07
31
32 DB 018H,000H,000H,000H,000H,000H,000H,000H ; BT_07
33 DB 0FFH,0FFH,0FFH,0FFH,0FFH,0E7H,0C3H,0C3H ; TH_08
34
35 DB 0E7H,0FFH,0FFH,0FFH,0FFH,0FFH,0FFH,0FFH ; BT_08
36 DB 000H,000H,000H,000H,03CH,066H,042H,042H ; TH_09
37
38 DB 066H,03CH,000H,000H,000H,000H,000H,000H ; BT_09
39 DB 0FFH,0FFH,0FFH,0FFH,0C3H,099H,0BDH,0BDH ; TH_0A
40
41 DB 099H,0C3H,0FFH,0FFH,0FFH,0FFH,0FFH,0FFH ; BT_0A
42 DB 000H,000H,01EH,0E0H,01AH,032H,078H,0CCH ; TH_0B
43
44 DB 0CCH,0CCH,078H,000H,000H,000H,000H,000H ; BT_0B
45 DB 000H,000H,03CH,066H,066H,066H,03CH,018H ; TH_0C
46
47 DB 07EH,018H,018H,000H,000H,000H,000H,000H ; BT_0C
48 DB 000H,000H,03FH,033H,03FH,030H,030H,030H ; TH_0D
49
50 DB 070H,0F0H,0E0H,000H,000H,000H,000H,000H ; BT_0D
51 DB 000H,000H,07FH,063H,07FH,063H,063H,063H ; TH_0E
52
53 DB 067H,0E7H,066H,0C0H,000H,000H,000H,000H ; BT_0E
54 DB 000H,000H,018H,018H,0DBH,03CH,0E7H,03CH ; TH_0F
55
56 DB 0DBH,018H,018H,000H,000H,000H,000H,000H ; BT_0F
57
58 DB 000H,000H,080H,0C0H,0E0H,0F8H,0FEH,0F8H ; TH_10
59
60 DB 0E0H,0C0H,080H,000H,000H,000H,000H,000H ; BT_10
61 DB 000H,000H,002H,066H,00EH,03EH,0FEH,03EH ; TH_11
62
63 DB 00EH,066H,002H,000H,000H,000H,000H,000H ; BT_11
64 DB 000H,000H,018H,03CH,07EH,018H,018H,018H ; TH_12
65
66 DB 07EH,03CH,018H,000H,000H,000H,000H,000H ; BT_12
67 DB 000H,000H,066H,066H,066H,066H,066H,066H ; TH_13

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0112	66 66	66
0118	00 00 66 00 00 00	67
	00 00 7F 0B 0B 0B	68
	7B 1B	69
0120	1B 1B 1B 00 00 00	70
0126	00 7C 6C 60 3B 6C	71
	C6 C6	72
012E	6C 3B 0C C6 7C 00	73
0134	00 00 00 00 00 00	74
	00 00	75
013C	FE FE FE 00 00 00	76
0142	00 00 1B 3C 7E 1B	77
	1B 1B	78
0144	7E 3C 1B 7E 00 00	79
0150	00 00 1B 3C 7E 1B	80
	1B 1B	81
0158	1B 1B 1B 00 00 00	82
015E	00 00 1B 1B 1B 1B	83
	1B 1B	84
0166	7E 3C 1B 00 00 00	85
	FE 0C	86
0174	1B 00 00 00 00 00	87
017A	00 00 00 00 30 60	88
	FE 60	89
0182	30 00 00 00 00 00	90
0188	00 00 00 00 00 00	91
	00 00	92
0190	FE 00 00 00 00 00	93
0196	00 00 00 00 2B 6C	94
	FE 6C	95
019E	2B 00 00 00 00 00	96
01A4	00 00 00 10 3B 3B	97
	7C 7C	98
01AC	FE FE 00 00 00 00	99
01B2	00 00 00 FE 7C 7C	100
	7C 3B	101
01BA	3B 10 00 00 00 00	102
		103
01C0	00 00 00 00 00 00	104
	00 00	105
01C8	00 00 00 00 00 00	106
01CE	00 00 1B 3C 3C 3C	107
	1B 1B	108
01D6	00 1B 1B 00 00 00	109
01DC	00 66 66 66 24 00	110
	00 00	111
01E4	00 00 00 00 00 00	112
01EA	00 00 6C 6C FE 6C	113
	6C 6C	114
01F2	FE 6C 6C 00 00 00	115
01F8	1B 7C 7C 00 00 00	116
	7C 0B	117
0200	86 6C 7C 1B 1B 00	118
0206	00 00 00 00 C6 C6	119
	0C 1B	120
020E	30 66 6C 00 00 00	121
0214	00 00 3B 6C 6C 3B	122
	7C 6C	123
021C	CC C6 7C 00 00 00	124
0222	00 30 30 30 60 00	125
	00 00	126
022A	00 00 00 00 00 00	127
0230	00 00 0C 1B 30 30	128
	30 30	129
0238	30 1B 0C 00 00 00	130
023E	00 00 30 1B 0C 0C	131
	0C 0C	132
0246	0C 1B 30 00 00 00	133
024C	00 00 00 00 66 3C	134
	FF 3C	135
0254	66 00 00 00 00 00	136
025A	00 00 00 00 1B 1B	137
	7E 1B	138
0262	1B 00 00 00 00 00	139
0268	00 00 00 00 00 00	140
	00 00	141
0270	1B 1B 1B 30 00 00	142
0276	00 00 00 00 00 00	143
	FE 00	144
027E	00 00 00 00 00 00	145
0284	00 00 00 00 00 00	146
	00 00	147
028C	00 1B 1B 00 00 00	148
0292	00 00 02 06 0C 1B	149
	30 60	150
029A	C0 80 00 00 00 00	151
		152
02A0	00 00 7C 6C C6 DE	153
	F6 E6	154
02A6	C6 C6 7C 00 00 00	155
	1B 1B	156
02B6	1B 1B 7E 00 00 00	157
02BC	00 00 7C 6C 06 0C	158
	1B 30	159
02C4	60 C6 FE 00 00 00	160
02CA	00 00 7C 6C 06 06	161
	3C 06	162
02D2	06 C6 7C 00 00 00	163
02D8	00 00 0C 1C 3C 6C	164
	CC FE	165
02E0	0C 0C 1E 00 00 00	166
02E6	00 00 FE C0 C0 C0	167
	FC 06	168
02F2	06 C6 7C 00 00 00	169
02F4	00 00 1B 60 C0 C0	170
	FC C6	171
02FC	C6 C6 7C 00 00 00	172
0302	00 00 FE C6 06 0C	173
	1B 30	174
030A	30 30 30 00 00 00	175
0310	00 00 7C 6C C6 C6	176
	7C C6	177
0318	C6 C6 7C 00 00 00	178
031E	00 00 7C 6C C6 C6	179
	7E 06	180
0326	06 0C 7B 00 00 00	181
032C	00 00 00 1B 1B 00	182
	00 00	183
0334	1B 1B 00 00 00 00	184
033A	00 00 00 1B 1B 00	185
	00 00	186
0342	1B 1B 30 00 00 00	187
0348	00 00 06 0C 1B 30	188
	60 30	189
		190
		191
DB	000H,066H,066H,000H,000H,000H	BT_13
DB	000H,000H,07FH,0DBH,0DBH,0DBH,07BH,01BH	TH_14
DB	01BH,01BH,01BH,000H,000H,000H	BT_14
DB	000H,07CH,0C6H,060H,03BH,06CH,0C6H,0C6H	TH_15
DB	06CH,03BH,00CH,0C6H,07CH,000H	BT_15
DB	000H,000H,000H,000H,000H,000H,000H,000H	TH_16
DB	0FEH,0FEH,0FEH,000H,000H,000H	BT_16
DB	000H,000H,01BH,03CH,07EH,01BH,01BH,01BH	TH_17
DB	07EH,03CH,01BH,07EH,000H,000H	BT_17
DB	000H,000H,01BH,03CH,07EH,01BH,01BH,01BH	TH_18
DB	01BH,01BH,01BH,000H,000H,000H	BT_18
DB	000H,000H,01BH,01BH,01BH,01BH,01BH,01BH	TH_19
DB	07EH,03CH,01BH,000H,000H,000H	BT_19
DB	000H,000H,000H,000H,01BH,00CH,0FEH,00CH	TH_1A
DB	01BH,000H,000H,000H,000H,000H	BT_1A
DB	000H,000H,000H,000H,030H,060H,0FEH,060H	TH_1B
DB	030H,000H,000H,000H,000H,000H	BT_1B
DB	000H,000H,000H,000H,000H,0C0H,0C0H,0C0H	TH_1C
DB	0FEH,000H,000H,000H,000H,000H	BT_1C
DB	000H,000H,000H,000H,02BH,06CH,0FEH,06CH	TH_1D
DB	02BH,000H,000H,000H,000H,000H	BT_1D
DB	000H,000H,000H,010H,03BH,03BH,07CH,07CH	TH_1E
DB	0FEH,0FEH,000H,000H,000H,000H	BT_1E
DB	000H,000H,000H,0FEH,0FEH,07CH,07CH,03BH	TH_1F
DB	03BH,010H,000H,000H,000H,000H	BT_1F
DB	000H,000H,000H,000H,000H,000H,000H,000H	TH_20 SP
DB	000H,000H,000H,000H,000H,000H	BT_20 SP
DB	000H,000H,01BH,03CH,03CH,03CH,01BH,01BH	TH_21 !
DB	000H,01BH,01BH,000H,000H,000H	BT_21 !
DB	000H,066H,066H,066H,02BH,000H,000H,000H	TH_22 "
DB	000H,000H,000H,000H,000H,000H	BT_22 "
DB	000H,000H,06CH,06CH,0FEH,06CH,06CH,06CH	TH_23 #
DB	0FEH,06CH,06CH,000H,000H,000H	BT_23 #
DB	01BH,01BH,07CH,0C6H,0C2H,0C0H,07CH,060H	TH_24 \$
DB	086H,0C6H,07CH,01BH,01BH,000H	BT_24 \$
DB	000H,000H,000H,000H,0C2H,0C6H,00CH,01BH	TH_25 %
DB	030H,066H,0C6H,000H,000H,000H	BT_25 %
DB	000H,000H,03BH,06CH,06CH,03BH,076H,00CH	TH_26 &
DB	0CCH,0CCH,076H,000H,000H,000H	BT_26 &
DB	000H,030H,030H,030H,060H,000H,000H,000H	TH_27 *
DB	000H,000H,000H,000H,000H,000H	BT_27 *
DB	000H,000H,00CH,01BH,030H,030H,030H,030H	TH_28 (
DB	030H,01BH,00CH,000H,000H,000H	BT_28 (
DB	000H,000H,030H,01BH,00CH,00CH,00CH,00CH	TH_29 )
DB	00CH,01BH,030H,000H,000H,000H	BT_29 )
DB	000H,000H,000H,000H,066H,03CH,0FFH,03CH	TH_2A *
DB	066H,000H,000H,000H,000H,000H	BT_2A *
DB	000H,000H,000H,000H,01BH,01BH,07EH,01BH	TH_2B +
DB	01BH,000H,000H,000H,000H,000H	BT_2B +
DB	000H,000H,000H,000H,000H,000H,000H,000H	TH_2C ,
DB	01BH,01BH,01BH,030H,000H,000H	BT_2C ,
DB	000H,000H,000H,000H,000H,0FEH,000H	TH_2D -
DB	000H,000H,000H,000H,000H,000H	BT_2D -
DB	000H,000H,000H,000H,000H,000H,000H,000H	TH_2E .
DB	000H,01BH,01BH,000H,000H,000H	BT_2E .
DB	000H,000H,002H,066H,00CH,01BH,030H,060H	TH_2F /
DB	0C0H,080H,000H,000H,000H,000H	BT_2F /
DB	000H,000H,07CH,0C6H,0CEH,0DEH,0F6H,0E6H	TH_30 0
DB	0C6H,0C6H,07CH,000H,000H,000H	BT_30 0
DB	000H,000H,01BH,03BH,07BH,01BH,01BH,01BH	TH_31 1
DB	01BH,01BH,07EH,000H,000H,000H	BT_31 1
DB	000H,000H,07CH,0C6H,066H,00CH,01BH,030H	TH_32 2
DB	060H,0C6H,0FEH,000H,000H,000H	BT_32 2
DB	000H,000H,07CH,0C6H,066H,066H,03CH,066H	TH_33 3
DB	066H,0C6H,07CH,000H,000H,000H	BT_33 3
DB	000H,000H,00CH,01CH,03CH,06CH,0CCH,0FEH	TH_34 4
DB	00CH,00CH,01EH,000H,000H,000H	BT_34 4
DB	000H,000H,0FEH,0C0H,0C0H,0C0H,0FCH,066H	TH_35 5
DB	066H,0C6H,07CH,000H,000H,000H	BT_35 5
DB	000H,000H,03BH,066H,0C0H,0C0H,0FCH,0C6H	TH_36 6
DB	0C6H,0C6H,07CH,000H,000H,000H	BT_36 6
DB	000H,000H,0FEH,0C6H,066H,00CH,01BH,030H	TH_37 7
DB	030H,030H,030H,000H,000H,000H	BT_37 7
DB	000H,000H,07CH,0C6H,0C6H,0C6H,07CH,0C6H	TH_38 8
DB	0C6H,0C6H,07CH,000H,000H,000H	BT_38 8
DB	000H,000H,07CH,0C6H,0C6H,0C6H,07EH,066H	TH_39 9
DB	066H,00CH,07BH,000H,000H,000H	BT_39 9
DB	000H,000H,000H,01BH,01BH,000H,000H,000H	TH_3A :
DB	01BH,01BH,000H,000H,000H,000H	BT_3A :
DB	000H,000H,000H,01BH,01BH,000H,000H,000H	TH_3B :
DB	01BH,01BH,030H,000H,000H,000H	BT_3B :
DB	000H,000H,066H,00CH,01BH,030H,060H,030H	TH_3C <

0350	18	0C	06	00	00	00	192	DB	018H,00CH,006H,000H,000H,000H	: BT_3C <
0356	00	00	00	00	00	7E	193	DB	000H,000H,000H,000H,000H,07EH,000H,000H	: TH_3D =
	00	00					194			: BT_3D =
035E	7E	00	00	00	00	00	195	DB	07EH,000H,000H,000H,000H,000H	: TH_3E >
0364	00	00	60	30	18	0C	196	DB	000H,006H,006H,030H,018H,00CH,006H,00CH	: BT_3E >
	0C	00					197			: TH_3F >
036C	18	30	60	00	00	00	198	DB	018H,030H,006H,000H,000H,000H	: BT_3F >
0372	18	00	7C	6C	C6	0C	199	DB	000H,000H,07CH,0C6H,0C6H,0C6H,018H,018H	: TH_3F >
	00	18	18	00	00	00	200			: BT_3F ?
037A	00	18	18	00	00	00	201	DB	000H,018H,018H,000H,000H,000H	: TH_80 #
0380	00	00	7C	6C	C6	DE	202	DB	000H,000H,07CH,0C6H,0C6H,0DEH,0DEH	: BT_A0 #
	DE	DE					203			: TH_A1 A
0388	00	00	7C	00	00	00	204	DB	00CH,0C6H,07CH,000H,000H,000H	: BT_A1 A
038E	00	00	10	38	6C	C6	205	DB	000H,000H,010H,038H,0C6H,0C6H,0FEH	: BT_A2 B
	C6	FE					206			: TH_A3 C
0396	C6	C6	C6	00	00	00	207	DB	0C6H,0C6H,0C6H,000H,000H,000H	: BT_A3 C
039C	00	00	FC	66	66	66	208	DB	000H,000H,07CH,0C6H,0C6H,0C6H,07CH,066H	: BT_A4 D
	7C	66					209			: BT_A5 E
03A4	66	66	FC	00	00	00	210	DB	066H,066H,0FCH,000H,000H,000H	: TH_A6 F
03AA	00	00	3C	66	C2	C0	211	DB	000H,000H,03CH,066H,0C2H,0C0H,0C0H,0C0H	: BT_A7 G
	C0	C0					212			: TH_A8 H
03B2	C2	66	3C	00	00	00	213	DB	0C2H,066H,03CH,000H,000H,000H	: BT_A9 I
03B8	00	00	F8	6C	66	66	214	DB	000H,000H,0F8H,06CH,066H,066H,066H,066H	: TH_A9 J
	66	66					215			: BT_A4 D
03C0	66	6C	F8	00	00	00	216	DB	066H,06CH,0F8H,000H,000H,000H	: BT_A5 E
03C6	00	00	FC	66	62	68	217	DB	000H,000H,0FEH,066H,0C2H,068H,078H,068H	: BT_A6 F
	78	68					218			: TH_A7 G
03CE	62	66	FE	00	00	00	219	DB	062H,066H,0FEH,000H,000H,000H	: BT_A8 H
03D4	00	00	FC	66	62	68	220	DB	000H,000H,0FEH,066H,0C2H,068H,078H,068H	: TH_A9 I
	78	68					221			: BT_A6 F
03DC	60	60	F0	00	00	00	222	DB	060H,060H,0F0H,000H,000H,000H	: TH_A7 G
03E2	00	00	3C	66	C2	C0	223	DB	000H,000H,03CH,066H,0C2H,0C0H,0C0H,0C0H	: BT_A8 H
	C0	DE					224			: TH_A9 I
03EA	C6	66	3A	00	00	00	225	DB	0C6H,066H,03AH,000H,000H,000H	: BT_A4 D
03F0	00	00	C6	C6	C6	C6	226	DB	000H,000H,0C6H,0C6H,0C6H,0C6H,0FEH,0C6H	: BT_A5 E
	FE	C6					227			: TH_A6 F
03F8	C6	C6	C6	00	00	00	228	DB	0C6H,0C6H,0C6H,000H,000H,000H	: BT_A7 G
03FE	00	00	3C	18	18	18	229	DB	000H,000H,03CH,018H,018H,018H,018H,018H	: TH_A8 H
	18	18					230			: TH_A9 I
0406	18	18	3C	00	00	00	231	DB	018H,018H,03CH,000H,000H,000H	: BT_A4 D
040C	00	00	1E	0C	0C	0C	232	DB	000H,000H,01EH,00CH,00CH,00CH,00CH,00CH	: BT_A5 E
	0C	0C					233			: TH_AA J
0414	CC	CC	78	00	00	00	234	DB	0CCH,0CCH,078H,000H,000H,000H	: TH_AB K
041A	00	00	E6	66	6C	6C	235	DB	000H,000H,066H,066H,06CH,06CH,078H,06CH	: BT_AB K
	78	6C					236			: TH_AC L
0422	6C	66	E6	00	00	00	237	DB	06CH,066H,0E6H,000H,000H,000H	: BT_AD M
0428	00	00	F0	60	60	60	238	DB	000H,000H,0F0H,060H,060H,060H,060H,060H	: TH_AD M
	60	60					239			: BT_AE N
0430	62	66	FE	00	00	00	240	DB	062H,066H,0FEH,000H,000H,000H	: TH_AF O
0436	00	00	CE	EE	FE	FE	241	DB	000H,000H,0C6H,0E6H,0FEH,0FEH,0D6H,0C6H	: BT_A7 G
	D6	C6					242			: TH_AB K
043E	C6	C6	C6	00	00	00	243	DB	0C6H,0C6H,0C6H,000H,000H,000H	: BT_A8 H
0444	00	00	CE	E6	FE	FE	244	DB	000H,000H,0C6H,0E6H,0FEH,0FEH,0DEH,0CEH	: TH_AB K
	DE	CE					245			: BT_AE N
044C	C6	C6	C6	00	00	00	246	DB	0C6H,0C6H,0C6H,000H,000H,000H	: TH_AF O
0452	00	00	38	6C	C6	C6	247	DB	000H,000H,038H,06CH,0C6H,0C6H,0C6H,0C6H	: BT_A7 G
	C6	C6					248			: TH_AB K
045A	C6	6C	38	00	00	00	249	DB	0C6H,06CH,038H,000H,000H,000H	: BT_A8 H
	00	00					250			: TH_AB K
0460	00	00	FC	66	66	66	251	DB	000H,000H,0FCH,066H,066H,066H,07CH,060H	: BT_A9 I
	7C	60					252			: TH_AB K
0468	60	60	F0	00	00	00	253	DB	060H,060H,0F0H,000H,000H,000H	: BT_A4 D
046E	00	00	7C	6C	C6	C6	254	DB	000H,000H,07CH,0C6H,0C6H,0C6H,0C6H,0D6H	: TH_AB K
	C6	DE					255			: BT_50 P
0476	DE	7C	0C	0E	00	00	256	DB	0DEH,07CH,00CH,0DEH,000H,000H	: TH_51 Q
047C	00	00	FC	66	66	66	257	DB	000H,000H,0FCH,066H,066H,066H,07CH,06CH	: BT_52 R
	7C	6C					258			: TH_53 S
0484	66	66	E6	00	00	00	259	DB	066H,066H,0E6H,000H,000H,000H	: BT_54 T
048A	00	00	7C	6C	C6	60	260	DB	000H,000H,07CH,0C6H,0C6H,060H,038H,00CH	: TH_55 U
	38	0C					261			: BT_56 V
0492	C6	C6	7C	00	00	00	262	DB	0C6H,0C6H,07CH,000H,000H,000H	: TH_57 W
0498	00	00	7E	7E	5A	18	263	DB	000H,000H,07EH,07EH,05AH,018H,018H,018H	: BT_58 X
	18	18					264			: TH_59 Y
04A0	18	18	3C	00	00	00	265	DB	018H,018H,03CH,000H,000H,000H	: BT_5A Z
04A6	00	00	C6	C6	C6	C6	266	DB	000H,000H,0C6H,0C6H,0C6H,0C6H,0C6H,0C6H	: TH_5B [
	C6	C6					267			: BT_5C ]
04AE	C6	C6	7C	00	00	00	268	DB	0C6H,0C6H,07CH,000H,000H,000H	: TH_5D ^
04B4	00	00	C6	C6	C6	C6	269	DB	000H,000H,0C6H,0C6H,0C6H,0C6H,0C6H,0C6H	: BT_5E _
	C6	C6					270			: TH_5F `
04BC	6C	38	10	00	00	00	271	DB	06CH,038H,010H,000H,000H,000H	: BT_5A Z
04C2	00	00	C6	C6	C6	C6	272	DB	000H,000H,0C6H,0C6H,0C6H,0C6H,0D6H,0D6H	: TH_5B [
	D6	DE					273			: BT_5C ]
04CA	FE	7C	6C	00	00	00	274	DB	0FEH,07CH,06CH,000H,000H,000H	: TH_5D ^
04D0	00	00	C6	C6	C6	38	275	DB	000H,000H,0C6H,0C6H,0C6H,038H,038H,038H	: BT_5E _
	38	38					276			: TH_5F `
04D8	6C	C6	C6	00	00	00	277	DB	0C6H,0C6H,0C6H,000H,000H,000H	: BT_5A Z
04DE	00	00	66	66	66	66	278	DB	000H,000H,066H,066H,066H,066H,03CH,018H	: TH_5B [
	3C	18					279			: BT_5C ]
04E6	18	18	3C	00	00	00	280	DB	018H,018H,03CH,000H,000H,000H	: TH_5D ^
04EC	00	00	FE	6C	8C	18	281	DB	000H,000H,0FEH,0C6H,08CH,018H,030H,060H	: BT_5A Z
	30	60					282			: TH_5B [
04FA	C2	C6	FE	00	00	00	283	DB	0C2H,0C6H,0FEH,000H,000H,000H	: BT_5C ]
04FA	00	00	3C	30	30	30	284	DB	000H,000H,03CH,030H,030H,030H,030H,030H	: TH_5D ^
	30	30					285			: BT_5A Z
0502	30	30	30	00	00	00	286	DB	030H,030H,03CH,000H,000H,000H	: TH_5B [
0508	00	00	80	C0	E0	70	287	DB	000H,000H,080H,0C0H,0E0H,070H,038H,01CH	: BT_5C ]
	38	1C					288			: TH_5D ^
0510	0E	06	02	00	00	00	289	DB	0DEH,066H,002H,000H,000H,000H	: BT_5E _
0516	00	00	3C	0C	0C	0C	290	DB	000H,000H,03CH,00CH,00CH,00CH,00CH,00CH	: TH_5D ^
	0C	0C					291			: BT_5C ]
051E	0C	0C	3C	00	00	00	292	DB	00CH,00CH,03CH,000H,000H,000H	: TH_5E _
0524	10	38	6C	C6	00	00	293	DB	010H,038H,06CH,0C6H,000H,000H,000H,000H	: BT_5D ]
	00	00					294			: TH_5E _
052C	00	00	00	00	00	00	295	DB	000H,000H,000H,000H,000H,000H	: BT_5E _
0532	00	00	00	00	00	00	296	DB	000H,000H,000H,000H,000H,000H,000H,000H	: TH_5F _
	00	00					297			: BT_5F _
053A	00	00	00	00	FF	00	298	DB	000H,000H,000H,000H,0FFH,000H	: TH_60 ^
	00	00					299			: TH_61 LOWER_CASE A
0540	30	30	18	00	00	00	300	DB	030H,030H,018H,000H,000H,000H,000H,000H	: BT_62 L.C. B
	00	00					301			: TH_63 L.C. C
0548	00	00	00	00	00	00	302	DB	000H,000H,000H,000H,000H,000H	: BT_64 L.C. D
054E	00	00	00	00	00	78	303	DB	000H,000H,000H,000H,000H,078H,00CH,07CH	: TH_65 L.C. E
	0C	7C					304			: BT_61 LOWER_CASE A
0556	CC	CC	76	00	00	00	305	DB	0CCH,0CCH,076H,000H,000H,000H	: TH_62 L.C. B
055C	00	00	E0	60	78	00	306	DB	000H,000H,0E0H,060H,060H,078H,06CH,066H	: BT_63 L.C. C
	6C	66					307			: TH_64 L.C. D
0564	66	66	7C	00	00	00	308	DB	066H,066H,07CH,000H,000H,000H	: TH_65 L.C. E
056A	00	00	00	00	00	7C	309	DB	000H,000H,000H,000H,000H,07CH,0C6H,0C0H	: BT_66 L.C. F
	C6	C0					310			: TH_67 L.C. G
0572	C0	C6	7C	00	00	00	311	DB	0C0H,0C6H,07CH,000H,000H,000H	: TH_68 L.C. H
0578	00	00	1C	0C	3C	00	312	DB	000H,000H,01CH,00CH,00CH,03CH,06CH,0CCH	: TH_69 L.C. I
	6C	CC					313			: BT_64 L.C. D
0580	CC	CC	76	00	00	00	314	DB	0CCH,0CCH,076H,000H,000H,000H	: TH_65 L.C. E
0586	C6	FE	00	00	00	7C	315	DB	000H,000H,000H,000H,000H,07CH,0C6H,0FEH	: TH_66 L.C. F
	C6	FE					316			: TH_67 L.C. G
							317			: TH_



0702	38	6C	38	00	38	6C	444	DB	038H,06CH,038H,000H,038H,06CH,0C6H,0C6H ;	TH_8F
070A	FE	C6	C6	00	00	00	445	DB	0FEH,0C6H,0C6H,000H,000H,000H ;	BT_8F
07E0	18	30	60	00	FE	66	446	DB	018H,030H,060H,000H,0FEH,066H,060H,07CH ;	TH_90
07E8	60	7C					447	DB		
07F0	00	66	FE	00	00	00	450	DB	060H,066H,0FEH,000H,000H,000H ;	BT_90
07FE	00	00	00	00	CC	76	451	DB	000H,000H,000H,000H,0CCH,076H,036H,07EH ;	TH_91
07FF	36	7E					452	DB		
0800	D8	D8	6E	00	00	00	453	DB	0D8H,0D8H,06EH,000H,000H,000H ;	BT_91
080C	FE	CC					454	DB	000H,000H,03EH,06CH,0CCH,0CCH,0FEH,0CCH ;	TH_92
080A	00	10	38	6C	00	7C	455	DB	0CCH,0CCH,0CEH,000H,000H,000H ;	BT_92
0810	C6	C6					456	DB	000H,010H,038H,06CH,000H,07CH,0C6H,0C6H ;	TH_93
0812	00	00	7C	00	00	00	457	DB		
0818	00	00	C6	C6	00	7C	458	DB	0C6H,0C6H,07CH,000H,000H,000H ;	BT_93
0820	C6	C6					459	DB	000H,000H,0C6H,0C6H,000H,07CH,0C6H,0C6H ;	TH_94
0826	00	60	30	18	00	7C	460	DB	0C6H,0C6H,07CH,000H,000H,000H ;	BT_94
082E	C6	C6					461	DB	000H,060H,030H,018H,000H,07CH,0C6H,0C6H ;	TH_95
0834	00	C6	7C	00	00	CC	462	DB	0C6H,0C6H,07CH,000H,000H,000H ;	BT_95
083C	CC	CC					463	DB	000H,030H,078H,0CCH,000H,0CCH,0CCH,0CCH ;	TH_96
0842	00	60	30	18	00	CC	464	DB	0CCH,0CCH,076H,000H,000H,000H ;	BT_96
084A	CC	CC					465	DB	000H,060H,030H,018H,000H,0CCH,0CCH,0CCH ;	TH_97
0850	00	C6	C6	00	00	C6	466	DB	0CCH,0CCH,076H,000H,000H,000H ;	BT_97
0858	C6	C6					467	DB	000H,000H,0C6H,0C6H,000H,0C6H,0C6H ;	TH_98
085E	00	C6	C6	38	00	C6	470	DB	0C6H,07EH,006H,00CH,078H,000H ;	BT_98
0866	C6	C6					471	DB	000H,0C6H,0C6H,038H,06CH,0C6H,0C6H ;	TH_99
086C	00	C6	38	00	00	00	472	DB	0C6H,06CH,038H,000H,000H,000H ;	BT_99
0874	C6	C6					473	DB	000H,0C6H,0C6H,000H,0C6H,0C6H,0C6H ;	TH_9A
087A	00	18	18	3C	66	60	474	DB	0C6H,06CH,038H,000H,000H,000H ;	BT_9A
0882	3C	18	18	00	00	00	475	DB	000H,018H,018H,03CH,066H,060H,060H,066H ;	TH_9B
0888	00	38	6C	64	60	F0	476	DB	03CH,018H,018H,000H,000H,000H ;	BT_9B
0890	60	66	FC	00	00	00	477	DB	000H,038H,06CH,064H,060H,0F0H,060H,060H ;	TH_9C
0896	00	00	66	6C	3C	18	478	DB	060H,0E6H,0FCH,000H,000H,000H ;	BT_9C
089E	7E	18	18	00	00	00	479	DB	000H,000H,066H,066H,03CH,018H,07EH,018H ;	TH_9D
08A4	00	F8	CC	C6	F8	CC	480	DB	07EH,018H,018H,000H,000H,000H ;	BT_9D
08AC	CC	CC					481	DB	000H,0F8H,0CCH,0CCH,0F8H,0C4H,0CCH,0DEH ;	TH_9E
08B2	00	DE	18	18	18	18	482	DB	0CCH,0CCH,0C6H,000H,000H,000H ;	BT_9E
08BA	18	18	18	D8	70	00	483	DB	000H,0DEH,018H,018H,018H,07EH,018H ;	TH_9F
08C0	00	18	30	60	00	78	484	DB	018H,018H,018H,0D8H,070H,000H ;	BT_9F
08C8	0C	7C					485	DB	000H,018H,030H,060H,000H,078H,00CH,07CH ;	TH_A0
08CE	00	0C	18	30	00	38	486	DB	0CCH,0CCH,076H,000H,000H,000H ;	BT_A0
08D2	18	18	30	60	00	00	487	DB	000H,00CH,018H,030H,000H,038H,018H,018H ;	TH_A1
08DC	00	18	30	60	00	7C	488	DB	018H,018H,03CH,000H,000H,000H ;	BT_A1
08E4	C6	C6					489	DB	000H,018H,030H,060H,000H,07CH,0C6H,0C6H ;	TH_A2
08EA	00	18	30	60	00	CC	490	DB	0C6H,0C6H,07CH,000H,000H,000H ;	BT_A2
08F2	CC	CC					491	DB	000H,018H,030H,060H,000H,0CCH,0CCH,0CCH ;	TH_A3
08F8	00	00	76	DC	00	00	492	DB	0CCH,0CCH,076H,000H,000H,000H ;	BT_A3
0900	66	66	66	00	00	00	493	DB	000H,000H,076H,00CH,000H,0CCH,066H,066H ;	TH_A4
0906	FE	DE					494	DB	066H,066H,066H,000H,000H,000H ;	BT_A4
090E	CE	C6					495	DB	076H,0DCH,000H,0C6H,066H,0F6H,0FEH,0DEH ;	TH_A5
0914	00	3C	6C	00	00	00	496	DB	0CEH,0C6H,0C6H,000H,000H,000H ;	BT_A5
091C	7E	00					497	DB	000H,03CH,06CH,06CH,03EH,000H,07EH,000H ;	TH_A6
0922	00	00	00	00	00	00	498	DB	000H,000H,000H,000H,000H,000H ;	BT_A6
092A	00	00	00	00	00	00	499	DB	000H,038H,06CH,06CH,038H,000H,07CH,000H ;	TH_A7
0930	00	00	30	30	00	30	500	DB	000H,000H,000H,000H,000H,000H ;	BT_A7
0938	C6	C6					501	DB	000H,000H,030H,030H,000H,030H,030H,060H ;	TH_A8
093E	00	00	00	00	00	00	502	DB	0C6H,0C6H,07CH,000H,000H,000H ;	BT_A8
0946	00	00	00	00	00	00	503	DB	000H,000H,000H,000H,000H,0FEH,0C0H ;	TH_A9
094C	FE	06					504	DB	0C0H,0C0H,000H,000H,000H,000H ;	BT_A9
0954	06	06	00	00	00	00	505	DB	000H,000H,000H,000H,000H,0FEH,006H ;	TH_AA
095A	00	00	C0	C6	CC	D8	506	DB	006H,006H,000H,000H,000H,000H ;	BT_AA
0962	DC	8C	06	18	3E	00	507	DB	000H,0C0H,0C0H,0C6H,0CCH,0D8H,030H,060H ;	TH_AB
0968	00	00	C0	C6	CC	D8	508	DB	0DCH,086H,00CH,018H,03EH,000H ;	BT_AB
0970	30	66					509	DB	000H,0C0H,0C0H,0C6H,0CCH,0D8H,030H,066H ;	TH_AC
0976	00	00	00	00	00	00	510	DB	0CEH,09EH,03EH,006H,006H,000H ;	BT_AC
097E	3C	3C	18	00	00	00	511	DB	000H,000H,018H,018H,000H,018H,018H,03CH ;	TH_AD
0984	00	00	00	00	36	6C	512	DB	03CH,03CH,018H,000H,000H,000H ;	BT_AD
098C	D8	6C					513	DB	000H,000H,000H,000H,036H,06CH,0D8H,06CH ;	TH_AE
0992	00	00	00	00	00	00	514	DB	036H,000H,000H,000H,000H,000H ;	BT_AE
099A	D8	6C					515	DB	000H,000H,000H,000H,0D8H,06CH,036H,06CH ;	TH_AF
09A0	11	44	11	44	11	44	516	DB	0D8H,000H,000H,000H,000H,000H ;	BT_AF
09A8	11	44	11	44	11	44	517	DB	011H,044H,011H,044H,011H,044H,011H,044H ;	TH_B0
09AE	55	AA	55	AA	55	AA	518	DB	011H,044H,011H,044H,011H,044H ;	BT_B0
09B6	55	AA	55	AA	55	AA	519	DB	055H,0AAH,055H,0AAH,055H,0AAH,055H,0AAH ;	TH_B1
09BC	DD	77	DD	77	DD	77	520	DB	055H,0AAH,055H,0AAH,055H,0AAH ;	BT_B1
09C4	DD	77	DD	77	DD	77	521	DB	0DDH,077H,0DDH,077H,0DDH,077H,0DDH,077H ;	TH_B2
09CA	18	18	18	18	18	18	522	DB	0DDH,077H,0DDH,077H,0DDH,077H ;	BT_B2
09D2	18	18	18	18	18	18	523	DB	018H,018H,018H,018H,018H,018H,018H ;	TH_B3
09DA	18	18	18	18	18	18	524	DB	018H,018H,018H,018H,018H,018H,018H ;	BT_B3
09E0	18	18	18	18	18	18	525	DB	018H,018H,018H,018H,018H,018H,018H ;	BT_B4
09E6	18	18	18	18	18	18	526	DB	018H,018H,018H,018H,018H,018H,018H ;	TH_B5
09FC	36	36	36	36	36	36	527	DB	018H,018H,018H,018H,018H,018H,018H ;	BT_B5
0A02	00	00	00	00	00	00	528	DB	036H,036H,036H,036H,036H,036H,036H,036H ;	TH_B6
0A0A	36	36	36	36	36	36	529	DB	036H,036H,036H,036H,036H,036H,036H,036H ;	BT_B6
							530	DB	000H,000H,000H,000H,000H,000H,000H,0FEH ;	TH_B7
							531	DB	036H,036H,036H,036H,036H,036H ;	BT_B7

0A10	00 00 00 00 00 F8	570	DB	000H,000H,000H,000H,000H,0F8H,018H,0F8H ;	TH_B8
	18 F8	571			
0A18	18 18 18 18 18 18	572	DB	018H,018H,018H,018H,018H,018H	BT_B8
0A1E	36 36 36 36 36 F6	573	DB	036H,036H,036H,036H,036H,0F6H,006H,0F6H ;	TH_B9
	06 F6	574			
0A26	36 36 36 36 36 36	575	DB	036H,036H,036H,036H,036H,036H	BT_B9
0A2C	36 36 36 36 36 36	576	DB	036H,036H,036H,036H,036H,036H,036H,036H ;	TH_BA
	36 36	577			
0A34	36 36 36 36 36 36	578	DB	036H,036H,036H,036H,036H,036H	BT_BA
0A3A	00 00 00 00 00 FE	579	DB	000H,000H,000H,000H,000H,0FEH,006H,0F6H ;	TH_BB
	06 FE	580			
0A42	36 36 36 36 36 36	581	DB	036H,036H,036H,036H,036H,036H	BT_BB
0A48	36 36 36 36 36 F6	582	DB	036H,036H,036H,036H,036H,0F6H,006H,0FEH ;	TH_BC
	06 FE	583			
0A50	00 00 00 00 00 00	584	DB	000H,000H,000H,000H,000H,000H	BT_BC
0A56	36 36 36 36 36 36	585	DB	036H,036H,036H,036H,036H,036H,036H,0FEH ;	TH_BD
	36 FE	586			
0A5E	00 00 00 00 00 00	587	DB	000H,000H,000H,000H,000H,000H	BT_BD
0A64	18 18 18 18 18 F8	588	DB	018H,018H,018H,018H,018H,0F8H,018H,0F8H ;	TH_BE
	18 F8	589			
0A6C	00 00 00 00 00 00	590	DB	000H,000H,000H,000H,000H,000H	BT_BE
0A72	00 00 00 00 00 00	591	DB	000H,000H,000H,000H,000H,000H,000H,0F8H ;	TH_BF
	00 F8	592			
0A7A	18 18 18 18 18 18	593	DB	018H,018H,018H,018H,018H,018H	BT_BF
	18 18	594			
0A80	18 18 18 18 18 18	595	DB	018H,018H,018H,018H,018H,018H,018H,01FH ;	TH_C0
	18 1F	596			
0A88	00 00 00 00 00 00	597	DB	000H,000H,000H,000H,000H,000H	BT_C0
0A8E	18 18 18 18 18 18	598	DB	018H,018H,018H,018H,018H,018H,018H,0FFH ;	TH_C1
	18 1F	599			
0A96	00 00 00 00 00 00	600	DB	000H,000H,000H,000H,000H,000H	BT_C1
0A9C	00 00 00 00 00 00	601	DB	000H,000H,000H,000H,000H,000H,000H,0FFH ;	TH_C2
	00 FF	602			
0AA4	18 18 18 18 18 18	603	DB	018H,018H,018H,018H,018H,018H	BT_C2
0AA2	18 18 18 18 18 18	604	DB	018H,018H,018H,018H,018H,018H,018H,01FH ;	TH_C3
	18 1F	605			
0AB2	18 18 18 18 18 18	606	DB	018H,018H,018H,018H,018H,018H	BT_C3
0AB8	00 00 00 00 00 00	607	DB	000H,000H,000H,000H,000H,000H,000H,0FFH ;	TH_C4
	00 FF	608			
0AC0	00 00 00 00 00 00	609	DB	000H,000H,000H,000H,000H,000H	BT_C4
0AC6	18 18 18 18 18 18	610	DB	018H,018H,018H,018H,018H,018H,018H,0FFH ;	TH_C5
	18 FF	611			
0ACE	18 18 18 18 18 18	612	DB	018H,018H,018H,018H,018H,018H	BT_C5
0AD4	18 18 18 18 18 1F	613	DB	018H,018H,018H,018H,018H,01FH,018H,01FH ;	TH_C6
	18 1F	614			
0ADC	18 18 18 18 18 18	615	DB	018H,018H,018H,018H,018H,018H	BT_C6
0AE2	36 36 36 36 36 36	616	DB	036H,036H,036H,036H,036H,036H,036H,037H ;	TH_C7
	36 37	617			
0AEA	36 36 36 36 36 36	618	DB	036H,036H,036H,036H,036H,036H	BT_C7
0AF0	36 36 36 36 36 37	619	DB	036H,036H,036H,036H,036H,037H,030H,03FH ;	TH_C8
	30 3F	620			
0AF8	00 00 00 00 00 00	621	DB	000H,000H,000H,000H,000H,000H	BT_C8
0AFE	00 00 00 00 00 3F	622	DB	000H,000H,000H,000H,000H,03FH,030H,037H ;	TH_C9
	30 37	623			
0B06	36 36 36 36 36 36	624	DB	036H,036H,036H,036H,036H,036H	BT_C9
0B0C	36 36 36 36 36 F7	625	DB	036H,036H,036H,036H,036H,0F7H,000H,0FFH ;	TH_CA
	00 FF	626			
0B14	00 00 00 00 00 00	627	DB	000H,000H,000H,000H,000H,000H	BT_CA
0B1A	00 00 00 00 00 3F	628	DB	000H,000H,000H,000H,000H,0FFH,000H,0F7H ;	TH_CB
	00 F7	629			
0B22	36 36 36 36 36 36	630	DB	036H,036H,036H,036H,036H,036H	BT_CB
0B28	36 36 36 36 36 37	631	DB	036H,036H,036H,036H,036H,037H,030H,037H ;	TH_CC
	30 37	632			
0B30	36 36 36 36 36 36	633	DB	036H,036H,036H,036H,036H,036H	BT_CC
0B36	00 00 00 00 00 FF	634	DB	000H,000H,000H,000H,000H,0FFH,000H,0FFH ;	TH_CD
	00 FF	635			
0B3E	00 00 00 00 00 00	636	DB	000H,000H,000H,000H,000H,000H	BT_CD
0B44	36 36 36 36 36 37	637	DB	036H,036H,036H,036H,036H,0F7H,000H,0F7H ;	TH_CE
	00 F7	638			
0B4C	36 36 36 36 36 36	639	DB	036H,036H,036H,036H,036H,036H	BT_CE
0B52	18 18 18 18 18 FF	640	DB	018H,018H,018H,018H,018H,0FFH,000H,0FFH ;	TH_CF
	00 FF	641			
0B5A	00 00 00 00 00 00	642	DB	000H,000H,000H,000H,000H,000H	BT_CF
	00 00	643			
0B60	36 36 36 36 36 36	644	DB	036H,036H,036H,036H,036H,036H,036H,0FFH ;	TH_D0
	36 FF	645			
0B68	00 00 00 00 00 00	646	DB	000H,000H,000H,000H,000H,000H	BT_D0
0B6E	00 00 00 00 00 FF	647	DB	000H,000H,000H,000H,000H,0FFH,000H,0FFH ;	TH_D1
	00 FF	648			
0B76	18 18 18 18 18 18	649	DB	018H,018H,018H,018H,018H,018H	BT_D1
0B7C	00 00 00 00 00 00	650	DB	000H,000H,000H,000H,000H,000H,000H,0FFH ;	TH_D2
	00 FF	651			
0B84	36 36 36 36 36 36	652	DB	036H,036H,036H,036H,036H,036H	BT_D2
0B8A	36 36 36 36 36 36	653	DB	036H,036H,036H,036H,036H,036H,036H,03FH ;	TH_D3
	36 3F	654			
0B92	00 00 00 00 00 00	655	DB	000H,000H,000H,000H,000H,000H	BT_D3
0B98	18 18 18 18 18 1F	656	DB	018H,018H,018H,018H,018H,01FH,018H,01FH ;	TH_D4
	18 1F	657			
0BA0	00 00 00 00 00 00	658	DB	000H,000H,000H,000H,000H,000H	BT_D4
0BA6	00 00 00 00 00 1F	659	DB	000H,000H,000H,000H,000H,01FH,018H,01FH ;	TH_D5
	18 1F	660			
0BAE	18 18 18 18 18 18	661	DB	018H,018H,018H,018H,018H,018H	BT_D5
0BB4	00 00 00 00 00 00	662	DB	000H,000H,000H,000H,000H,000H,000H,03FH ;	TH_D6
	00 3F	663			
0BBC	36 36 36 36 36 36	664	DB	036H,036H,036H,036H,036H,036H	BT_D6
0BC2	36 36 36 36 36 36	665	DB	036H,036H,036H,036H,036H,036H,036H,0FFH ;	TH_D7
	36 FF	666			
0BCA	36 36 36 36 36 36	667	DB	036H,036H,036H,036H,036H,036H	BT_D7
0BD0	18 18 18 18 18 FF	668	DB	018H,018H,018H,018H,018H,0F8H,018H,0FFH ;	TH_D8
	18 FF	669			
0BD8	18 18 18 18 18 18	670	DB	018H,018H,018H,018H,018H,018H	BT_D8
0BDE	18 18 18 18 18 18	671	DB	018H,018H,018H,018H,018H,018H,018H,0F8H ;	TH_D9
	18 F8	672			
0BE6	00 00 00 00 00 00	673	DB	000H,000H,000H,000H,000H,000H	BT_D9
0BEC	00 00 00 00 00 00	674	DB	000H,000H,000H,000H,000H,000H,000H,01FH ;	TH_DA
	00 1F	675			
0BF4	18 18 18 18 18 18	676	DB	018H,018H,018H,018H,018H,018H	BT_DA
0BF8	FF FF FF FF FF FF	677	DB	0FFH,0FFH,0FFH,0FFH,0FFH,0FFH,0FFH,0FFH ;	TH_DB
	FF FF	678			
0C02	FF FF FF FF FF FF	679	DB	0FFH,0FFH,0FFH,0FFH,0FFH,0FFH	BT_DB
0C08	00 00 00 00 00 00	680	DB	000H,000H,000H,000H,000H,000H,000H,0FFH ;	TH_DC
	00 FF	681			
0C10	FF FF FF FF FF FF	682	DB	0FFH,0FFH,0FFH,0FFH,0FFH,0FFH	BT_DC
0C16	F0 F0 F0 F0 F0 F0	683	DB	0F0H,0F0H,0F0H,0F0H,0F0H,0F0H,0F0H,0F0H ;	TH_DD
	F0 F0	684			
0C1E	F0 F0 F0 F0 F0 F0	685	DB	0F0H,0F0H,0F0H,0F0H,0F0H,0F0H	BT_DD
0C24	0F 0F 0F 0F 0F 0F	686	DB	00FH,00FH,00FH,00FH,00FH,00FH,00FH,00FH ;	TH_DE
	0F 0F	687			
0C2C	0F 0F 0F 0F 0F 0F	688	DB	00FH,00FH,00FH,00FH,00FH,00FH	BT_DE
0C32	FF FF FF FF FF FF	689	DB	0FFH,0FFH,0FFH,0FFH,0FFH,0FFH,0FFH,000H ;	TH_DF
	FF 00	690			
0C3A	00 00 00 00 00 00	691	DB	000H,000H,000H,000H,000H,000H	BT_DF
	00 00	692			
0C40	00 00 00 00 00 76	693	DB	000H,000H,000H,000H,000H,076H,0DCH,0D8H ;	TH_E0
	DC D8	694			
0C48	D8 DC 76 00 00 00	695	DB	0D8H,0DCH,076H,000H,000H,000H	BT_E0

0C4E	00 00 00 00 7C C6	696	DB	000H,000H,000H,000H,07CH,0C6H,0FCH,0C6H ;	TH_E1
	FC C6	697			
0C56	C6 FC C0 C0 40 00	698	DB	0C6H,0FCH,0C0H,0C0H,040H,000H ;	BT_E1
0C5C	00 00 FE C6 C6 00	699	DB	000H,000H,0FEH,0C6H,0C6H,0C0H,0C0H ;	TH_E2
	C0 C0	700			
0C64	C0 C0 C0 00 00 00	701	DB	0C0H,0C0H,0C0H,000H,000H,000H	BT_E2
0C6A	00 00 00 00 FE C6	702	DB	000H,000H,000H,000H,0FEH,06CH,06CH ;	TH_E3
	6C 6C	703			
0C72	6C 6C 6C 00 00 00	704	DB	06CH,06CH,06CH,000H,000H,000H	BT_E3
0C78	00 00 FE C6 C6 30	705	DB	000H,000H,0FEH,0C6H,06CH,030H,018H,030H ;	TH_E4
	18 30	706			
0C80	60 C6 FE 00 00 00	707	DB	060H,0C6H,0FEH,000H,000H,000H	BT_E4
0C86	00 00 00 00 00 7E	708	DB	000H,000H,000H,000H,000H,07EH,0DBH,0DBH ;	TH_E5
	D8 D8	709			
0C8E	D8 D8 70 00 00 00	710	DB	0DBH,0DBH,070H,000H,000H,000H	BT_E5
0C94	00 00 00 00 66 66	711	DB	000H,000H,000H,000H,07EH,066H,066H ;	TH_E6
	66 66	712			
0C9C	7C 60 60 C0 00 00	713	DB	07CH,060H,060H,0C0H,000H,000H	BT_E6
0CA2	00 00 00 00 76 DC	714	DB	000H,000H,000H,000H,07EH,0DC,018H,018H ;	TH_E7
	18 18	715			
0CAA	18 18 18 00 00 00	716	DB	018H,018H,018H,000H,000H,000H	BT_E7
0CB0	00 00 7E 18 3C 66	717	DB	000H,000H,07EH,018H,03CH,066H,066H ;	TH_E8
	66 66	718			
0CB8	3C 1E 7E 00 00 00	719	DB	03CH,018H,07EH,000H,000H,000H	BT_E8
0CBE	00 00 38 6C C6 C6	720	DB	000H,000H,038H,06CH,0C6H,0C6H,0FEH,0C6H ;	TH_E9
	FE C6	721			
0CC6	6C 6C 38 00 00 00	722	DB	0C6H,06CH,038H,000H,000H,000H	BT_E9
0CCC	00 00 38 6C C6 C6	723	DB	000H,000H,038H,06CH,0C6H,0C6H,06CH ;	TH_EA
	6C 6C	724			
0CD4	6C 6C EE 00 00 00	725	DB	06CH,06CH,0EEH,000H,000H,000H	BT_EA
0CDA	00 00 1E 30 18 0C	726	DB	000H,000H,01EH,030H,018H,00CH,03EH,066H ;	TH_EB
	3E 66	727			
0CE2	66 66 3C 00 00 00	728	DB	066H,066H,03CH,000H,000H,000H	BT_EB
0CE8	00 00 00 00 00 7E	729	DB	000H,000H,000H,000H,000H,07EH,0DBH,0DBH ;	TH_EC
	D8 D8	730			
0CF0	7E 00 00 00 00 00	731	DB	07EH,000H,000H,000H,000H,000H	BT_EC
0CF6	00 00 03 06 7E DB	732	DB	000H,000H,003H,066H,07EH,0DBH,0DBH,0F3H ;	TH_ED
	D8 F3	733			
0CFE	7E 60 C0 00 00 00	734	DB	07EH,060H,0C0H,000H,000H,000H	BT_ED
0D04	00 00 1C 30 60 60	735	DB	000H,000H,01CH,030H,060H,060H,07CH,060H ;	TH_EE
	7C 60	736			
0D0C	60 30 1C 00 00 00	737	DB	060H,030H,01CH,000H,000H,000H	BT_EE
0D12	00 00 00 7C C6 C6	738	DB	000H,000H,000H,07CH,0C6H,0C6H,0C6H,0C6H ;	TH_EF
	C6 C6	739			
0D1A	C6 C6 C6 00 00 00	740	DB	0C6H,0C6H,0C6H,000H,000H,000H	BT_EF
		741			
0D20	00 00 00 FE 00 00	742	DB	000H,000H,000H,0FEH,000H,000H,0FEH,000H ;	TH_F0
	FE 00	743			
0D28	00 FE 00 00 00 00	744	DB	000H,0FEH,000H,000H,000H,000H	BT_F0
0D2E	00 00 00 18 18 7E	745	DB	000H,000H,000H,018H,018H,07EH,018H,018H ;	TH_F1
	18 18	746			
0D36	00 00 FF 00 00 00	747	DB	000H,000H,0FFH,000H,000H,000H	BT_F1
0D3C	00 30 18 0C 06	748	DB	000H,000H,030H,018H,00CH,066H,00CH,018H ;	TH_F2
	0C 18	749			
0D44	30 00 7E 00 00 00	750	DB	030H,000H,07EH,000H,000H,000H	BT_F2
0D4A	00 00 0C 18 30 60	751	DB	000H,000H,00CH,018H,030H,060H,030H,018H ;	TH_F3
	30 18	752			
0D52	0C 00 7E 00 00 00	753	DB	00CH,000H,07EH,000H,000H,000H	BT_F3
0D58	00 00 0E 18 18 18	754	DB	000H,000H,00EH,018H,018H,018H,018H,018H ;	TH_F4
	18 18	755			
0D60	18 18 18 18 18 18	756	DB	018H,018H,018H,018H,018H,018H	BT_F4
0D66	18 18 18 18 18 18	757	DB	018H,018H,018H,018H,018H,018H,018H,018H ;	TH_F5
	18 18	758			
0D6E	D8 D8 70 00 00 00	759	DB	0DBH,0DBH,070H,000H,000H,000H	BT_F5
0D74	00 00 00 18 18 00	760	DB	000H,000H,000H,018H,018H,000H,07EH,000H ;	TH_F6
	7E 00	761			
0D7C	18 18 00 00 00 00	762	DB	018H,018H,000H,000H,000H,000H	BT_F6
0D82	00 00 00 76 DC	763	DB	000H,000H,000H,000H,076H,0DC,000H,076H ;	TH_F7
	00 76	764			
0D8A	DC 00 00 00 00 00	765	DB	0DC,000H,000H,000H,000H,000H	BT_F7
0D90	00 38 6C 6C 38 00	766	DB	000H,038H,06CH,06CH,038H,000H,000H,000H ;	TH_F8
	00 00	767			
0D98	00 00 00 00 00 00	768	DB	000H,000H,000H,000H,000H,000H	BT_F8
0D9E	00 00 00 00 00 00	769	DB	000H,000H,000H,000H,000H,000H,018H,018H ;	TH_F9
	18 18	770			
0DA6	00 00 00 00 00 00	771	DB	000H,000H,000H,000H,000H,000H	BT_F9
0DAC	00 00 00 00 00 00	772	DB	000H,000H,000H,000H,000H,000H,000H,018H ;	TH_FA
	00 18	773			
0DB4	00 00 00 00 00 00	774	DB	000H,000H,000H,000H,000H,000H	BT_FA
0DBA	00 0F 0C 0C 0C 0C	775	DB	000H,00FH,00CH,00CH,00CH,00CH,00CH,0ECH ;	TH_FB
	0C EC	776			
0DC2	6C 3C 1C 00 00 00	777	DB	06CH,03CH,01CH,000H,000H,000H	BT_FB
0DC8	00 D8 6C 6C 6C 6C	778	DB	000H,0DBH,06CH,06CH,06CH,06CH,06CH,000H ;	TH_FC
	6C 00	779			
0DD0	00 00 00 00 00 00	780	DB	000H,000H,000H,000H,000H,000H	BT_FC
0DD6	00 70 D8 30 60 C8	781	DB	000H,070H,0DBH,030H,060H,0C8H,0F8H,000H ;	TH_FD
	F8 00	782			
0DD E	00 00 00 00 00 00	783	DB	000H,000H,000H,000H,000H,000H	BT_FD
0DE4	00 00 00 00 7C 7C	784	DB	000H,000H,000H,000H,07CH,07CH	TH_FE
	7C 7C	785			
0DEC	7C 7C 00 00 00 00	786	DB	07CH,07CH,000H,000H,000H,000H	BT_FE
0DF2	00 00 00 00 00 00	787	DB	000H,000H,000H,000H,000H,000H,000H,000H ;	TH_FF
	00 00	788			
0DFA	00 00 00 00 00 00	789	DB	000H,000H,000H,000H,000H,000H	BT_FF
0E00		790			
		791	CODE	ENDS	
			END		





00C8	18 00	57			
	18 18 18 7E 3C	58			
	18 00	59			
00D0	00 18 0C FE 0C 18	60			
	00 00	61			
00D8	00 30 60 FE 60 30	62			
	00 00	63			
00E0	00 00 C0 C0 C0 FE	64			
	00 00	65			
00E8	00 24 66 FF 66 24	66			
	00 00	67			
00F0	00 18 3C 7E FF FF	68			
	00 00	69			
00F8	00 FF 7E 3C 18	70			
	00 00	71			
		72			
0100	00 00 00 00 00 00	73			
	00 00	74			
0108	30 78 78 30 30 00	75			
	30 00	76			
0110	6C 6C 6C 00 00 00	77			
	00 00	78			
0118	6C 6C FE 6C FE 6C	79			
	6C 00	80			
0120	30 7C C0 78 0C F8	81			
	00 00	82			
0128	00 C6 CC 18 30 66	83			
	C6 00	84			
0130	38 6C 38 76 DC CC	85			
	76 00	86			
0138	60 60 C0 00 00 00	87			
	00 00	88			
0140	18 30 60 60 60 30	89			
	18 00	90			
0148	60 30 18 18 18 30	91			
	60 00	92			
0150	00 66 3C FF 3C 66	93			
	00 00	94			
0158	00 30 30 FC 30 30	95			
	00 00	96			
0160	00 00 00 00 00 30	97			
	30 60	98			
0168	00 00 00 FC 00 00	99			
	00 00	100			
0170	00 00 00 00 00 30	101			
	30 00	102			
0178	06 0C 18 30 60 C0	103			
	80 00	104			
		105			
0180	7C CE CE DE F6 E6	106			
	7C 00	107			
0188	30 70 30 30 30 30	108			
	FC 00	109			
0190	78 CC 0C 38 60 CC	110			
	FC 00	111			
0198	78 CC 0C 38 0C CC	112			
	00 00	113			
01A0	1C 3C 6C CC FE 0C	114			
	1E 00	115			
01A8	FC C0 F8 0C 0C CC	116			
	78 00	117			
01B0	38 60 C0 F8 CC CC	118			
	78 00	119			
01B8	FC CC 0C 18 30 30	120			
	30 00	121			
01C0	78 CC CC 78 CC CC	122			
	78 00	123			
01C8	78 CC CC 7C 0C 18	124			
	70 00	125			
01D0	00 30 30 00 00 30	126			
	30 00	127			
01D8	00 30 30 00 00 30	128			
	30 60	129			
01E0	18 30 60 C0 60 30	130			
	18 00	131			
01E8	00 00 FC 00 00 FC	132			
	00 00	133			
01F0	60 30 18 0C 18 30	134			
	60 00	135			
01F8	78 CC 0C 18 30 00	136			
	30 00	137			
		138			
0200	7C CE DE DE DE C0	139			
	78 00	140			
0208	30 78 CC CC FC CC	141			
	CC 00	142			
0210	FC 66 66 7C 66 66	143			
	FC 00	144			
0218	3C 66 C0 C0 C0 66	145			
	3C 00	146			
0220	F8 6C 66 66 66 6C	147			
	F8 00	148			
0228	FE 62 68 78 68 62	149			
	FE 00	150			
0230	FE 62 68 78 68 60	151			
	F0 00	152			
0238	3C 66 C0 C0 CE 66	153			
	3E 00	154			
0240	CC CC CC FC CC CC	155			
	CC 00	156			
0248	78 30 30 30 30 30	157			
	78 00	158			
0250	1E 0C 0C 0C CC CC	159			
	00 00	160			
0258	E6 66 6C 78 6C 66	161			
	E6 00	162			
0260	F0 60 60 62 66	163			
	FE 00	164			
0268	C6 EE FE D6 C6	165			
	C6 00	166			
0270	C6 E6 FE D6 CE C6	167			
	C6 00	168			
0278	38 6C C6 C6 C6 C6	169			
	38 00	170			
		171			
0280	FC 66 66 7C 60 60	172			
	F0 00	173			
0288	78 CC CC CC DC 78	174			
	1C 00	175			
0290	FC 66 66 7C 6C 66	176			
	E6 00	177			
0298	78 CC E0 70 1C CC	178			
	78 00	179			
02A0	FC B4 30 30 30 30	180			
	78 00	181			
02A8	CC CC CC CC CC CC	182			
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0280	FC 00	183			
	CC CC CC CC CC 78	184	DB	OCCH, OCCH, OCCH, OCCH, OCCH, 078H, 030H, 000H ;	V D_56
	30 00	185			
0288	C6 C6 C6 D6 FE EE	186	DB	OC6H, OC6H, OC6H, OD6H, OFEH, OE6H, OC6H, 000H ;	W D_57
	C6 00	187			
02C0	C6 C6 C6 38 38 6C	188	DB	OC6H, OC6H, 06CH, 038H, 038H, 06CH, OC6H, 000H ;	X D_58
	C6 00	189			
02C8	CC CC CC 78 30 30	190	DB	OCCH, OCCH, OCCH, 078H, 030H, 030H, 078H, 000H ;	Y D_59
	78 00	191			
02D0	FE C6 8C 18 32 66	192	DB	OFEH, OC6H, 08CH, 018H, 032H, 066H, OFEH, 000H ;	Z D_5A
	FE 00	193			
02D8	78 60 60 60 60 60	194	DB	078H, 060H, 060H, 060H, 060H, 060H, 078H, 000H ;	[ D_5B
	78 00	195			
02E0	CO 60 30 18 0C 06	196	DB	OC0H, 060H, 030H, 018H, 00CH, 006H, 002H, 000H ;	BACKSLASH D_5C
	02 00	197			
02E8	78 18 18 18 18 18	198	DB	078H, 018H, 018H, 018H, 018H, 078H, 000H ;	] D_5D
	78 00	199			
02F0	10 38 6C C6 00 00	200	DB	010H, 038H, 06CH, OC6H, 000H, 000H, 000H, 000H ;	CIRCUMFLEX D_5E
	00 00	201			
02F8	00 00 00 00 00 00	202	DB	000H, 000H, 000H, 000H, 000H, 000H, 000H, 000H ;	_ D_5F
	00 FF	203			
		204			
0300	30 30 18 00 00 00	205	DB	030H, 030H, 018H, 000H, 000H, 000H, 000H, 000H ;	' D_60
	00 00	206			
0308	00 00 78 0C 7C CC	207	DB	000H, 000H, 078H, 00CH, 07CH, OCCH, 076H, 000H ;	LOWER CASE A D_61
	76 00	208			
0310	E0 60 60 7C 66 66	209	DB	0E0H, 060H, 060H, 07CH, 066H, 066H, 0DCH, 000H ;	L.C. B D_62
	DC 00	210			
0318	00 00 78 CC CC CC	211	DB	000H, 000H, 078H, OCCH, OCCH, OCCH, 078H, 000H ;	L.C. C D_63
	78 00	212			
0320	1C 0C 0C 7C CC CC	213	DB	01CH, 00CH, 00CH, 07CH, OCCH, OCCH, 076H, 000H ;	L.C. D D_64
	76 00	214			
0328	00 00 78 CC FC CO	215	DB	000H, 000H, 078H, OCCH, 0FCH, OCCH, 078H, 000H ;	L.C. E D_65
	78 00	216			
0330	F0 60 60 F0 60 60	217	DB	038H, 06CH, 060H, 0F0H, 060H, 060H, 0F0H, 000H ;	L.C. F D_66
	30 00	218			
0338	00 00 78 CC CC 7C	219	DB	000H, 000H, 076H, OCCH, OCCH, 07CH, OCCH, 0F8H ;	L.C. G D_67
	0C F8	220			
0340	E0 60 6C 76 66 66	221	DB	0E0H, 060H, 06CH, 076H, 066H, 066H, 0E6H, 000H ;	L.C. H D_68
	E6 00	222			
0348	00 00 70 30 30 30	223	DB	030H, 000H, 070H, 030H, 030H, 030H, 078H, 000H ;	L.C. I D_69
	78 00	224			
0350	0C 00 0C 0C 0C CC	225	DB	00CH, 000H, 00CH, 00CH, 00CH, OCCH, OCCH, 078H ;	L.C. J D_6A
	CC 78	226			
0358	E0 60 66 6C 78 6C	227	DB	0E0H, 060H, 066H, 06CH, 078H, 06CH, 0E6H, 000H ;	L.C. K D_6B
	E6 00	228			
0360	70 30 30 30 30 30	229	DB	070H, 030H, 030H, 030H, 030H, 030H, 078H, 000H ;	L.C. L D_6C
	78 00	230			
0368	00 00 CC FE FE FE	231	DB	000H, 000H, OCCH, OFEH, OFEH, OD6H, OC6H, 000H ;	L.C. M D_6D
	C6 00	232			
0370	00 00 F8 CC CC CC	233	DB	000H, 000H, 0F8H, OCCH, OCCH, OCCH, OCCH, 00CH, 000H ;	L.C. N D_6E
	CC 00	234			
0378	00 00 78 CC CC CC	235	DB	000H, C00H, 078H, OCCH, OCCH, OCCH, 078H, 000H ;	L.C. O D_6F
	78 00	236			
		237			
0380	00 00 DC 66 66 7C	238	DB	000H, 000H, 0DCH, 066H, 066H, 07CH, 060H, 0F0H ;	L.C. P D_70
	60 F0	239			
0388	00 00 76 CC CC 7C	240	DB	000H, 000H, 076H, OCCH, OCCH, 07CH, 00CH, 01EH ;	L.C. Q D_71
	0C 1E	241			
0390	00 00 DC 76 66 60	242	DB	000H, 000H, 0DCH, 076H, 066H, 060H, 0F0H, 000H ;	L.C. R D_72
	F0 00	243			
0398	00 00 7C CO 78 0C	244	DB	000H, 000H, 07CH, 0C0H, 078H, 00CH, 0F8H, 000H ;	L.C. S D_73
	F8 00	245			
03A0	10 30 7C 30 30 34	246	DB	010H, 030H, 07CH, 030H, 030H, 034H, 018H, 000H ;	L.C. T D_74
	18 00	247			
03A8	00 00 CC CC CC CC	248	DB	000H, 000H, OCCH, OCCH, OCCH, OCCH, 076H, 000H ;	L.C. U D_75
	76 00	249			
03B0	00 00 CC CC CC 78	250	DB	000H, 000H, OCCH, OCCH, OCCH, 078H, 030H, 000H ;	L.C. V D_76
	30 00	251			
03B8	00 00 C6 D6 FE FE	252	DB	000H, 000H, OC6H, OD6H, OFEH, OFEH, 06CH, 000H ;	L.C. W D_77
	6C 00	253			
03C0	00 00 C6 6C 38 6C	254	DB	000H, 000H, OC6H, 06CH, 038H, 06CH, OC6H, 000H ;	L.C. X D_78
	C6 00	255			
03C8	00 00 CC CC CC 7C	256	DB	000H, 000H, OCCH, OCCH, OCCH, 07CH, 00CH, 0F8H ;	L.C. Y D_79
	0C F8	257			
03D0	00 00 FC 98 30 64	258	DB	000H, 000H, 0FCH, 098H, 030H, 064H, 0FCH, 000H ;	L.C. Z D_7A
	FC 00	259			
03D8	1C 30 30 E0 30 30	260	DB	01CH, 030H, 030H, 0E0H, 030H, 030H, 01CH, 000H ;	L.B. BRACK_D_7B
	1C 00	261			
03E0	18 18 18 00 18 18	262	DB	018H, 018H, 018H, 000H, 018H, 018H, 018H, 000H ;	] D_7C
	18 00	263			
03E8	E0 30 30 1C 30 30	264	DB	0E0H, 030H, 030H, 01CH, 030H, 030H, 0E0H, 000H ;	R BRACK_D_7D
	E0 00	265			
03F0	00 00 00 00 00 00	266	DB	076H, 0DCH, 000H, 000H, 000H, 000H, 000H, 000H ;	TILDE D_7E
	76 DC	267			
03F8	00 10 38 6C C6 C6	268	DB	000H, 010H, 038H, 06CH, OC6H, OC6H, 0C6H, OFEH, 000H ;	DELTA D_7F
	FE 00	269			
		270			
0400		271	INT_1F_1	LABEL BYTE	
		272			
0400	78 CC CO CC 78 18	273	DB	078H, OCCH, OCCH, OCCH, 078H, 018H, 00CH, 078H ;	D_80
	0C 78	274			
0408	00 00 00 CC CC CC	275	DB	000H, OCCH, 000H, OCCH, OCCH, OCCH, 07EH, 000H ;	D_81
	7E 00	276			
0410	1C 00 78 CC FC CO	277	DB	01CH, 000H, 078H, OCCH, 0FCH, OCCH, 078H, 000H ;	D_82
	78 00	278			
0418	FE C3 3C 06 3E 66	279	DB	07EH, 0C3H, 03CH, 006H, 03EH, 066H, 03FH, 000H ;	D_83
	3F 00	280			
0420	00 00 78 0C 7C CC	281	DB	OCCH, 000H, 078H, 00CH, 07CH, OCCH, 07EH, 000H ;	D_84
	7E 00	282			
0428	E0 70 78 0C 7C CC	283	DB	0E0H, 000H, 078H, 00CH, 07CH, OCCH, 07EH, 000H ;	D_85
	7E 00	284			
0430	30 30 78 0C 7C CC	285	DB	030H, 030H, 078H, 00CH, 07CH, OCCH, 07EH, 000H ;	D_86
	7E 00	286			
0438	00 00 78 CO CO 78	287	DB	000H, 000H, 078H, 0C0H, OCCH, 078H, 00CH, 038H ;	D_87
	0C 38	288			
0440	7E C3 3C 66 7E 7E	289	DB	07EH, 0C3H, 03CH, 066H, 07EH, 060H, 03CH, 000H ;	D_88
	3C 00	290			
0448	00 00 78 CC FC CO	291	DB	OCCH, 000H, 078H, OCCH, 0FCH, OCCH, 078H, 000H ;	D_89
	78 00	292			
0450	E0 78 78 CC FC CO	293	DB	0E0H, 000H, 078H, OCCH, 0FCH, OCCH, 078H, 000H ;	D_8A
	78 00	294			
0458	CC 00 70 30 30 30	295	DB	OCCH, 000H, 070H, 030H, 030H, 030H, 078H, 000H ;	D_8B
	78 00	296			
0460	7C C6 38 18 18 18	297	DB	07CH, OC6H, 038H, 018H, 018H, 018H, 03CH, 000H ;	D_8C
	3C 00	298			
0468	E0 70 70 30 30 30	299	DB	0E0H, 000H, 070H, 030H, 030H, 030H, 078H, 000H ;	D_8D
	78 00	300			
0470	C6 38 6C 6C FE C6	301	DB	OCCH, 038H, 06CH, OC6H, OFEH, OC6H, OC6H, 000H ;	D_8E
	C6 00	302			
0478	30 30 00 78 CC FC	303	DB	030H, 030H, 000H, 078H, OCCH, 0FCH, OCCH, 000H ;	D_8F
	CC 00	304			
		305			
0480	1C 00 FC 60 78 60	306	DB	01CH, 000H, 0FCH, 060H, 078H, 060H, 0FCH, 000H ;	D_90
	FC 00	307			
0488	00 00 7F 0C 7F CC	308			

0490	7F 00	309	DB	03EH, 06CH, 0CCH, 0FEH, 0CCH, 0CCH, 0CEH, 000H ;	D_92
	3E 6C	310			
	CE 00	311			
0498	78 CC 00 78 CC CC	312	DB	078H, 0CCH, 000H, 078H, 0CCH, 0CCH, 078H, 000H ;	D_93
	78 00	313			
04A0	00 CC 00 78 CC CC	314	DB	000H, 0CCH, 000H, 078H, 0CCH, 0CCH, 078H, 000H ;	D_94
	78 00	315			
04A8	00 E0 00 78 CC CC	316	DB	000H, 0E0H, 000H, 078H, 0CCH, 0CCH, 078H, 000H ;	D_95
	78 00	317			
04B0	78 CC 00 CC CC CC	318	DB	078H, 0CCH, 000H, 0CCH, 0CCH, 0CCH, 07EH, 000H ;	D_96
	7E 00	319			
04B8	00 E0 00 CC CC CC	320	DB	000H, 0E0H, 000H, 0CCH, 0CCH, 0CCH, 07EH, 000H ;	D_97
	7E 00	321			
04C0	00 CC 00 CC CC 7C	322	DB	000H, 0CCH, 000H, 0CCH, 0CCH, 07CH, 00CH, 0F8H ;	D_98
	0C F8	323			
04C8	C3 18 3C 66 66 3C	324	DB	0C3H, 018H, 03CH, 066H, 066H, 03CH, 018H, 000H ;	D_99
	18 00	325			
04D0	CC 00 CC CC CC CC	326	DB	0CCH, 000H, 0CCH, 0CCH, 0CCH, 0CCH, 078H, 000H ;	D_9A
	78 00	327			
04D8	18 18 7E C0 C0 7E	328	DB	018H, 018H, 07EH, 0CCH, 0CCH, 07EH, 018H, 018H ;	D_9B
	18 18	329			
04E0	38 6C 64 F0 60 E6	330	DB	038H, 06CH, 064H, 0F0H, 060H, 0E6H, 0FCH, 000H ;	D_9C
	FC 00	331			
04E8	CC CC 78 FC 30 FC	332	DB	0CCH, 0CCH, 078H, 0FCH, 030H, 0FCH, 030H, 030H ;	D_9D
	30 30	333			
04F0	F8 CC CC FA C6 CF	334	DB	0F8H, 0CCH, 0CCH, 0FAH, 0C6H, 0CFH, 0C6H, 0C7H ;	D_9E
	C6 C7	335			
04F8	0E 18 18 3C 18 18	336	DB	00EH, 018H, 018H, 03CH, 018H, 018H, 008H, 070H ;	D_9F
	08 70	337			
0500	1C 00 78 0C 7C CC	338			
	7E 00	339	DB	01CH, 000H, 078H, 00CH, 07CH, 0CCH, 07EH, 000H ;	D_A0
0508	38 00 70 30 30 30	340			
	78 00	341	DB	038H, 000H, 070H, 030H, 030H, 030H, 078H, 000H ;	D_A1
0510	00 1C 00 78 CC CC	342	DB	000H, 01CH, 000H, 078H, 0CCH, 0CCH, 078H, 000H ;	D_A2
	78 00	343			
0518	00 1C 00 CC CC CC	344	DB	000H, 01CH, 000H, 0CCH, 0CCH, 0CCH, 07EH, 000H ;	D_A3
	7E 00	345			
0520	00 F8 00 F8 CC CC	346	DB	000H, 0F8H, 000H, 0F8H, 0CCH, 0CCH, 0CCH, 000H ;	D_A4
	CC 00	347			
0528	FC 00 CC EC FC DC	348	DB	0FCH, 000H, 0CCH, 0ECH, 0FCH, 0DCH, 0CCH, 000H ;	D_A5
	CC 00	349			
0530	3C 6C 6C 3E 00 7E	350	DB	03CH, 06CH, 06CH, 03EH, 000H, 0CCH, 07EH, 000H, 000H ;	D_A6
	00 00	351			
0538	38 6C 6C 38 00 7C	352	DB	038H, 06CH, 06CH, 038H, 000H, 07CH, 000H, 000H ;	D_A7
	00 00	353			
0540	30 00 30 60 C0 CC	354	DB	030H, 000H, 030H, 060H, 0CCH, 0CCH, 078H, 000H ;	D_A8
	78 00	355			
0548	00 00 00 FC C0 C0	356	DB	000H, 000H, 000H, 0FCH, 0CCH, 0CCH, 000H, 000H ;	D_A9
	00 00	357			
0550	00 00 00 FC 0C 0C	358	DB	000H, 000H, 000H, 0FCH, 0CCH, 0CCH, 000H, 000H ;	D_AA
	00 00	359			
0558	C3 C6 CC DE 33 66	360	DB	0C3H, 0C6H, 0CCH, 0DEH, 033H, 066H, 0CCH, 00FH ;	D_AB
	CC 0F	361			
0560	C3 C6 CC DB 37 6F	362	DB	0C3H, 0C6H, 0CCH, 0DBH, 037H, 06FH, 0CFH, 003H ;	D_AC
	CF 03	363			
0568	18 18 00 18 18 18	364	DB	018H, 018H, 000H, 018H, 018H, 018H, 018H, 000H ;	D_AD
	18 00	365			
0570	00 33 66 CC 66 33	366	DB	000H, 033H, 066H, 0CCH, 066H, 033H, 000H, 000H ;	D_AE
	00 00	367			
0578	00 CC 66 33 66 CC	368	DB	000H, 0CCH, 066H, 033H, 066H, 0CCH, 000H, 000H ;	D_AF
	00 00	369			
		370			
0580	22 88 22 88 22 88	371	DB	022H, 088H, 022H, 088H, 022H, 088H ;	D_B0
	22 88	372			
0588	55 AA 55 AA 55 AA	373	DB	055H, 0AAH, 055H, 0AAH, 055H, 0AAH, 055H, 0AAH ;	D_B1
	55 AA	374			
0590	DB 77 DB EE DB 77	375	DB	0DBH, 077H, 0DBH, 0EEH, 0DBH, 077H, 0DBH, 0EEH ;	D_B2
	DB EE	376			
0598	18 18 18 18 18 18	377	DB	018H, 018H, 018H, 018H, 018H, 018H, 018H, 018H ;	D_B3
	18 18	378			
05A0	18 18 18 18 F8 18	379	DB	018H, 018H, 018H, 018H, 0F8H, 018H, 018H, 018H ;	D_B4
	18 18	380			
05A8	18 18 F8 18 F8 18	381	DB	018H, 018H, 0F8H, 018H, 0F8H, 018H, 018H, 018H ;	D_B5
	18 18	382			
05B0	36 36 36 36 F6 36	383	DB	036H, 036H, 036H, 036H, 0F6H, 036H, 036H, 036H ;	D_B6
	36 36	384			
05B8	00 00 00 00 FE 36	385	DB	000H, 000H, 000H, 000H, 0FEH, 036H, 036H, 036H ;	D_B7
	36 36	386			
05C0	00 00 F8 18 F8 18	387	DB	000H, 000H, 0F8H, 018H, 0F8H, 018H, 018H, 018H ;	D_B8
	18 18	388			
05C8	36 36 F6 06 F6 36	389	DB	036H, 036H, 0F6H, 006H, 0F6H, 036H, 036H, 036H ;	D_B9
	36 36	390			
05D0	36 36 36 36 36 36	391	DB	036H, 036H, 036H, 036H, 036H, 036H, 036H, 036H ;	D_BA
	36 36	392			
05D8	00 00 FE 06 F6 36	393	DB	000H, 000H, 0FEH, 006H, 0F6H, 036H, 036H, 036H ;	D_BB
	36 36	394			
05E0	36 36 F6 06 FE 00	395	DB	036H, 036H, 0F6H, 006H, 0FEH, 000H, 000H, 000H ;	D_BC
	00 00	396			
05E8	36 36 36 36 FE 00	397	DB	036H, 036H, 036H, 036H, 0FEH, 000H, 000H, 000H ;	D_BD
	00 00	398			
05F0	18 18 F8 18 F8 00	399	DB	018H, 018H, 0F8H, 018H, 0F8H, 000H, 000H, 000H ;	D_BE
	00 00	400			
05F8	00 00 00 F8 18 18	401	DB	000H, 000H, 000H, 000H, 0F8H, 018H, 018H, 018H ;	D_BF
	18 00	402			
		403			
0600	18 18 18 18 FF 00	404	DB	018H, 018H, 018H, 018H, 01FH, 000H, 000H, 000H ;	D_C0
	00 00	405			
0608	18 18 18 18 FF 00	406	DB	018H, 018H, 018H, 018H, 0FFH, 000H, 000H, 000H ;	D_C1
	00 00	407			
0610	00 00 00 00 FF 18	408	DB	000H, 000H, 000H, 000H, 0FFH, 018H, 018H, 018H ;	D_C2
	18 18	409			
0618	18 18 18 18 1F 18	410	DB	018H, 018H, 018H, 018H, 01FH, 018H, 018H, 018H ;	D_C3
	18 18	411			
0620	00 00 00 00 FF 00	412	DB	000H, 000H, 000H, 000H, 0FFH, 000H, 000H, 000H ;	D_C4
	00 00	413			
0628	18 18 18 18 FF 18	414	DB	018H, 018H, 018H, 018H, 0FFH, 018H, 018H, 018H ;	D_C5
	18 18	415			
0630	18 18 1F 18 1F 18	416	DB	018H, 018H, 01FH, 018H, 01FH, 018H, 018H, 018H ;	D_C6
	18 18	417			
0638	36 36 36 36 F7 36	418	DB	036H, 036H, 036H, 036H, 037H, 036H, 036H, 036H ;	D_C7
	36 36	419			
0640	36 36 37 30 3F 00	420	DB	036H, 036H, 037H, 030H, 03FH, 000H, 000H, 000H ;	D_C8
	00 00	421			
0648	00 00 3F 30 37 36	422	DB	000H, 000H, 03FH, 030H, 037H, 036H, 036H, 036H ;	D_C9
	36 36	423			
0650	36 36 F7 00 FF 00	424	DB	036H, 036H, 0F7H, 000H, 0FFH, 000H, 000H, 000H ;	D_CA
	00 00	425			
0658	00 00 FF 00 F7 36	426	DB	000H, 000H, 0FFH, 000H, 0F7H, 036H, 036H, 036H ;	D_CB
	36 36	427			
0660	36 36 37 30 37 36	428	DB	036H, 036H, 037H, 030H, 037H, 036H, 036H, 036H ;	D_CC
	36 36	429			
0668	00 00 FF 00 FF 00	430	DB	000H, 000H, 0FFH, 000H, 0FFH, 000H, 000H, 000H ;	D_CD
	00 00	431			
0670	36 36 F7 00 F7 36	432	DB	036H, 036H, 0F7H, 000H, 0F7H, 036H, 036H, 036H ;	D_CE
	36 36	433			
		434			

0678	18 18 FF 00 FF 00	435	DB	018H,018H,0FFH,000H,0FFH,000H,000H,000H ;	D_CF
	00 00	436			
0680	36 36 36 36 FF 00	437	DB	036H,036H,036H,036H,0FFH,000H,000H,000H ;	D_D0
	00 00	438			
0688	00 00 FF 00 FF 18	439	DB	000H,000H,0FFH,000H,0FFH,018H,018H,018H ;	D_D1
	18 18	440			
0690	00 00 00 00 FF 36	441	DB	000H,000H,000H,000H,0FFH,036H,036H,036H ;	D_D2
	36 36	442			
0698	36 36 36 36 3F 00	443	DB	036H,036H,036H,036H,03FH,000H,000H,000H ;	D_D3
	00 00	444			
06A0	18 18 1F 18 1F 00	445	DB	018H,018H,01FH,018H,01FH,000H,000H,000H ;	D_D4
	00 00	446			
06A8	00 00 1F 18 1F 18	447	DB	000H,000H,01FH,018H,01FH,018H,018H,018H ;	D_D5
	18 18	448			
06B0	00 00 00 00 3F 36	449	DB	000H,000H,000H,000H,03FH,036H,036H,036H ;	D_D6
	36 36	450			
06B8	36 36 36 36 FF 36	451	DB	036H,036H,036H,036H,0FFH,036H,036H,036H ;	D_D7
	36 36	452			
06C0	18 18 FF 18 FF 18	453	DB	018H,018H,0FFH,018H,0FFH,018H,018H,018H ;	D_D8
	18 18	454			
06C8	18 18 18 18 F8 00	455	DB	018H,018H,018H,018H,0F8H,000H,000H,000H ;	D_D9
	00 00	456			
06D0	00 00 00 00 1F 18	457	DB	000H,000H,000H,000H,01FH,018H,018H,018H ;	D_DA
	18 18	458			
06D8	FF FF FF FF FF FF	459	DB	0FFH,0FFH,0FFH,0FFH,0FFH,0FFH,0FFH,0FFH ;	D_DB
	FF FF	460			
06E0	00 00 00 00 FF FF	461	DB	000H,000H,000H,000H,0FFH,0FFH,0FFH,0FFH ;	D_DC
	FF FF	462			
06E8	F0 F0 F0 F0 F0 F0	463	DB	0F0H,0F0H,0F0H,0F0H,0F0H,0F0H,0F0H,0F0H ;	D_DD
	F0 F0	464			
06F0	0F 0F 0F 0F 0F 0F	465	DB	00FH,00FH,00FH,00FH,00FH,00FH,00FH,00FH ;	D_DE
	0F 0F	466			
06F8	FF FF FF FF FF FF	467	DB	0FFH,0FFH,0FFH,0FFH,000H,000H,000H,000H ;	D_DF
	00 00	468			
		469			
0700	00 00 76 DC C8 DC	470	DB	000H,000H,076H,0DCH,0C8H,0DCH,076H,000H ;	D_E0
	76 00	471			
0708	00 78 C8 FC CC F8	472	DB	000H,078H,0CCH,0F8H,0CCH,0F8H,0CCH,0CCH ;	D_E1
	C0 C0	473			
0710	00 FC CC C0 C0 C0	474	DB	000H,0FCH,0CCH,0CCH,0CCH,0CCH,0CCH,000H ;	D_E2
	C0 00	475			
0718	00 FE 6C 6C 6C 6C	476	DB	000H,0FEH,06CH,06CH,06CH,06CH,06CH,000H ;	D_E3
	6C 00	477			
0720	FC CC 60 30 60 CC	478	DB	0FCH,0CCH,060H,030H,060H,0CCH,0FCH,000H ;	D_E4
	FC 00	479			
0728	00 00 7E DB D8 DB	480	DB	000H,000H,07EH,0DBH,0DBH,0DBH,070H,000H ;	D_E5
	70 00	481			
0730	00 66 66 66 66 7C	482	DB	000H,066H,066H,066H,066H,07CH,060H,0CCH ;	D_E6
	60 C0	483			
0738	00 76 DC 18 18 18	484	DB	000H,076H,0DCH,018H,018H,018H,018H,000H ;	D_E7
	18 00	485			
0740	FC 30 78 CC CC 78	486	DB	0FCH,030H,078H,0CCH,0CCH,078H,030H,0FCH ;	D_E8
	30 FC	487			
0748	38 6C C6 FE 6C 6C	488	DB	038H,06CH,0C6H,0FEH,0C6H,06CH,038H,000H ;	D_E9
	38 00	489			
0750	38 6C C6 C6 6C 6C	490	DB	038H,06CH,0C6H,0C6H,06CH,06CH,0EEH,000H ;	D_EA
	EE 00	491			
0758	1C 30 18 7C CC CC	492	DB	01CH,030H,018H,07CH,0CCH,0CCH,078H,000H ;	D_EB
	78 00	493			
0760	00 0E 7E DB DB 7E	494	DB	000H,000H,07EH,0DBH,0DBH,07EH,000H,000H ;	D_EC
	00 00	495			
0768	06 0C 7E DB DB 7E	496	DB	006H,00CH,07EH,0DBH,0DBH,07EH,060H,0CCH ;	D_ED
	60 C0	497			
0770	38 60 C0 F8 C0 60	498	DB	038H,060H,0CCH,0F8H,0CCH,060H,038H,000H ;	D_EE
	38 00	499			
0778	78 CC CC CC CC CC	500	DB	078H,0CCH,0CCH,0CCH,0CCH,0CCH,0CCH,000H ;	D_EF
	CC 00	501			
		502			
0780	00 FC 00 FC 00 FC	503	DB	000H,0FCH,000H,0FCH,000H,0FCH,000H,000H ;	D_F0
	00 00	504			
0788	30 30 FC 30 30 00	505	DB	030H,030H,0FCH,030H,030H,000H,0FCH,000H ;	D_F1
	FC 00	506			
0790	60 30 18 30 60 00	507	DB	060H,030H,018H,030H,060H,000H,0FCH,000H ;	D_F2
	FC 00	508			
0798	18 30 60 30 18 00	509	DB	018H,030H,060H,030H,018H,000H,0FCH,000H ;	D_F3
	FC 00	510			
07A0	0E 18 18 18 18 18	511	DB	00EH,018H,018H,018H,018H,018H,018H,018H ;	D_F4
	18 18	512			
07A8	18 18 18 18 18 DB	513	DB	018H,018H,018H,018H,018H,0DBH,0DBH,070H ;	D_F5
	DB 70	514			
07B0	30 30 00 FC 00 30	515	DB	030H,030H,000H,0FCH,000H,030H,030H,000H ;	D_F6
	30 00	516			
07B8	00 76 DC 00 76 DC	517	DB	000H,076H,0DCH,000H,076H,0DCH,000H,000H ;	D_F7
	00 00	518			
07C0	38 6C 6C 38 00 00	519	DB	038H,06CH,06CH,038H,000H,000H,000H,000H ;	D_F8
	00 00	520			
07C8	00 00 00 18 18 00	521	DB	000H,000H,000H,018H,018H,000H,000H,000H ;	D_F9
	00 00	522			
07D0	00 00 00 00 18 00	523	DB	000H,000H,000H,000H,018H,000H,000H,000H ;	D_FA
	00 00	524			
07D8	0F 0C 0C 0C EC 6C	525	DB	00FH,00CH,00CH,00CH,0ECH,06CH,03CH,01CH ;	D_FB
	3C 1C	526			
07E0	78 6C 6C 6C 6C 00	527	DB	078H,06CH,06CH,06CH,06CH,000H,000H,000H ;	D_FC
	00 00	528			
07E8	70 18 30 60 78 00	529	DB	070H,018H,030H,060H,078H,000H,000H,000H ;	D_FD
	00 00	530			
07F0	00 00 3C 3C 3C 3C	531	DB	000H,000H,03CH,03CH,03CH,03CH,000H,000H ;	D_FE
	00 00	532			
07F8	00 00 00 00 00 00	533	DB	000H,000H,000H,000H,000H,000H,000H,000H ;	D_FF
	00 00	534			
		535			
0800		536			
		537			

CODE  
ENDS  
END

0000	1	PAGE,120
0000	2	SUBTTL END ADDRESS
0000	3	SEGMENT PUBLIC
0000	4	END ADDRESS
0000	5	LABEL BYTE
0000	6	ENDS
	7	END

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